VS-U5FX120FA120

Vishay Semiconductors





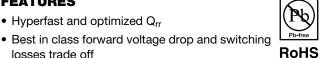
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PRIMARY CHARACTERISTICS									
V _R	1200 V								
V _F (typical) at 60 A, per diode	2.65 V								
t _{rr} (typical) at 60 A, per diode	46 ns								
$I_{F(DC)}$ per module at $T_C = 70 \ ^{\circ}C$	120 A								
Туре	Modules - diode, FRED Pt®								
Package	SOT-227								
Circuit configuration	Two separate diodes, parallel pin-out								

FEATURES

losses trade off

Hyperfast and optimized Q_{rr}



COMPLIANT

- · Optimized for high speed operation
- 175 °C maximum operating junction temperature
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and gualified for industrial level
- UL approved file E78996
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, the VS-U5FX120FA120 is the right choice for high frequency converters, both soft switched / resonant. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters, and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Cathode to anode voltage	V _R		1200	V			
Continuous forward current per diode	I _F	T _C = 70 °C	60	^			
Single pulse forward current per diode	I _{FSM}	T _J = 25 °C	290	A			
Maximum power dissipation per module	PD	T _C = 70 °C	304	W			
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V			
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	1200	-	-		
Forward voltage	V _{FM}	I _F = 60 A	-	2.75	3.52	V	
		I _F = 60 A, T _J = 150 °C	-	2.15	-		
		V _R = 1200 V	-	0.6	80		
Reverse leakage current	I _{RM}	T _J = 125 °C, V _R = 1200 V	-	62	-	μA	
		T _J = 150 °C, V _R = 1200 V	-	220	-		

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Powerse receivery time	+	$T_J = 25 \ ^\circ C$		-	46	-	ns A	
Reverse recovery time	t _{rr}	T _J = 125 °C	I _F = 60 A, di _F /dt = 1000 A/μs, V _R = 800 V	-	91	-		
Poole recovery ourrent		T _J = 25 °C		-	24	-		
Peak recovery current	I _{RRM}	T _J = 125 °C		-	42	-		
Reverse recovery charge	0	T _J = 25 °C		-	1.2	-	μC	
Reverse recovery charge Q _{rr}		T _J = 125 °C		-	3.5	-	μΟ	
Junction capacitance	CT	V _R = 1200 V		-	23	-	pF	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance junction to case, per diode			-	-	0.69		
Thermal resistance junction to case, per module	R _{thJC}		-	-	0.345	°C/W	
Thermal resistance case to heatsink, per module	R _{thCS}	Flat, greased surface	-	0.05	-		
Weight			-	30	-	g	
Mounting torque		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)	
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)	
Case style				SO	Г-227		

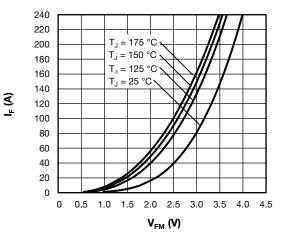


Fig. 1 - Typical Forward Voltage Drop Characteristics

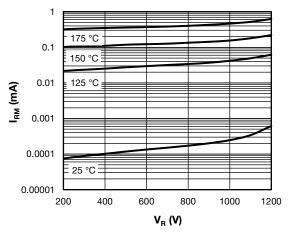


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



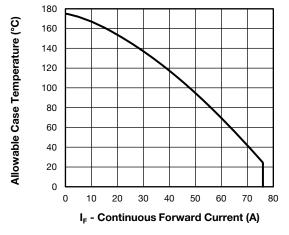


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

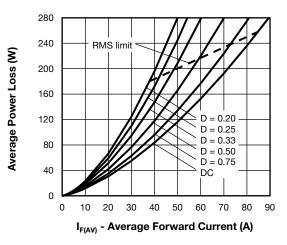


Fig. 4 - Average Power Loss vs. Average Forward Current

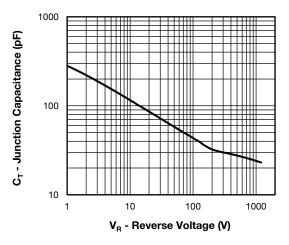


Fig. 5 - Junction Capacitance vs. Reverse Voltage

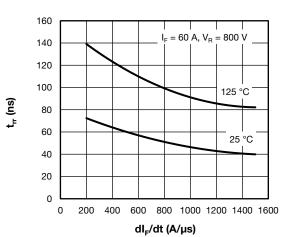


Fig. 6 - Diode Reverse Recovery Time vs. dl_F/dt

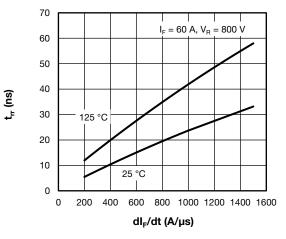


Fig. 7 - Diode Reverse Recovery Current vs. dl_F/dt

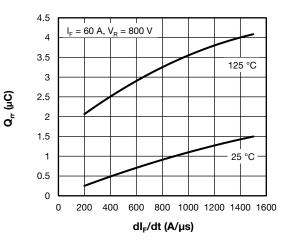


Fig. 8 - Diode Reverse Recovery Charge vs. dl_F/dt

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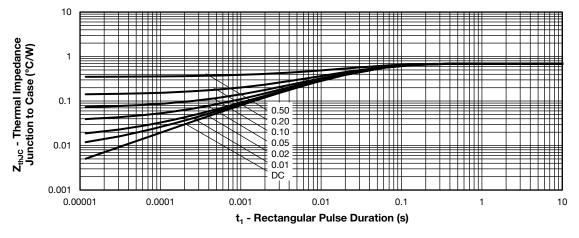


Fig. 9 - Maximum Thermal Impedance Junction to Case

ORDERING INFORMATION TABLE

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ISHAY

Device code	VS-	U5F	x	120	F	Α	120	
	1	2	3	4	5	6	7	
	1 - 2 - 3 - 4 - 5 - 6 - 7 -	U5F X = Cur F = Pac	F = Gen Hyperfa rent rati circuit c kage in	0	Pt [®] far D Pt [®] di nodule ation (tv SOT-22	nily ode (120 = ⁻ vo sepa 27 stanc	rate dio	odes, parallel pin-out) ulated base)

CIRCUIT CONFI	GURATION	
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two separate diodes, parallel pin-out	F	Lead Assignment

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95423</u>						
Packaging information	www.vishay.com/doc?95425					

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