Vishay Semiconductors

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Ultrafast Rectifier, 8 A FRED Pt[®]



TO-220 FullPAK 2L

PRIMARY CHARACTERISTICS						
I _{F(AV)}	8 A					
V _R	500 V					
V _F at I _F	0.9 V					
t _{rr} (typ.)	28 ns					
T _J max.	175 °C					
Package	TO-220 FullPAK 2L					
Circuit configuration	Single					

FEATURES

- Low forward voltage drop
- Ultrafast soft recovery time
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package ($V_{INS} = 2500 V_{RMS}$)
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

Ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V _{RRM}		500	V			
Average rectified forward current in DC	I _{F(AV)}	T _C = 124 °C	8	٨			
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	110	A			
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	500	-	-		
Ferrieral colleges	V _F	I _F = 8 A	-	1.05	1.25	V	
Forward voltage V _F		I _F = 8 A, T _J = 150 °C	-	0.9	1.03		
		V _R = V _R rated	-	0.005	9		
Reverse leakage current I _R		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	5	50	μA	
Junction capacitance	CT	V _R = 500 V	-	6	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH	

Pb-free RoHS COMPLIANT HALOGEN

FREE

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 10$	00 A/µs, V _R = 30 V	-	28	-		
	+	$I_F = 8 \text{ A}, \text{ d}_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	54	-	ns	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	50	-	115	
		T _J = 125 °C		-	90	-		
Book receiver autrent		T _J = 25 °C	$I_F = 8 A,$ $dI_F/dt = 200 A/\mu s,$ $V_R = 200 V,$	-	7.0	-	А	
Peak recovery current	I _{RRM}	T _J = 125 °C		-	10	-	~	
Reverse recovery charge	0	T _J = 25 °C		-	180	-	nC	
	Q _{rr}	T _J = 125 °C		-	450	-		

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C		
Thermal resistance, junction-to-case	R _{thJC}		-	4.4	5.5			
Thermal resistance, junction-to-ambient	R _{thJA}	Typical socket mount	-	-	50	°C/W		
Typical thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-			
Weight			-	2.0	-	g		
weight			-	0.007	-	oz.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Marking device		Case style TO-220 FullPAK 2L	ETU0805FP					

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 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
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VS-ETU0805FP-M3

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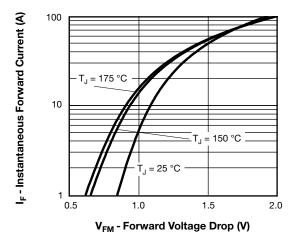
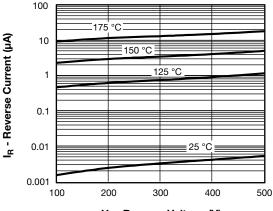
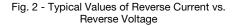


Fig. 1 - Typical Forward Voltage Drop Characteristics



V_R - Reverse Voltage (V)



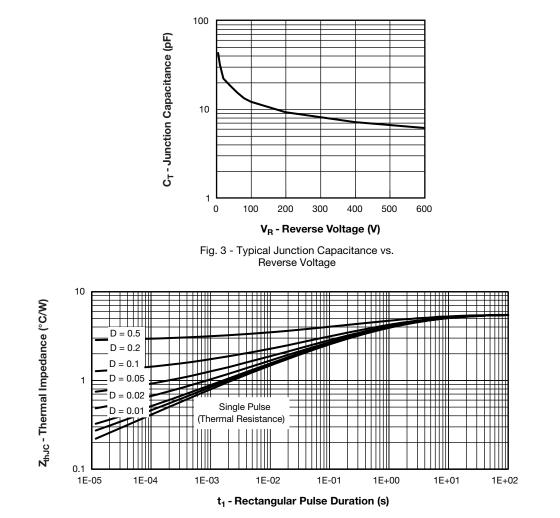


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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Allowable Case Temperature (°C)

Average Power Loss (W)

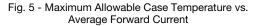
60

0

2 4 6 8 10 I_{F(AV)} - Average Forward Current (A)

DC

12



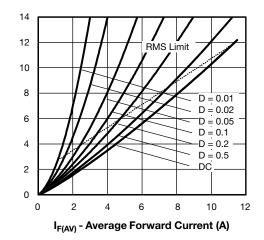


Fig. 6 - Forward Power Loss Characteristics

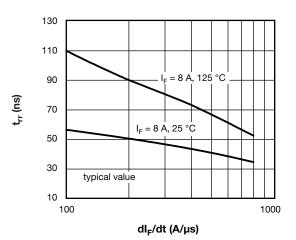


Fig. 7 - Typical Reverse Recovery vs. dl_F/dt

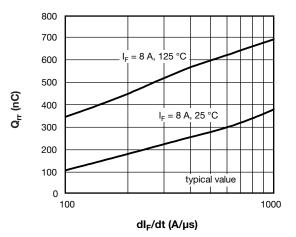
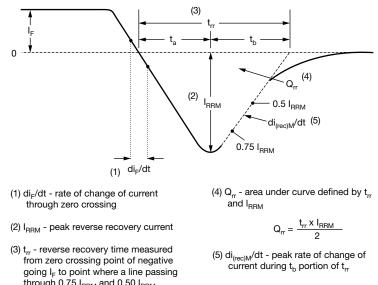


Fig. 8 - Typical Stored Charge vs. dl_F/dt



	 di_F/dt - rate of change of current through zero crossing 	(4) ${\rm Q}_{\rm rr}$ - area under curve defined by ${\rm t}_{\rm rr}$ and ${\rm I}_{\rm RRM}$	
	(2) I_{RRM} - peak reverse recovery current	$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$	
	(3) t _{rr} - reverse recovery time measured from zero crossing point of negative going I _F to point where a line passing through 0.75 I _{RRM} and 0.50 I _{RRM} extrapolated to zero current.	(5) di _{(rec)M} /dt - peak rate of change of current during t _b portion of t _{rr}	
	Fig. 9 - Reverse Recovery V	Waveform and Definitions	
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ORDERING INFORMATION TABLE

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Device code	VS-	Е	т	U	08	05	FP	-M3
			•				••	
		2	3	4	5	6	(7)	8
	1 -	- Visł	nay Sem	nicondu	ctors pr	oduct		
	2 -	- Circ	cuit con	figuratio	n:			
		E =	single					
	3 -	• T =	TO-220)				
	4	• U =	hyperfa	ast reco	very tim	е		
	5	- Cur	rent coo	de: 08 =	8 A			
	6	- Volt	age co	de: 05 =	500 V			
	7 -	· FP :	= TO-22	0 FullPA	AK 2L			
	8 -	- Env	ironmer	ntal digit	:			
		-M3	3 = halo	gen-free	, RoHS	compli	ant, and	d termin

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-ETU0805FP-M3	50	1000	Antistatic plastic tube				

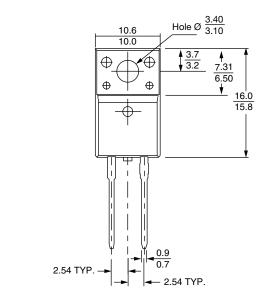
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96157					
Part marking information	www.vishay.com/doc?95392					

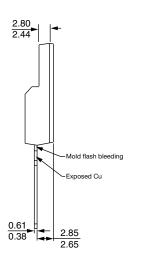


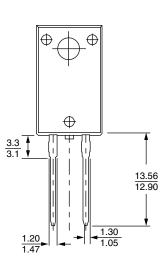
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2L TO-220 FullPAK

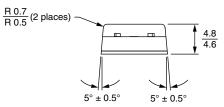
DIMENSIONS in millimeters







Bottom view





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