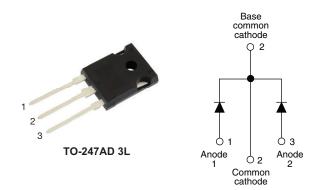
VS-C4PH3006LHN3

Vishay Semiconductors



Hyperfast Soft Recovery Diode, 2 x 15 A FRED Pt[®] Gen 4



PRODUCT SUMMARY						
Package	TO-247AD 3L					
I _{F(AV)}	2 x 15 A					
V _R	600 V					
V _F at I _F	1.28 V					
t _{rr} typ.	See Recovery table					
T _J max.	175 °C					
Diode variation	Common cathode					

FEATURES

- Gen 4 FRED Pt[®] technology
- \bullet Low I_{RRM} and reverse recovery charge
- Very low forward voltage drop
- Polyimide passivated chip for high reliability standard
- 175 °C operating junction temperature
- AEC-Q101 qualified, meets JESD 201 class 1
 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

Gen 4 Fred Pt technology, state of the art, ultralow V_F, soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Peak repetitive reverse voltage	V _{RRM}		600	V			
Average rectified forward current	I _{F(AV)}	T _C = 142 °C	15	^			
Non-repetitive peak surge current, per leg	I _{FSM}	T_C = 25 °C, t_p = 8.3 ms half sine wave	200	A			
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS (T_J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	600	-	-		
		I _F = 15 A	-	1.6	1.9		
Forward voltage	V _F	I _F = 30 A	-	1.87	-	v	
		I _F = 15 A, T _J = 125 °C	-	1.35	-		
		I _F = 30 A, T _J = 125 °C	-	1.67	-		
		I _F = 15 A, T _J = 150 °C	-	1.28	1.52		
		I _F = 30 A, T _J = 150 °C	-	1.61	-		
Bayaraa laakaga ayrrant	1-	$V_{R} = V_{R}$ rated	-	-	15		
Reverse leakage current	I _R	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA	
Junction capacitance	CT	V _R = 600 V	-	16	-	pF	



RoHS COMPLIANT

FREE

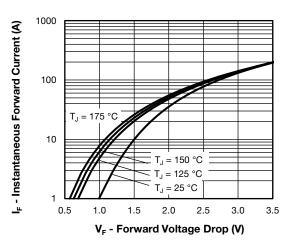
Revision: 21-Feb-17 1 Document Number: 95953 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



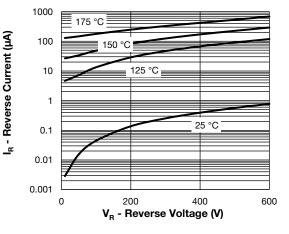
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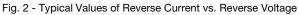
DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS		
Boverne receiver time	+	T _J = 25 °C		-	50	-	20	
Reverse recovery time	t _{rr}	T _J = 125 °C	1 15 4	-	70	-	ns	
Peak recovery current	I	T _J = 25 °C	l _F = 15 A dl _F /dt = 1000 A/µs	-	8.5	-	А	
Feak recovery current	IRRM	T _J = 125 °C	$V_{\rm R} = 400 \text{ V}$	-	16	-	~	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	250	-	nC	
neverse recovery charge	Qrr	T _J = 125 °C		-	600	-		

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Thermal resistance, junction to case	R _{thJC}		-	-	1.4	°C/W		
Thermal resistance, case to heat sink	R _{thCS}		-	0.4	-			
Weight			-	6.0	-	g		
Weight			-	0.21	-	oz.		
Mounting torque			6.0	_	12	kgf · cm		
			(5)	-	(10)	(lbf · in)		
Marking device		Case style TO-247AD 3L	C4PH3006LH					









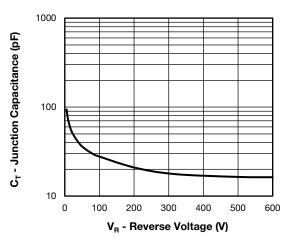
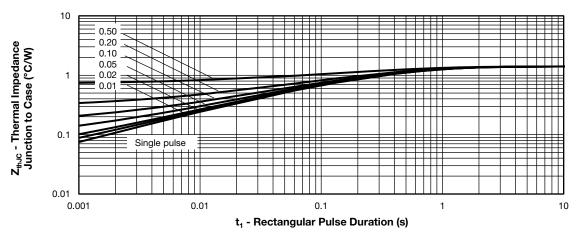


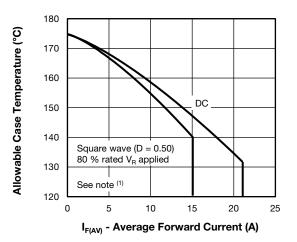
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

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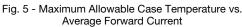
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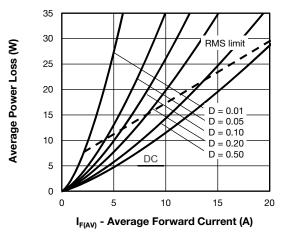






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Note

⁽¹⁾ Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC};$ $P_d = \text{forward power loss} = I_{CAVA} \times V_{CAVA} \text{ at } (I_{CAVA}/D) \text{ (see$

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{Fig. 5}) \\ \mathsf{P}_{\mathsf{dREV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R}1} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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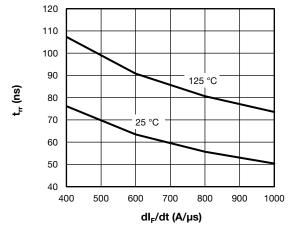


Fig. 7 - Typical Reverse Recovery Time vs. $dI_{\mbox{\scriptsize F}}/dt$

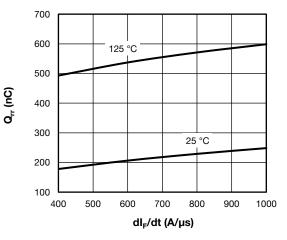


Fig. 8 - Typical Stored Charge vs. dl_F/dt

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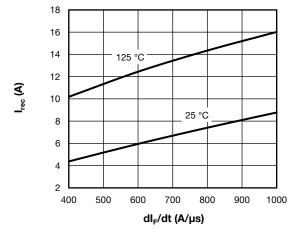


Fig. 9 - Typical Reverse Current vs. dl_F/dt

ORDERING INFORMATION TABLE

Device code	VS-	С	4	Р	н	30	06	L	Н	N3
	1	2	3	4	5	6	7	8	9	10
	1 -		•	niconduc	•	oduct				
	2 - Circuit configuration:									
	C = common cathode									
	3 - FRED Pt Gen 4									
	4 -	P =	TO-247	' packag	e					
	5 -	Pro	cess typ	be:						
		H =	hyperfa	st recov	/ery					
	6 -	Cur	rent rati	ng (30 =	2 x 15	A)				
	7 -	Volt	age rati	ng (06 =	= 600 V)					
	8 -	Pac	kage: L	= long l	ead					
	9 -	H =	AEC-Q	101 qua	lified					
	10 -	Env	ironmer	ntal digit	:					
		N3 =	= haloge	en-free,	RoHS-c	ompliar	nt, and t	otally le	ad (Pb)	-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-C4PH3006LHN3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626			
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007			

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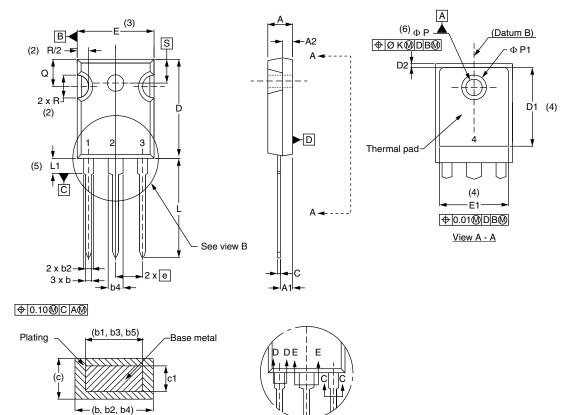
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Vishay Semiconductors

TO-247AD 3L

DIMENSIONS in millimeters and inches



View B

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
с	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

(2, 52, 51) (4) Section C - C, D - D, E - E

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	5 BSC	
ØК	0.2	254	0.0	010	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØР	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	' BSC	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

- ⁽³⁾ Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- ⁽⁵⁾ Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- ⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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 1
 Document Number: 95626

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