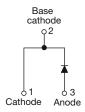


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Vishay Semiconductors

Ultralow V_F Hyperfast Rectifier for Discontinuous Mode PFC, 8 A FRED Pt[®]





PRIMARY CHARACTERISTICS									
I _{F(AV)}	8 A								
V _R	600 V								
V _F at I _F	0.81 V								
t _{rr} typ.	60 ns								
T _J max.	175 °C								
Package	TO-220AC 2L								
Circuit configuration	Single								

FEATURES

- · Hyperfast recovery time
- Benchmark ultralow forward voltage drop
- 175 °C operating junction temperature
- Low leakage current

COMPLIANT HALOGEN FREE

- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

State of the art, ultralow V_F , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Repetitive peak reverse voltage	V_{RRM}		600	V						
Average rectified forward current	I _{F(AV)}	T _C = 160 °C	8	Α						
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	175							
Repetitive peak forward current	I _{FM}		16							
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C						

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)										
PARAMETER	ARAMETER SYMBOL TEST CONDITIONS									
Breakdown voltage, blocking voltage	V _{BR} , V _R	-·· I _D = 1()() A		-	-					
Forward voltage	V _F	I _F = 8 A	-	0.96	1.05	V				
i orward voltage		I _F = 8 A, T _J = 150 °C	-	0.81	0.86					
Doverno lookago ourrent		$V_R = V_R$ rated	-	0.05	5					
Reverse leakage current	I _R	T _J = 150 °C, V _R = V _R rated	-	20	100	μA				
Junction capacitance	C _T	V _R = 600 V	-	17	-	pF				
Series inductance	L _S	Measured lead to lead 5 mm from package body	1	8.0	-	nΗ				



DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A}$	A/μs, V _R = 30 V	-	60	100				
	t _{rr}	$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A}$		150	250					
		T _J = 25 °C		-	170	-	ns - A - μC			
		T _J = 125 °C	I _F = 8 A	-	250	-				
Dools we account assument	I _{RRM}	T _J = 25 °C		-	15	-				
Peak recovery current		T _J = 125 °C	dl _F /dt = 200 A/µs V _R = 390 V	-	20	-				
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	1.3	-				
		T _J = 125 °C		-	2.6	-				

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL TEST CONDITIONS			TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C				
Thermal resistance, junction-to-case	R_{thJC}		-	1.4	2	°C/W				
Thermal resistance, junction-to-ambient per leg	R _{thJA} Typical socket mount		-	-	70					
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-					
Weight			-	2.0	-	g				
Weight			-	0.07	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style TO-220AC 2L	8ETL06							

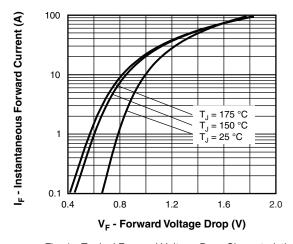


Fig. 1 - Typical Forward Voltage Drop Characteristics

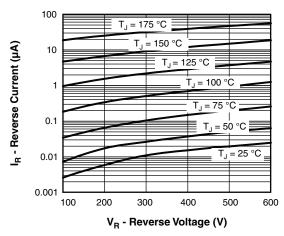


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

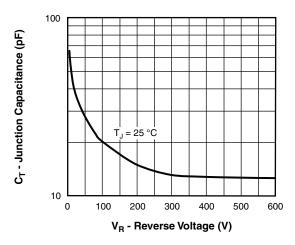


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

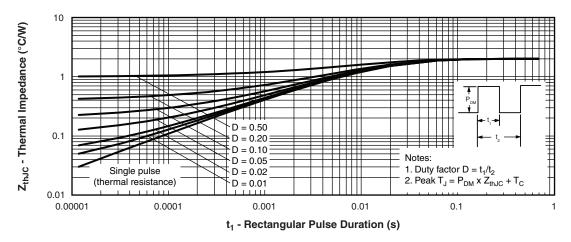


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

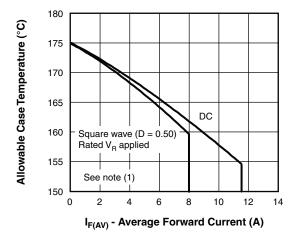


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

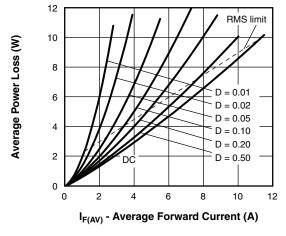


Fig. 6 - Forward Power Loss Characteristics

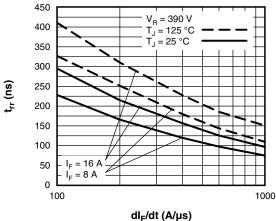
Note

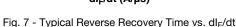
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 5); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R



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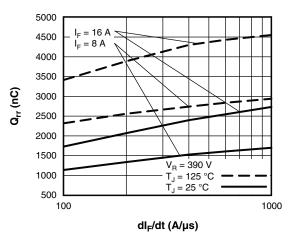
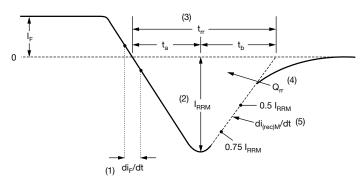


Fig. 8 - Typical Stored Charge vs. dl_E/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

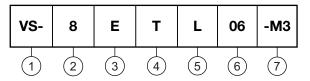
(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (8 = 8 A)
- **3** E = single diode
- T = TO-220, D²PAK (TO-263AB)
- 5 L = ultralow V_F hyperfast recovery
- 6 Voltage rating (06 = 600 V)
- 7 Environmental digit:
 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

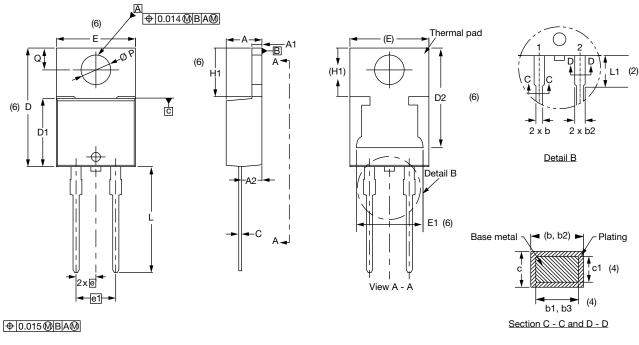
ORDERING INFORMATION (Example)									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-8ETL06-M3	50	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?96156						
Part marking information	www.vishay.com/doc?95391						
SPICE model	www.vishay.com/doc?96053						



TO-220AC 2L

DIMENSIONS in millimeters and inches



Lead tip

Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIN	IETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355							•	

Notes

- $^{(1)}$ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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