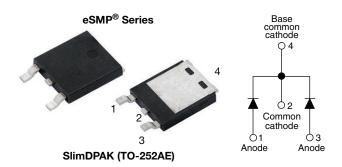
Vishay Semiconductors

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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 3 A			
V _R	200 V			
V _F at I _F	0.75 V			
t _{rr} (typ.)	20 ns			
T _J max.	175 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration	Common cathode			

FEATURES

- Hyperfast recovery time
- Low forward voltage drop reduced Q_{rr} and soft recovery
- Low leakage current
- Very low profile typical height of 1.3 mm
- 175 °C operating junction temperature
- Ideal for automated placement
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- · Polyimide passivation for high reliability standard
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^{\circ}\mathrm{C}$
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyper fast recovery rectifiers designed with optimized performance of forward voltage drop and hyper fast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage		V _{RRM}		200	V
Average rectified forward current	per leg	1	Total device, rated V_R , T_C = 166 °C	3	
Average rectilied forward current	per device	IF(AV)		6	А
Non-repetitive peak surge current	per leg	I _{FSM}	$T_J = 25 \ ^{\circ}C$, 10 ms sine pulse wave	70	
Operating junction and storage ten	nperatures	TJ, T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	200	-	-	
	V _F	I _F = 3 A	-	0.9	1.04	V
Forward voltage		I _F = 3 A, T _J = 150 °C	-	0.75	0.82	
		I _F = 6 A	-	1	1.2	
		I _F = 6 A, T _J = 150 °C	-	0.85	1.01	
Reverse leakage current	I _R	$V_{R} = V_{R}$ rated	-	-	5	μA
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	80	μΑ
Junction capacitance	CT	V _R = 200 V	-	12	-	pF

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1

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RoHS COMPLIANT HALOGEN



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS MIN			MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	20	-	
Boyeres resources time	+	I _F = 0.5 A, I _R = 1 A, I _{RR} = 0.25 A		-	-	25	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	17	-	ns
		T _J = 125 °C	I _F = 3 A dI _F /dt = 200 A/μs V _R = 160 V	-	26	-	
Deals receivers a urrent		T _J = 25 °C		-	1.8	-	А
Peak recovery current I _{RRM}	IRRM			-	3.2	-	A
	T _J = 25 °C		-	15	-	nC	
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	41	-	nc

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to ambient	R _{thJA} ⁽¹⁾⁽²⁾		-	75	90	°C/W
Thermal resistance, junction to mount, per leg	R _{thJM} ⁽³⁾		-	3.2	4	°C/W
Weight			-	0.20	-	g
Marking device		Case style SlimDPAK (TO-252AE)		6CV	/H02	

Notes

- $^{(1)}$ The heat generated must be less than thermal conductivity from junction-to-ambient; $dP_D/dT_J < 1R_{thJA}$
- ⁽²⁾ Free air, mounted or recommended copper pad area; thermal resistance R_{thJA} junction to ambient
- ⁽³⁾ Mounted on infinite heatsink

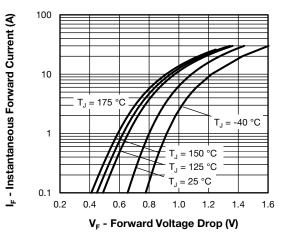


Fig. 1 - Typical Forward Voltage Drop Characteristics

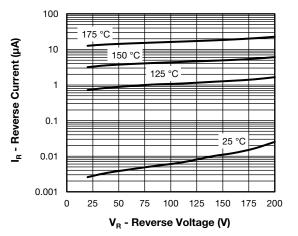


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

VS-6CVH02HM3

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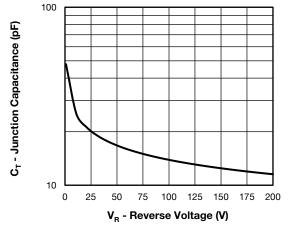


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

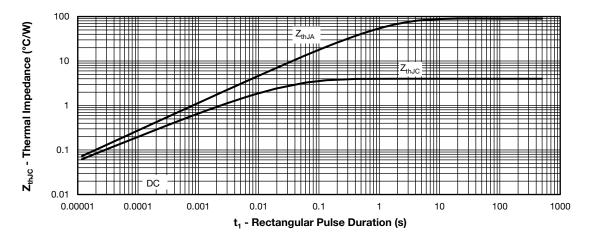
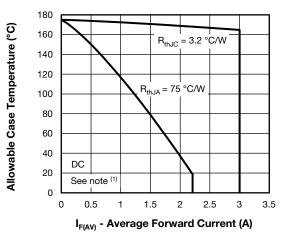
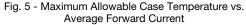


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



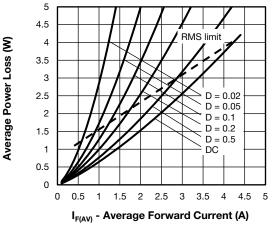
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Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward power loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \; at \; (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \; (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse power loss} = \mathsf{V}_{\mathsf{R}1} \times \mathsf{I}_{\mathsf{R}} \; (1 - \mathsf{D}); \; \mathsf{I}_{\mathsf{R}} \; at \; \mathsf{V}_{\mathsf{R}1} = \mathsf{rated V}_{\mathsf{R}} \end{array}$





Revision: 18-Feb-2022

3

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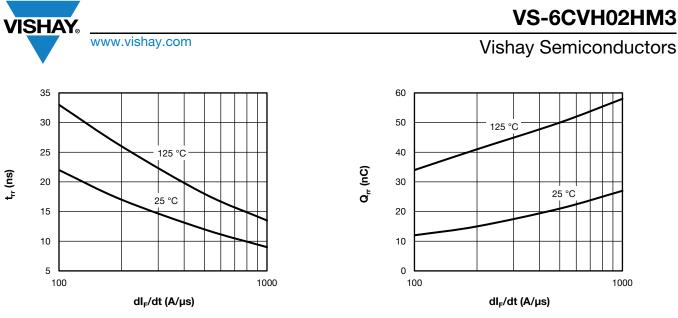


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

Fig. 8 - Typical Stored Charge vs. dl_F/dt

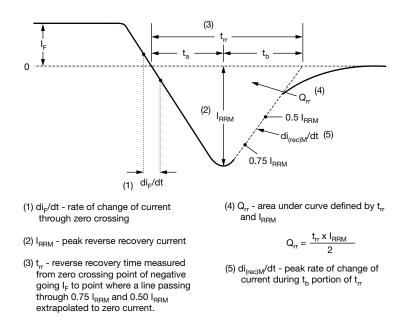
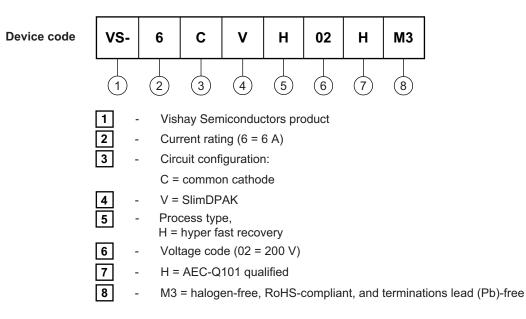


Fig. 9 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER REEL MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-6CVH02HM3/I	4500	4500	13"diameter plastic tape and reel			

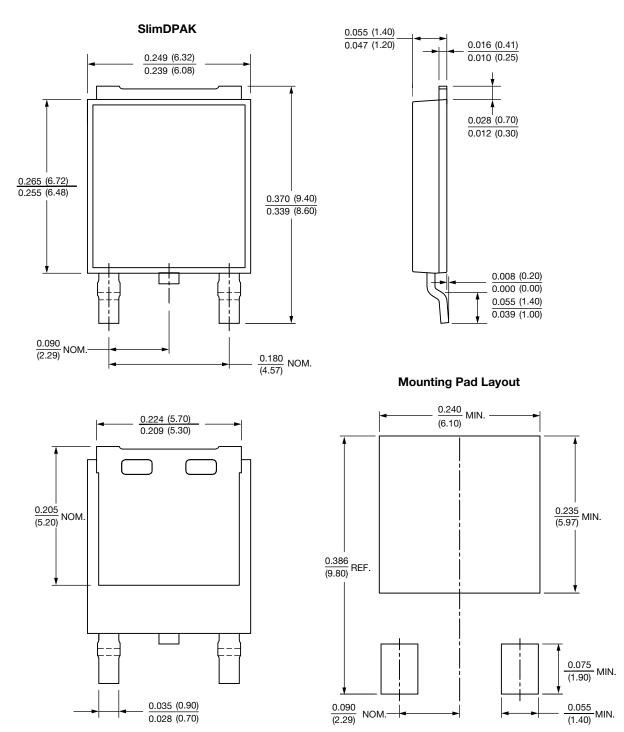
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96081			
Part marking information	www.vishay.com/doc?96085			
Packaging information	www.vishay.com/doc?88869			





SlimDPAK

DIMENSIONS in inches (millimeters)





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