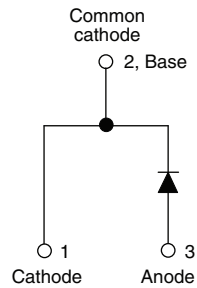


Ultrafast Rectifier, 30 A FRED Pt®


TO-247AC 2L


FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	30 A
V_R	300 V
V_F at I_F	0.9 V
t_{rr} typ.	See Recovery table
T_J max.	175 °C
Package	TO-247AC 2L
Circuit configuration	Single

DESCRIPTION / APPLICATION

300 V series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: TO-247AC 2L

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		300	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 143\text{ °C}$	30	A
Non-repetitive peak surge current	I_{FSM}	$T_J = 25\text{ °C}, t_p = 10\text{ ms}$	300	
Operating junction and storage temperatures	T_J, T_{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100\text{ }\mu\text{A}$	300	-	-	V
		$I_F = 30\text{ A}$	-	1.08	1.25	
Forward voltage	V_F	$I_F = 30\text{ A}, T_J = 125\text{ °C}$	-	0.9	1.00	
		$V_R = V_R$ rated	-	0.05	60	
Reverse leakage current	I_R	$T_J = 125\text{ °C}, V_R = V_R$ rated	-	280	600	μA
			-			
Junction capacitance	C_T	$V_R = 300\text{ V}$	-	90	-	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	3.5	-	nH



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t _{rr}	I _F = 1.0 A, di _F /dt = 50 A/μs, V _R = 30 V	-	-	55	ns
		T _J = 25 °C	-	38	-	
		T _J = 125 °C	-	52	-	
Peak recovery current	I _{RRM}	T _J = 25 °C	-	2.8	-	A
		T _J = 125 °C	-	7.3	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C	-	53	-	nC
		T _J = 125 °C	-	190	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	0.5	0.9	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	40	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-	
Weight			-	6.0	-	g
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC 2L	30EPH03			

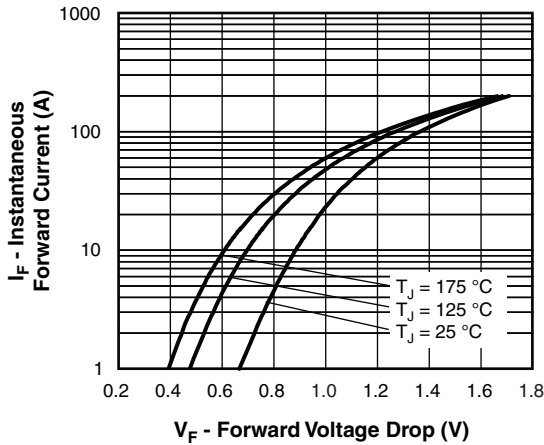


Fig. 1 - Typical Forward Voltage Drop Characteristics

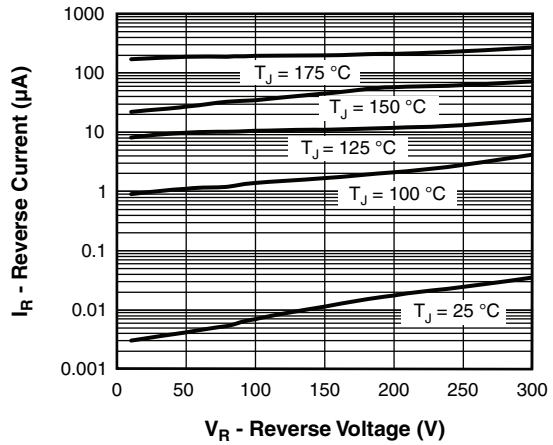


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

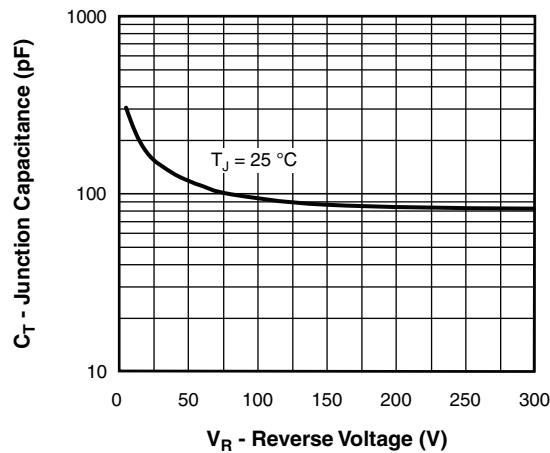


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

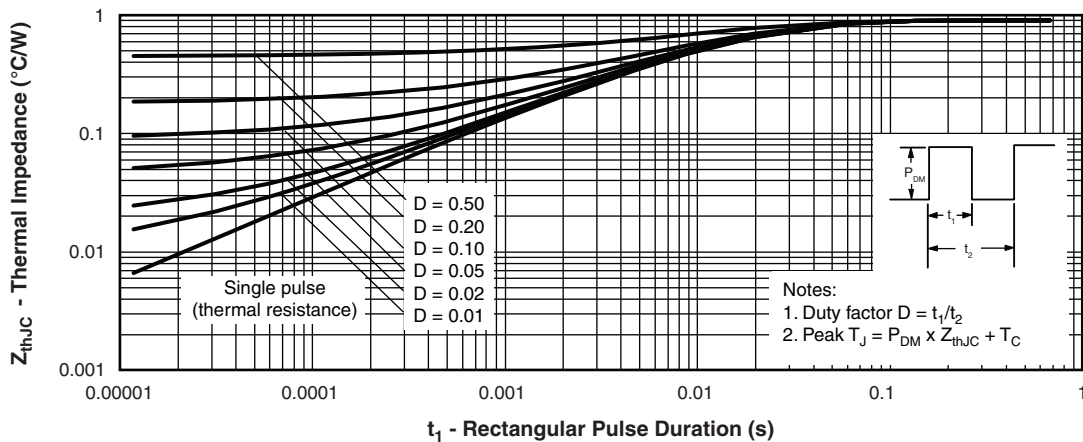


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

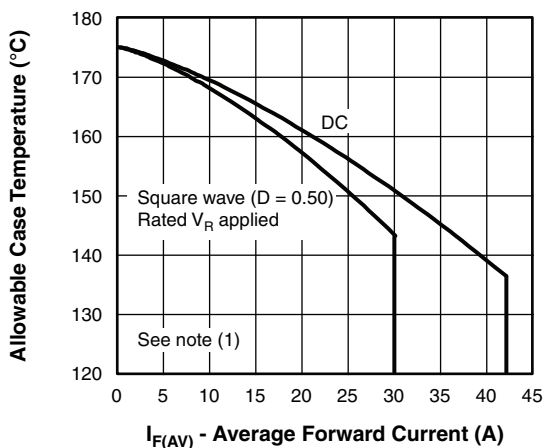


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

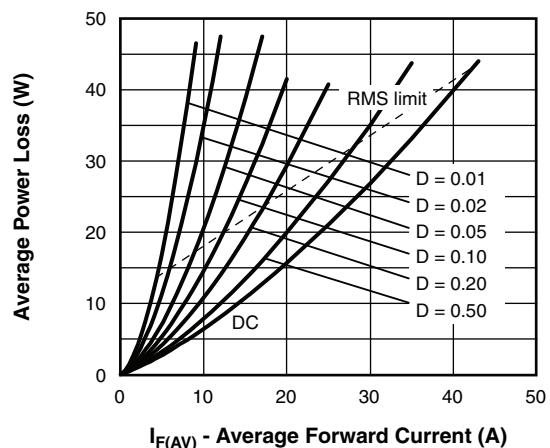


Fig. 6 - Forward Power Loss Characteristics

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R

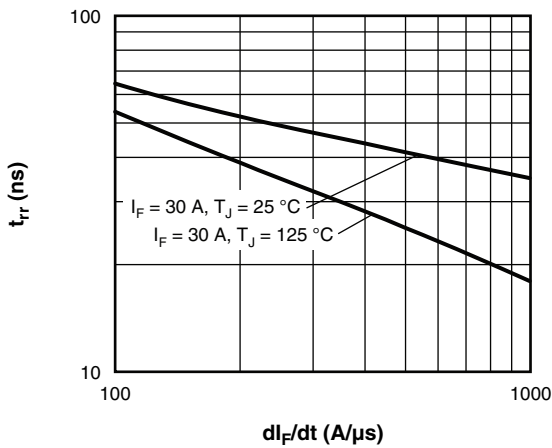


Fig. 7 - Typical Reverse Recovery Time vs. di_F/dt

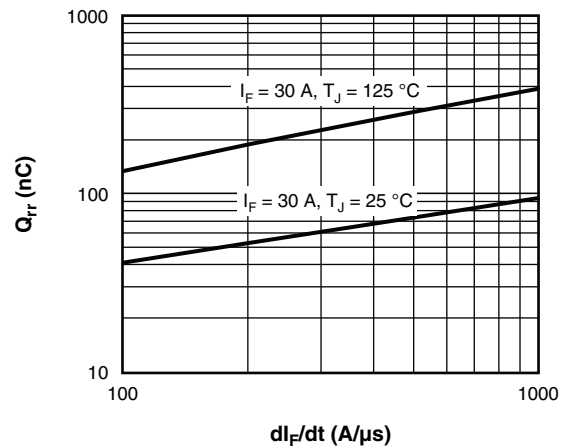
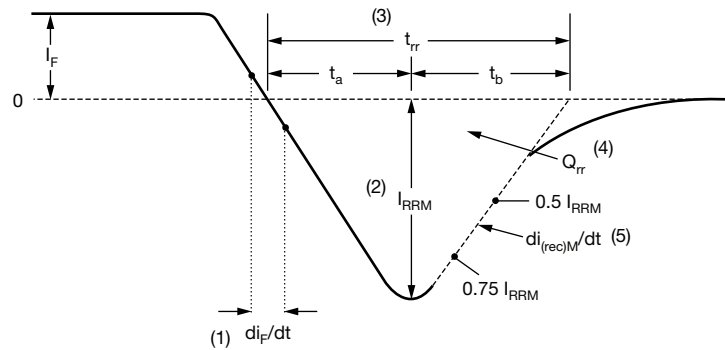


Fig. 8 - Typical Stored Charge vs. di_F/dt



- (1) di_F/dt - rate of change of current through zero crossing
- (2) I_{RRM} - peak reverse recovery current
- (3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.50 I_{RRM}$ extrapolated to zero current.

- (4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

- (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code	VS-	30	E	P	H	03	-N3
	①	②	③	④	⑤	⑥	⑦

- 1** - Vishay Semiconductors product
- 2** - Current rating (30 = 30 A)
- 3** - Circuit configuration:
E = single diode
- 4** - Package:
P = TO-247AC
- 5** - H = hyperfast recovery
- 6** - Voltage rating (03 = 300 V)
- 7** - Environmental digit:
-N3 = halogen-free, RoHS-compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-30EPH03-N3	25	500	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96144
Part marking information	www.vishay.com/doc?95648



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