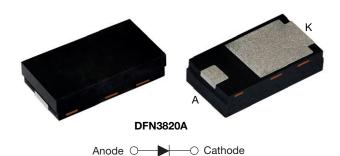
RoHS COMPLIANT

HALOGEN

FREE

Vishay Semiconductors

Ultrafast Rectifier, 2 A FRED Pt[®]



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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 A				
V _R	200 V				
V _F at I _F	0.71 V				
t _{rr} (typ.)	15 ns				
I _{FSM}	54 A				
T _J max.	175 °C				
Package	DFN3820A				
Circuit configuration	Single				

FEATURES

- Very low profile typical height of 0.88 mm
- · Ideal for automated placement
- Wettable flanks allows easy inspection with AOI (automated optical inspection). No X-ray necessary
- Low forward voltage drop, low power losses
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- For PFC, CRM snubber operation
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency inverters, DC/DC converters, freewheeling diodes, clamping and snubber, polarity protection, LED lighting

MECHANICAL DATA

Case: DFN3820A

Molding compound meets UL 94 V-0 flammability rating Terminals: matte tin plated leads, solderable per J-STD-002, meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		200	V		
Average rectified forward current	I _{F(AV)}	T _M = 165 °C	2	٨		
Non-repetitive peak surge current	I _{FSM}	$T_J = 25 \ ^{\circ}C$, 10 ms sine pulse	54	A		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	200	-	-		
Forward voltage	V	I _F = 2 A	-	0.88	0.95	V	
Forward voltage	V _F	I _F = 2 A, T _J = 150 °C	-	0.71	0.76		
Reverse leakage current	I_	V _R = V _R rated	-	-	2		
everse leakage current I _R		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	50	μA	
Junction capacitance	CT	V _R = 200 V	-	10	-	рF	

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS MIN.			MAX.	UNITS
		$I_{\rm F} = 0.5 \text{ A}, I_{\rm R} = 1 \text{ A}$	A, I _{rr} = 0.25 A	-	15	25	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	10	-	ns
		T _J = 125 °C		-	15	-	
Doold recovery ourrent	1	T _J = 25 °C	$I_F = 2 A$	-	3.1	-	A nC
Peak recovery current	I _{RRM}	T _J = 125 °C	dI _F /dt = 500 A/µs V _B = 200 V	-	4.7	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C	1	-	18	-	
		T _J = 125 °C		-	39	-	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C		
Thermal resistance, junction to mount	R _{thJM} ⁽¹⁾		-	5	6.3			
Thermal resistance, junction to ambient	R _{thJA}	Device mounted on FR4 PCB, 2 oz. standard footprint	-	140	-	°C/W		
Weight			-	0.023	-	9		
Marking device		Case style DFN3820A		21	-12			

Note

⁽¹⁾ Thermal resistance junction to mount follows JEDEC[®] 51-14 transient dual interface test method (TDIM)

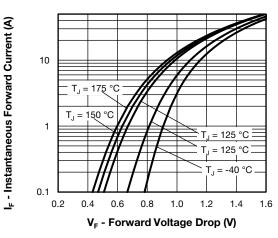


Fig. 1 - Typical Forward Voltage Drop Characteristics

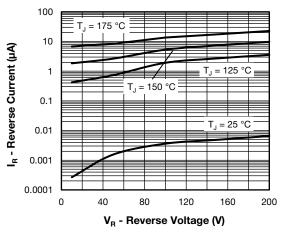


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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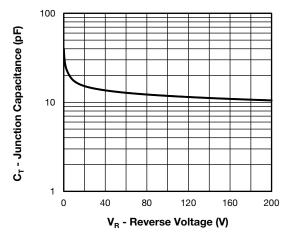


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

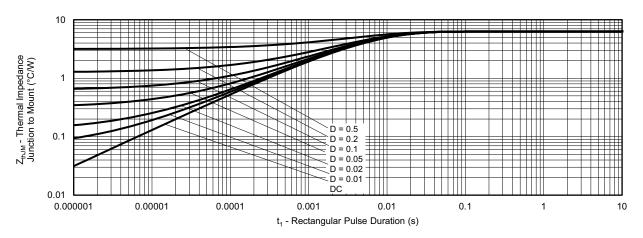
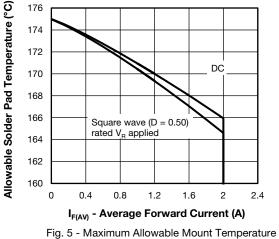


Fig. 4 - Maximum Transient Thermal Impedance, Junction to Mount



vs. Average Forward Current

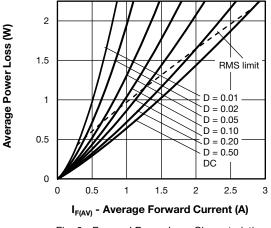


Fig. 6 - Forward Power Loss Characteristics

Note

 $\begin{array}{l} \mbox{Formula used: } T_M = T_J - (Pd + Pd_{REV}) \times R_{thJM}; \\ \mbox{Pd = forward power loss = } I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 5); } \\ \mbox{Pd}_{REV} = \mbox{inverse power loss = } V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = \mbox{rated } V_R \end{array}$

Revision: 26-Jul-2023

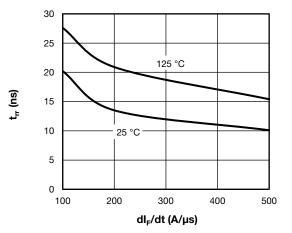
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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

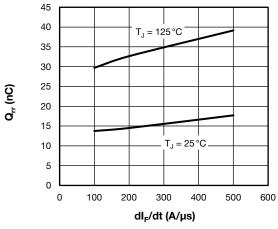
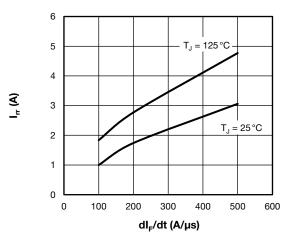


Fig. 8 - Typical Stored Charge vs. dl_F/dt





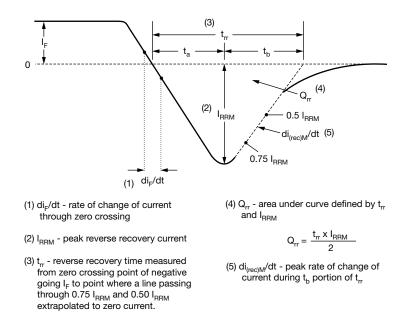


Fig. 10 - Reverse Recovery Waveform and Definitions

Revision: 26-Jul-2023	4	Document Number: 97083
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ORDERING INFORMATION TABLE

Device code	vs-	2	Е	A	н	02	-M3
		2	3	4	5	6	7
	1	- Visl	nay Sen	niconduo	ctors pro	oduct	
	2	- Cur	rent rati	ng (2 = 2	2 A)		
	3	- Circ	cuit conf	iguratior	า:		
		E =	single o	liode			
	4	- A =	DFN38	20A pac	kage		
	5	- Pro	cess typ	e,			
		H =	ultrafas	t recove	ery		
	6	- Vol	tage coo	de (02 =	200 V)		
	7	M3	s = halog	gen-free	, RoHS-	complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-2EAH02-M3/H	Н	3500	7" diameter plastic tape and reel				
VS-2EAH02-M3/I	I	14 000	13" diameter plastic tape and reel				

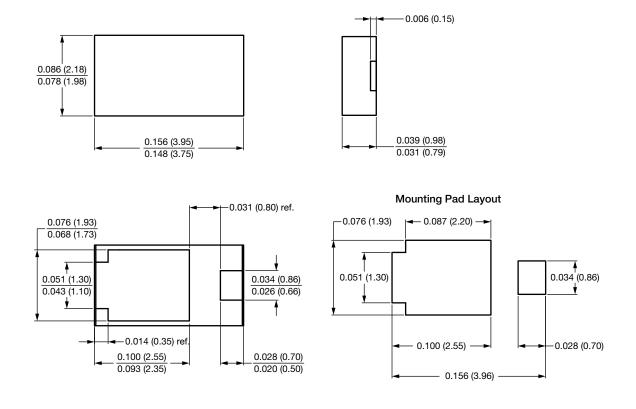
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?97066				
Part marking information	www.vishay.com/doc?97065				
Packaging information	www.vishay.com/doc?98488				
SPICE model	www.vishay.com/doc?97096				





DFN3820A, FRED Pt[®]

DIMENSIONS in inches (millimeters)





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