VF30100S

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Vishay General Semiconductor

High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.39$ V at $I_F = 5$ A

TMBS® ITO-220AB

PIN 1 O	PIN 2
PIN 3 O	 0

PRIMARY CHARACTERISTICS			
I _{F(AV)}	30 A		
V _{RRM}	100 V		
I _{FSM}	250 A		
V_F at $I_F = 30$ A	0.69 V		
T _J max.	150 °C		
Package	ITO-220AB		
Circuit configuration	Single		

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
 FREE
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

MECHANICAL DATA

Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	VF30100S	UNIT	
Maximum repetitive peak reverse voltage	V _{RRM}	100	V	
Maximum average forward rectified current (fig. 1)	I _{F(AV)}	30	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	250	А	
Isolation voltage from terminal to heat sink t = 1 min	V _{AC}	1500	V	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5 A	T _A = 25 °C	V _F (1)	0.47	-	V
	I _F = 10 A			0.55	-	
	I _F = 30 A			0.80	0.91	
	I _F = 5 A	T _A = 125 °C		0.39	-	
	I _F = 10 A			0.49	-	
	I _F = 30 A			0.69	0.78	
Reverse current	V _R = 70 V	T _A = 25 °C	I _R (2)	27	-	μA
		T _A = 125 °C		11	-	mA
	V _R = 100 V	T _A = 25 °C		70	1000	μA
		T _A = 125 °C		23	45	mA

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

Revision: 16-Mar-18 Document Number: 89195 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>





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THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)			
PARAMETER	SYMBOL	VF30100S	UNIT
Typical thermal resistance	$R_{ ext{ heta}JC}$	4.0	°C/W

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
ITO-220AB	VF30100S-M3/4W	1.805	4W	50/tube	Tube

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

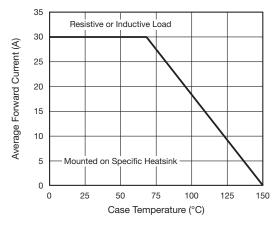


Fig. 1 - Forward Current Derating Curve

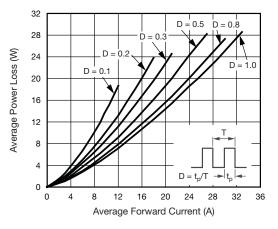


Fig. 2 - Forward Power Loss Characteristics

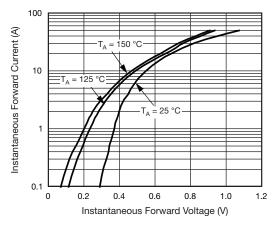


Fig. 3 - Typical Instantaneous Forward Characteristics

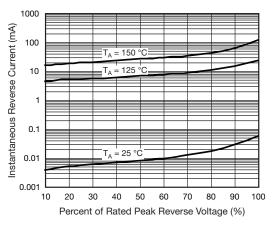
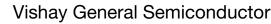
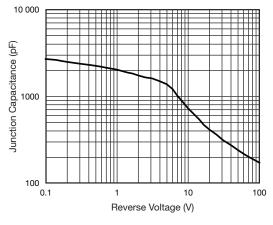


Fig. 4 - Typical Reverse Characteristics





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Fig. 5 - Typical Junction Capacitance

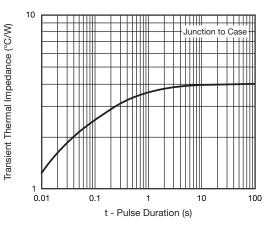
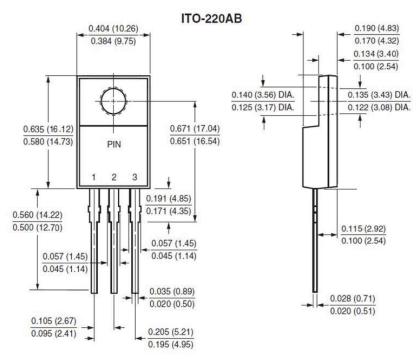


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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