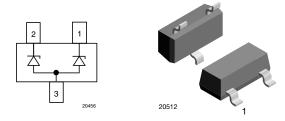
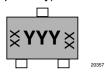




Dual-Line Unidirectional ESD Protection Diode in SOT-23



MARKING (example only)



YYY = type code (see table below) XX = date code

FEATURES

- Small SOT-23 package
- AEC-Q101 qualified available
- 2-line unidirectional ESD protection
- Working range 33 V
- Low leakage current I_R < 0.05 μA
- Low load capacitance C_D < 18 pF
- ESD immunity acc. IEC 61000-4-2
 ± 15 kV contact discharge
 ± 15 kV air discharge
- e3 pins plated with tin (Sn)
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS COMPLIANT HALOGEN



LINKS TO ADDITIONAL RESOURCES



ORDERING INFORMATION								
PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE				PACKAG			
	AEC-Q101 QUALIFIED	Rohs-compliant + Lead (Pb)-free terminations		TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
	QUALIFIED	STANDARD	GREEN	PLATED	15K/BOX = MOQ	10K/BOX = MOQ		
VESD33A2-03S	-	G	-	3	-08	-	VESD33A2-03S-G3-08	
VESD33A2-03S	Н	G	-	3	-08	-	VESD33A2-03SHG3-08	
VESD33A2-03S	-	G	-	3	-	-18	VESD33A2-03S-G3-18	
VESD33A2-03S	Н	G	-	3	-	-18	VESD33A2-03SHG3-18	

PACKAGE DATA								
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS		
VESD33A2-03S	SOT-23	D33	8.1 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I _{PPM}	1.6	Α		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P _{PP}	100	W		
COD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	15	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	- V _{ESD}	15	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T _{stg}	-55 to +150	°C		



ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	1	lines		
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	33	V		
Reverse voltage	at I _R = 0.1 μA	V_R	33	-	-	V		
Reverse current	at V _R = 33 V	I _R	-	< 0.01	0.1	μΑ		
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	35.5	37.4	39.3	V		
Reverse clamping voltage	at I _{PP} = I _{PPM} = 1.6 A, t _p = 8/20 μs	V _C	-	56	62.5	V		
Forward clamping voltage	at I _{PP} = 1 A, t _p = 300 μs	V _F	0.9	1.1	1.2	V		
	at $I_{PP} = I_{PPM} = 1.6 \text{ A}, t_p = 8/20 \mu\text{s}$	V_{F}	-	1.22	1.32	V		
Dynamic resistance	t _p = 100 ns (TLP; 1 A to 12 A)	r _{dyn}	-	3.6	-	Ω		
Capacitance	at V _R = 0 V; f = 1 MHz	C _D	12	15	18	pF		

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

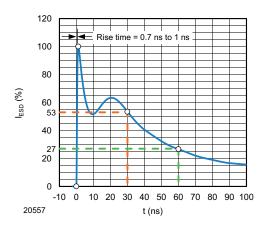


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω / 150 pF)

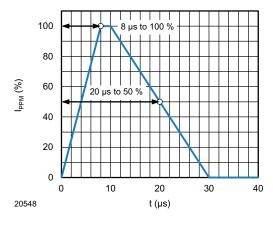


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

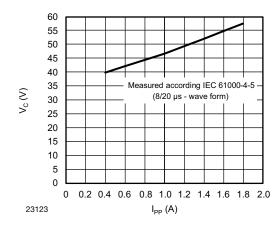


Fig. 3 - Typical Peak Clamping Voltage vs. Peak Pulse Current

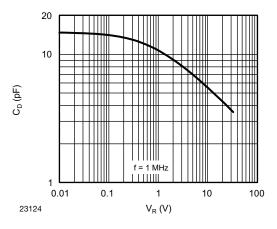


Fig. 4 - Typical Capacitance vs. Reverse Voltage



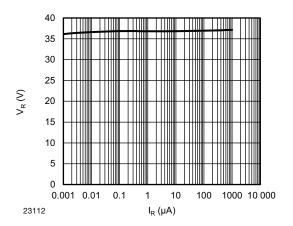


Fig. 5 - Typical Reverse Voltage vs. Reverse Current

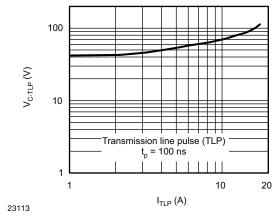


Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current

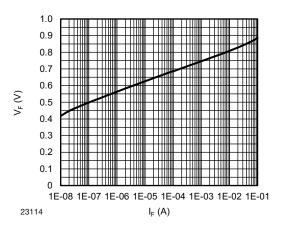


Fig. 7 - Typical Forward Voltage vs. Forward Current

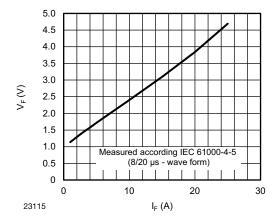
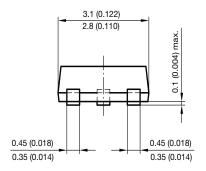
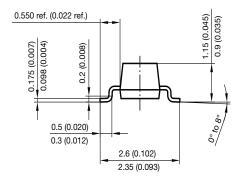


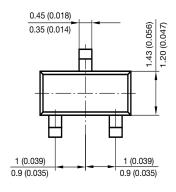
Fig. 8 - Typical Forward Voltage vs. Forward Current

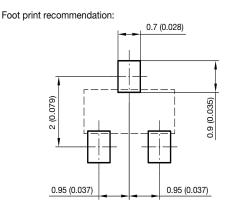


PACKAGE DIMENSIONS in millimeters (inches) SOT-23





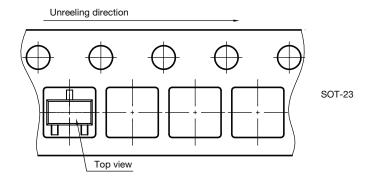




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ORIENTATION IN CARRIER TAPE SOT-23



Orientation in carrier tape SOT-23 S8-V-3929.01-006 (4) 04.02.2010 22607



A-A Section

CARRIER TAPE SOT-23

Ø 1.5 ^{+0.1} Ø 1.5 ^{+0.1} B B A 1.22 ± 0.1

B-B Section

 4 ± 0.1



Carrier tape SOT-23 Document no.: S8-V-3929.01-005 (4) Created - Date: 04. Feb. 2010 22856



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