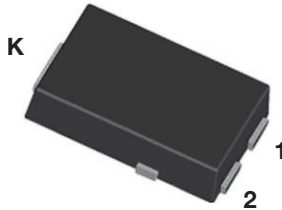
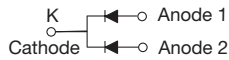


High Current Density Surface-Mount TMBS[®] (Trench MOS Barrier Schottky) Rectifier

 Ultra Low $V_F = 0.58 \text{ V}$ at $I_F = 1 \text{ A}$
eSMP[®] Series

SMPC (TO-277A)

ADDITIONAL RESOURCES

FEATURES

- Very low profile - typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
- Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT
HALOGEN
FREE
TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters and polarity protection applications.

MECHANICAL DATA
Case: SMPC (TO-277A)

 Molding compound meets UL 94 V-0 flammability rating
Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2 x 2.0 A
V_{RRM}	200 V
I_{FSM}	60 A
V_F at $I_F = 2 \text{ A}$	0.65 V
T_J max.	175 °C
Package	SMPC (TO-277A)
Circuit configuration	Common cathode

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)			
PARAMETER	SYMBOL	V4P22C	UNIT
Device marking code		V422C	
Maximum repetitive peak reverse voltage	V_{RRM}	200	V
Maximum average forward rectified current per device (fig. 1)	$I_{F(AV)}^{(1)}$	4.0	A
	$I_{F(AV)}^{(2)}$	2.8	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode	I_{FSM}	60	A
Operating junction temperature range	$T_J^{(3)}$	-40 to +175	°C
Storage temperature range	T_{STG}	-55 to +175	°C

Notes

(1) Mounted on 30 mm x 30 mm pad areas aluminum PCB

(2) Free air, mounted on recommended pad area

 (3) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$



ELECTRICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	$I_F = 1\text{ A}$	$T_A = 25\text{ }^\circ\text{C}$	$V_F^{(1)}$	0.74	-	V
	$I_F = 2\text{ A}$			0.79	0.87	
	$I_F = 1\text{ A}$	$T_A = 125\text{ }^\circ\text{C}$		0.58	-	
	$I_F = 2\text{ A}$			0.65	0.73	
Reverse current per diode	$V_R = 160\text{ V}$	$T_A = 25\text{ }^\circ\text{C}$	$I_R^{(2)}$	0.001	-	mA
		$T_A = 125\text{ }^\circ\text{C}$		0.3	-	
	$V_R = 200\text{ V}$	$T_A = 25\text{ }^\circ\text{C}$		-	0.05	
		$T_A = 125\text{ }^\circ\text{C}$		0.7	3	
Typical junction capacitance per diode	4.0 V, 1 MHz		C_J	125	-	pF

Notes(1) Pulse test: 300 μs pulse width, 1 % duty cycle(2) Pulse test: pulse width $\leq 40\text{ ms}$

THERMAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise specified)			
PARAMETER	SYMBOL	V4P22C	UNIT
Typical thermal resistance per device	$R_{\theta JA}^{(1)(2)}$	85	$^\circ\text{C/W}$
	$R_{\theta JM}^{(3)}$	5	

Notes(1) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$ (2) Free air, mounted on recommended copper pad area, 2 oz., FR4 PCB, thermal resistance $R_{\theta JA}$ - junction-to-ambient(3) Units mounted on 30 mm x 30 mm aluminum PCB, thermal resistance $R_{\theta JM}$ - junction-to-mount

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
V4P22C-M3/H	0.10	H	1500	7" diameter plastic tape and reel
V4P22C-M3/I	0.10	I	6500	13" diameter plastic tape and reel
V4P22CHM3/H ⁽¹⁾	0.10	H	1500	7" diameter plastic tape and reel
V4P22CHM3/I ⁽¹⁾	0.10	I	6500	13" diameter plastic tape and reel

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)

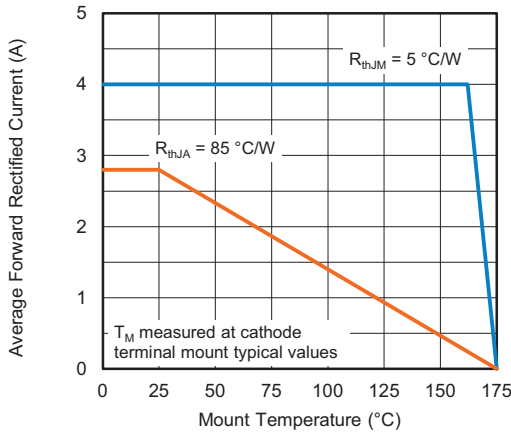


Fig. 1 - Maximum Forward Current Derating Curve

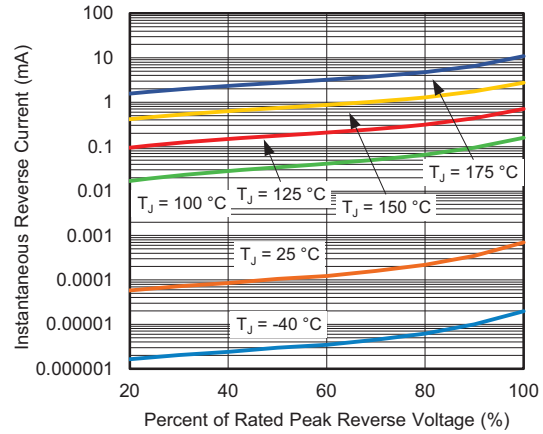


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

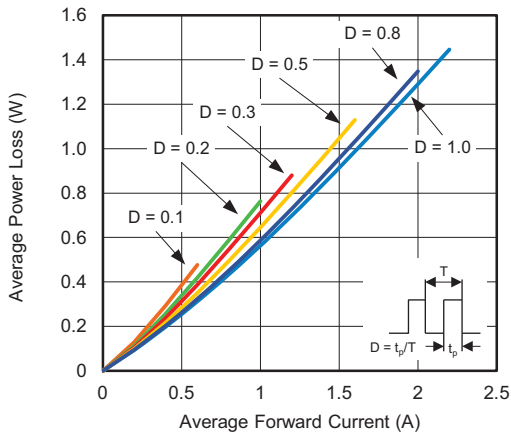


Fig. 2 - Forward Power Loss Characteristics Per Diode

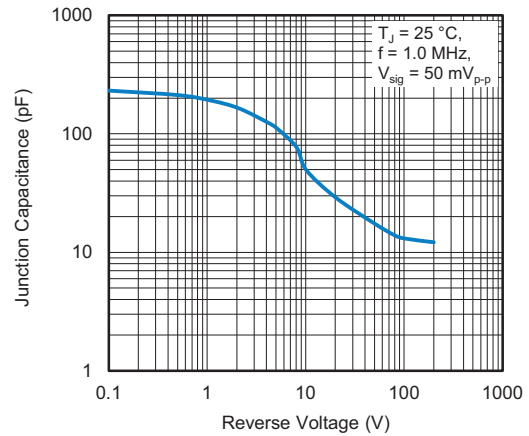


Fig. 5 - Typical Junction Capacitance Per Diode

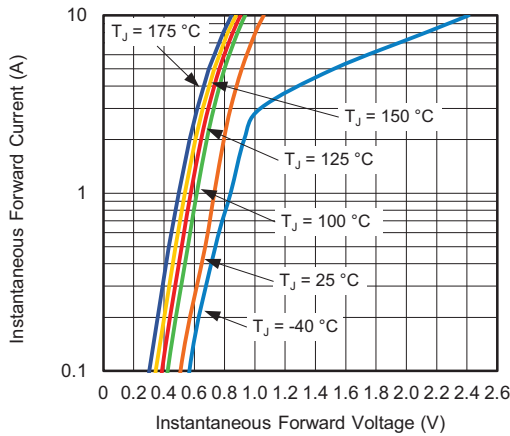


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

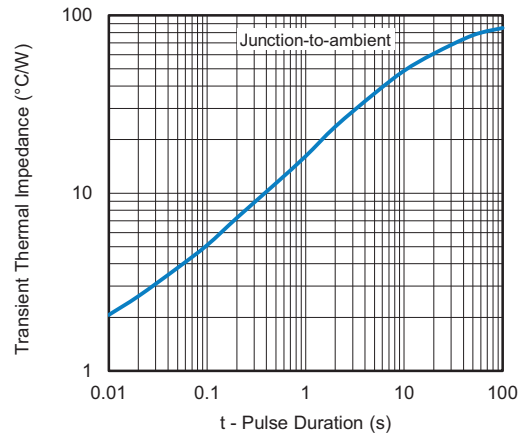
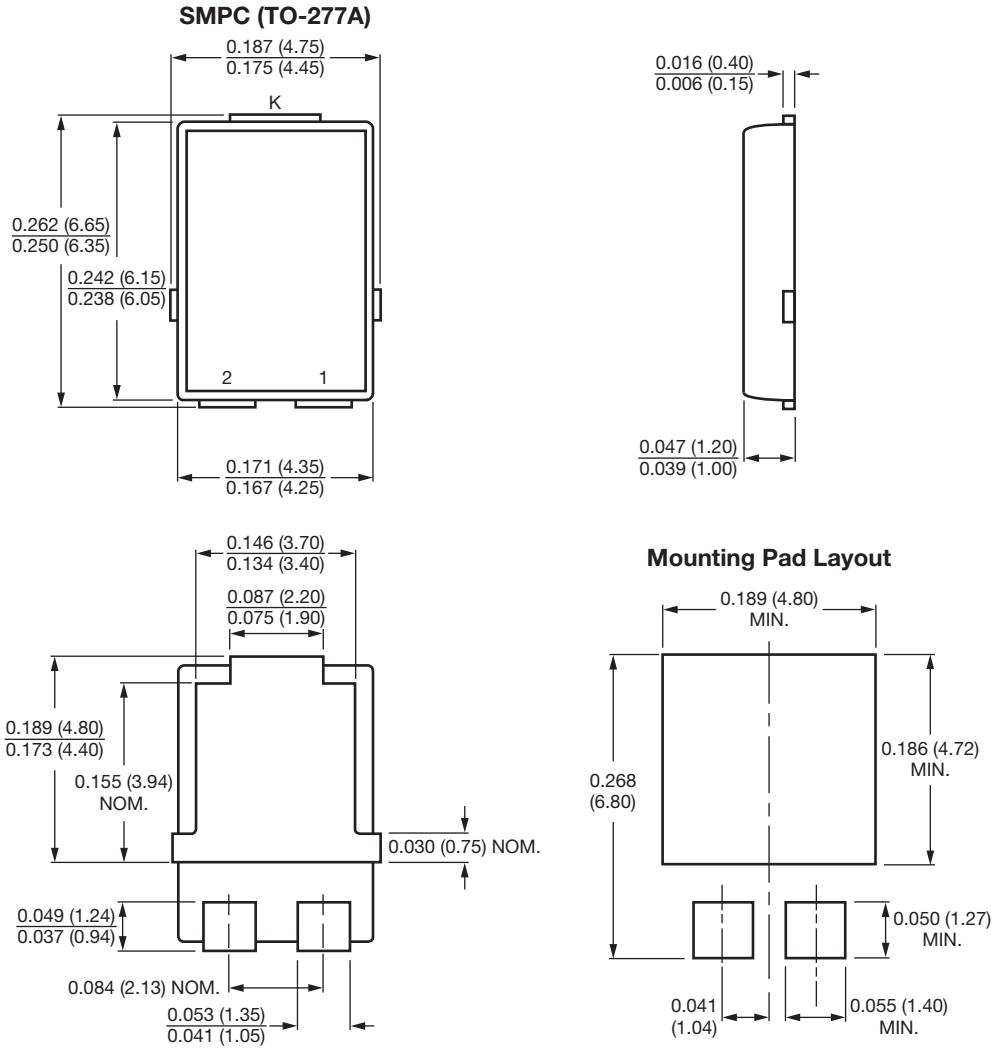


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



Conform to JEDEC® TO-277A



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.