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Vishay Siliconix

Automotive N-Channel 60 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY					
V _{DS} (V)	60				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.022				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.031				
I _D (A)	12				
Configuration	Single				

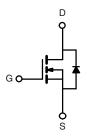
FEATURES

- TrenchFET® power MOSFET
- 100 % R_g and UIS tested
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



N-Channel MOSFET

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4850CEY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATING	(1) = 25 O, unless			
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	60	W
Gate-source voltage		V_{GS}	± 20	V
Continuous drain current	T _C = 25 °C	1	12	
	T _C = 125 °C	l _D	6.9	
Continuous source current (diode conduction)		I _S	6.2	А
Pulsed drain current ^a		I _{DM}	48	
Single pulse avalanche current	1 0.1 ml l	I _{AS}	23	
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	26	mJ
Maximum power dissipation	T _C = 25 °C	- P _D	6.8	W
	T _C = 125 °C		2.2	
Operating junction and storage temperature	e range	T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-ambient	PCB mount b	R_{thJA}	85	°C/W		
Junction-to-foot (Drain)		R_{thJF}	22	G/W		

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0, I _D = 250 μA		60	-	-		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		1.5	2	2.5	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
-	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V	-	-	1		
Zero gate voltage drain current		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	_	50	μΑ	
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	150		
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 \text{ V}$	30	_	-	Α	
	-D(011)	V _{GS} = 10 V	I _D = 6 A	-	0.018	0.022	Ω	
		V _{GS} = 10 V	I _D = 6 A, T _J = 125 °C	-	-	0.037		
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = 10 V	I _D = 6 A, T _J = 175 °C	-	-	0.047		
		V _{GS} = 4.5 V	I _D = 5 A	-	0.025	0.031		
Forward transconductance b	9 _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 6 \text{ A}$		-	21	-	S	
Dynamic ^b	0.0		· -					
Input capacitance	C _{iss}		V _{DS} = 25 V, f = 1 MHz	-	1100	1250	pF	
Output capacitance	Coss	V _{GS} = 0 V		-	171	235		
Reverse transfer capacitance	C _{rss}	1		-	77	95		
Total gate charge c	Qg			-	22.3	30		
Gate-source charge c	Q _{gs}	V _{GS} = 10 V	V V _{DS} = 30 V, I _D = 6 A	-	4.1	-	nC	
Gate-drain charge ^c	Q _{gd}	1		-	5.1	-		
Gate resistance	R _g		f = 1 MHz		0.41	2.1	Ω	
Turn-on delay time ^c	t _{d(on)}			-	10	11		
Rise time ^c	t _r	V_{DD} = 30 V, R_L = 30 Ω I_D \cong 1 A, V_{GEN} = 10 V, R_g = 1 Ω		-	4	14	ns	
Turn-off delay time ^c	t _{d(off)}			-	23	35		
Fall time ^c	t _f			-	11	14		
Source-Drain Diode Ratings and Charac	teristics b	I				l		
Pulsed current a	I _{SM}			-	-	48	Α	
Forward voltage	V_{SD}	I _F = 1.7 A, V _{GS} = 0 V		-	0.73	1.2	V	
Body diode reverse recovery time	t _{rr}	I _F = 2.8 A, di/dt = 100 A/μs		-	20	40	ns	
Body diode reverse recovery charge	Q_{rr}			-	19	38	nC	
Reverse recovery fall time	ta			-	15	-	ns	
Reverse recovery rise time	t _b			-	5	-		
Body diode peak reverse recovery current	I _{RM(REC)}			-	-2	-	Α	

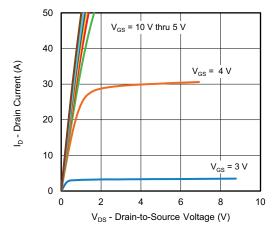
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

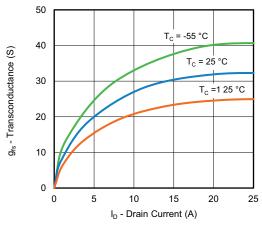
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



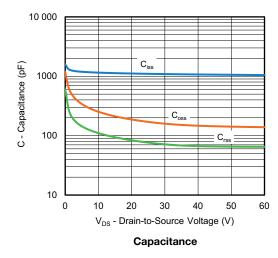
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

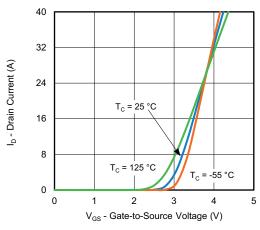


Output Characteristics

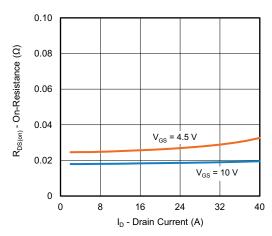


Transconductance

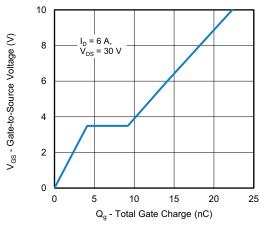




Transfer Characteristics



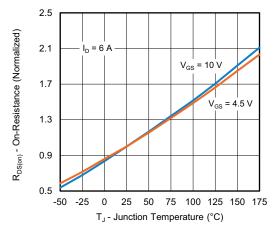
On-Resistance vs. Drain Current



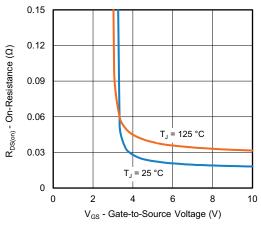
Gate Charge



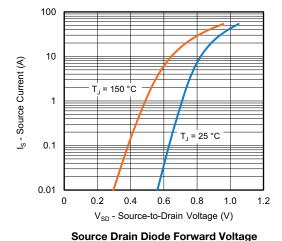
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

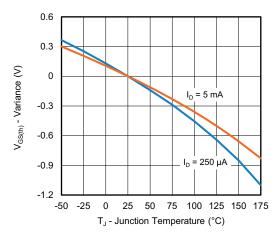


On-Resistance vs. Junction Temperature

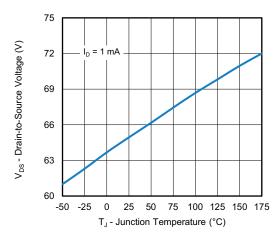


On-Resistance vs. Gate-to-Source Voltage

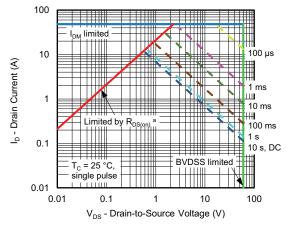




Threshold Voltage



Drain Source Breakdown vs. Junction Temperature



Safe Operating Area

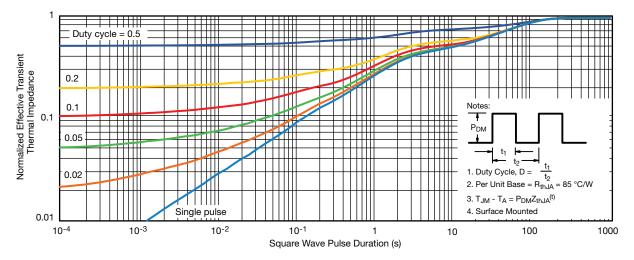
Note

a. V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

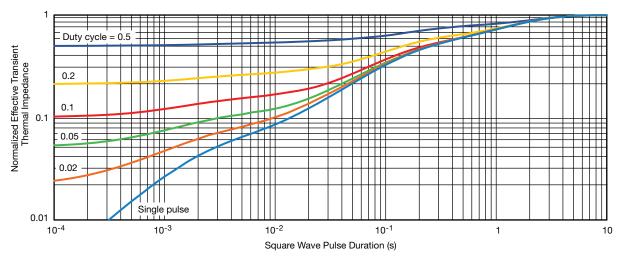
For technical questions, contact: automostech



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transie#nt Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62018.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INC	INCHES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
FCN: C-06527-Bey 11-Sen-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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