



Automotive N-Channel 60 V (D-S) 175 °C MOSFET

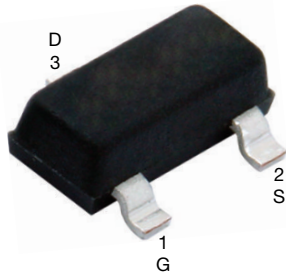


PRODUCT SUMMARY	
V _{DS} (V)	60
R _{DS(on)} (Ω) at V _{GS} = 10 V	0.085
R _{DS(on)} (Ω) at V _{GS} = 4.5 V	0.130
I _D (A)	4
Configuration	Single

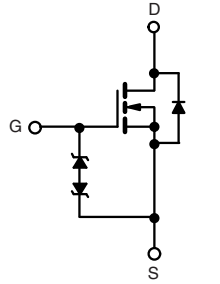
FEATURES

- TrenchFET® Power MOSFET
- AEC-Q101 Qualified^c
- 100 % R_g and UIS Tested
- Typical ESD Protection 800 V
- Material categorization:
For definitions of compliance please see www.vishay.com/doc?99912

SOT-23 (TO-236)



Top View



N-Channel MOSFET

Marking Code: 8Mxxx

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and Halogen-free	SQ2360EES-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	± 20	
Continuous Drain Current	T _C = 25 °C	I _D	4	A
	T _C = 125 °C		2.3	
Continuous Source Current (Diode Conduction)		I _S	3.7	
Pulsed Drain Current ^a		I _{DM}	16	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	6	
Single Pulse Avalanche Energy		E _{AS}	1.8	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	P _D	3	W
	T _C = 125 °C		1	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^b	R _{thJA}	166	°C/W
Junction-to-Foot (Drain)		R _{thJF}	50	

Notes

- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.



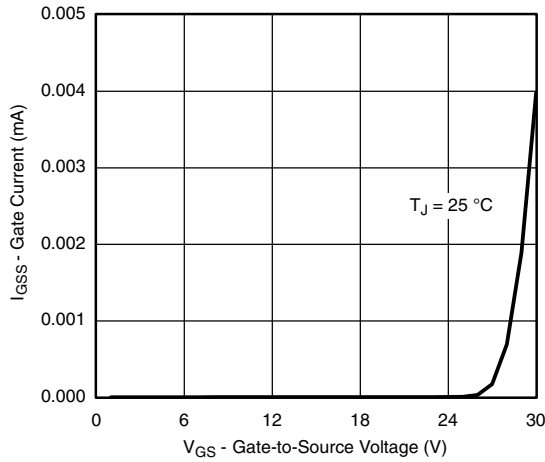
SPECIFICATIONS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0, I_D = 250\text{ }\mu\text{A}$		60	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$		1.5	-	2.5	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$		-	-	± 5.5	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}$	-	-	1	μA
		$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	50	
		$V_{GS} = 0\text{ V}$	$V_{DS} = 60\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	150	
On-State Drain Current ^a	$I_{D(on)}$	$V_{GS} = 10\text{ V}$	$V_{DS} \geq 5\text{ V}$	10	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$	$I_D = 6\text{ A}, T_J = 25\text{ }^\circ\text{C}$	-	0.058	0.085	Ω
		$V_{GS} = 10\text{ V}$	$I_D = 6\text{ A}, T_J = 125\text{ }^\circ\text{C}$	-	-	0.197	Ω
		$V_{GS} = 10\text{ V}$	$I_D = 6\text{ A}, T_J = 175\text{ }^\circ\text{C}$	-	-	0.258	
		$V_{GS} = 4.5\text{ V}$	$I_D = 5\text{ A}$	-	0.081	0.130	Ω
Forward Transconductance ^b	g_{fs}	$V_{DS} = -15\text{ V}, I_D = 1.9\text{ A}$		-	5.8	-	S
Dynamic ^b							
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}$	$V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	295	370	μF
Output Capacitance	C_{oss}			-	55	70	
Reverse Transfer Capacitance	C_{rss}			-	35	55	
Total Gate Charge ^c	Q_g	$V_{GS} = 10\text{ V}$	$V_{DS} = 30\text{ V}, I_D = 2\text{ A}$	-	7.40	12	nC
Gate-Source Charge ^c	Q_{gs}			-	0.95	-	
Gate-Drain Charge ^c	Q_{gd}			-	1.94	-	
Gate Resistance	R_g	f = 1 MHz		1.24	2.46	3.68	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 30\text{ V}, R_L = 15\text{ }\Omega$ $I_D \cong 2\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		-	5	8	ns
Rise Time ^c	t_r			-	11	17	
Turn-Off Delay Time ^c	$t_{d(off)}$			-	10	15	
Fall Time ^c	t_f			-	8	12	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed Current ^a	I_{SM}			-	-	16	A
Forward Voltage	V_{SD}	$I_F = 1.5\text{ A}, V_{GS} = 0$		-	0.8	1.2	V

Notes

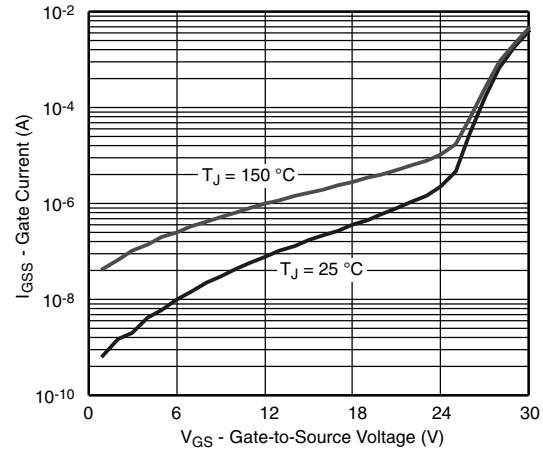
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

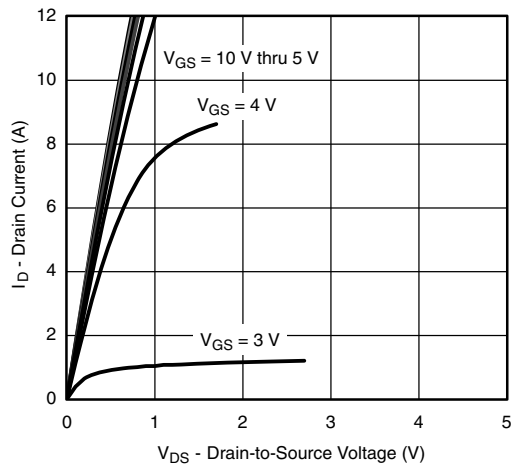
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



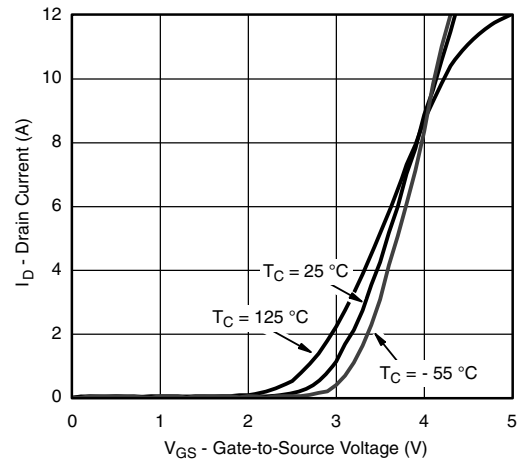
Gate Current vs. Gate-Source Voltage



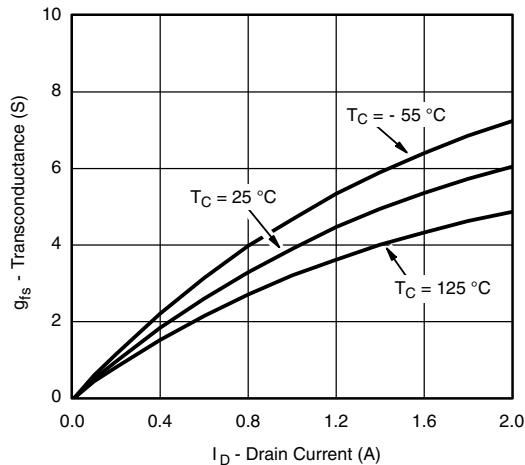
Gate Current vs. Gate-Source Voltage



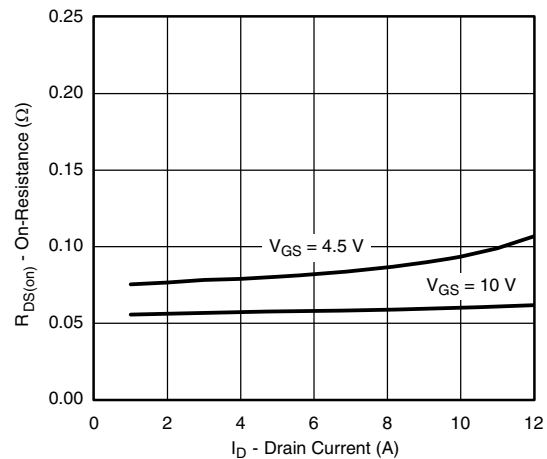
Output Characteristics



Transfer Characteristics



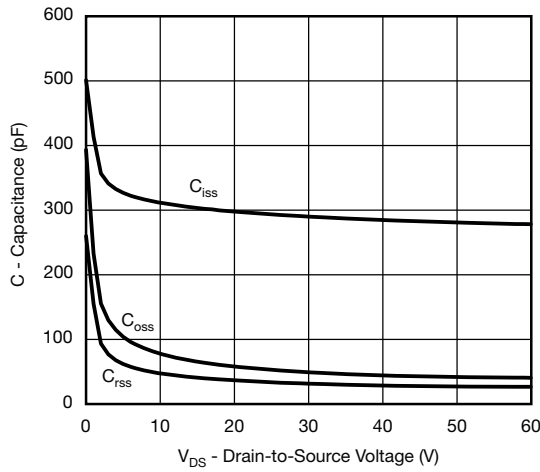
Transconductance



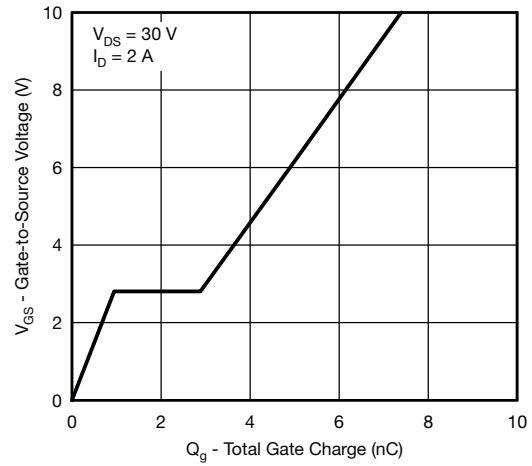
On-Resistance vs. Drain Current



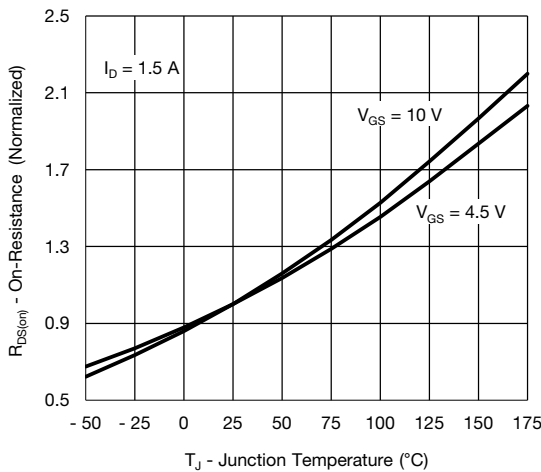
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



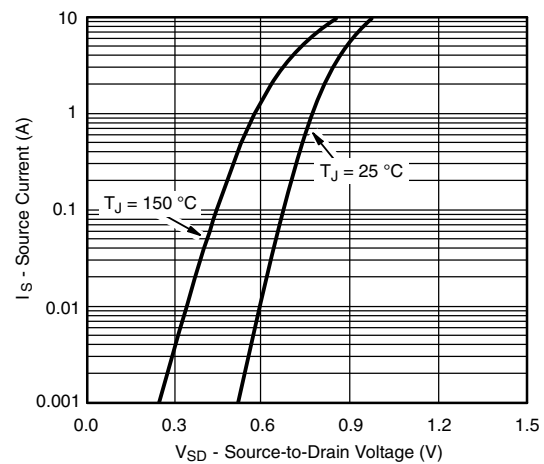
Capacitance



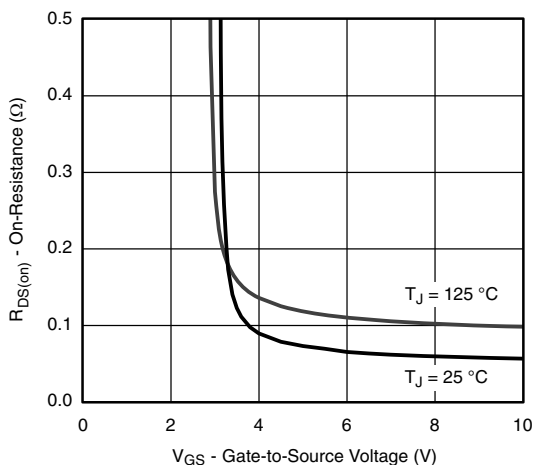
Gate Charge



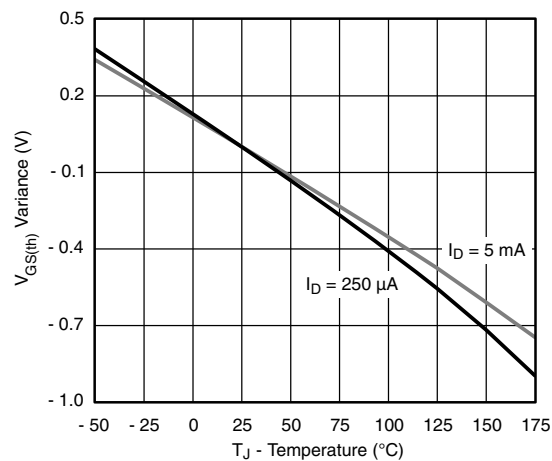
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

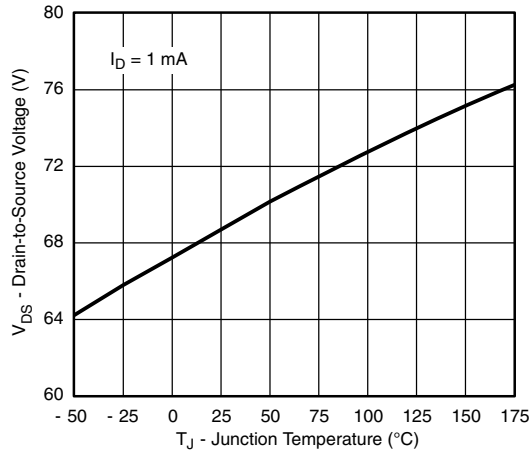


On-Resistance vs. Gate-Source Voltage

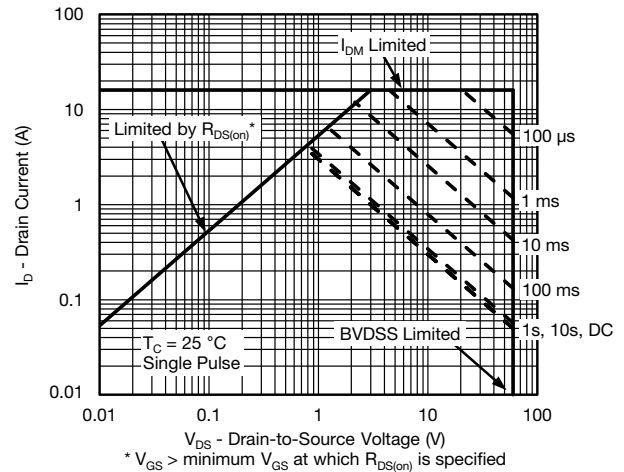


Threshold Voltage

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)

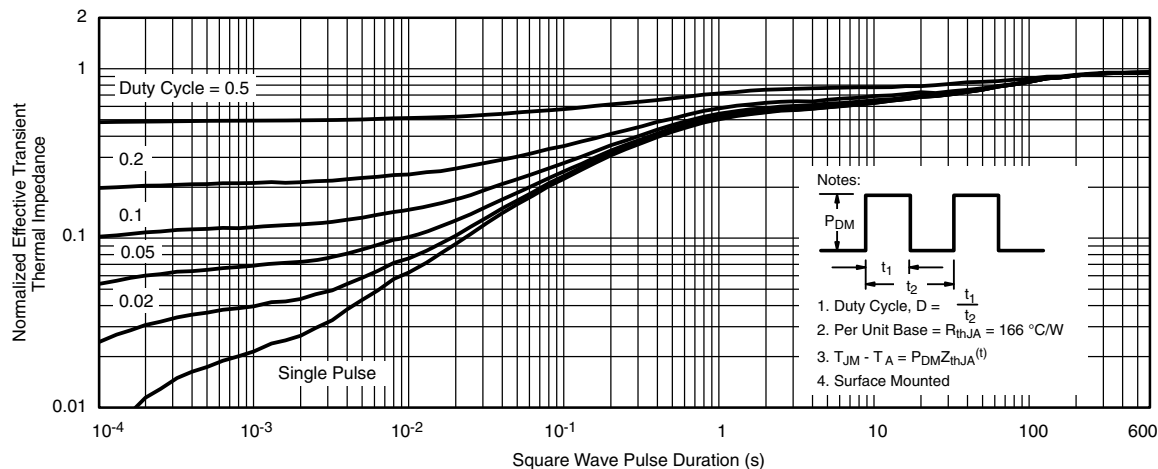


Drain-Source Breakdown vs. Junction Temperature



Safe Operating Area

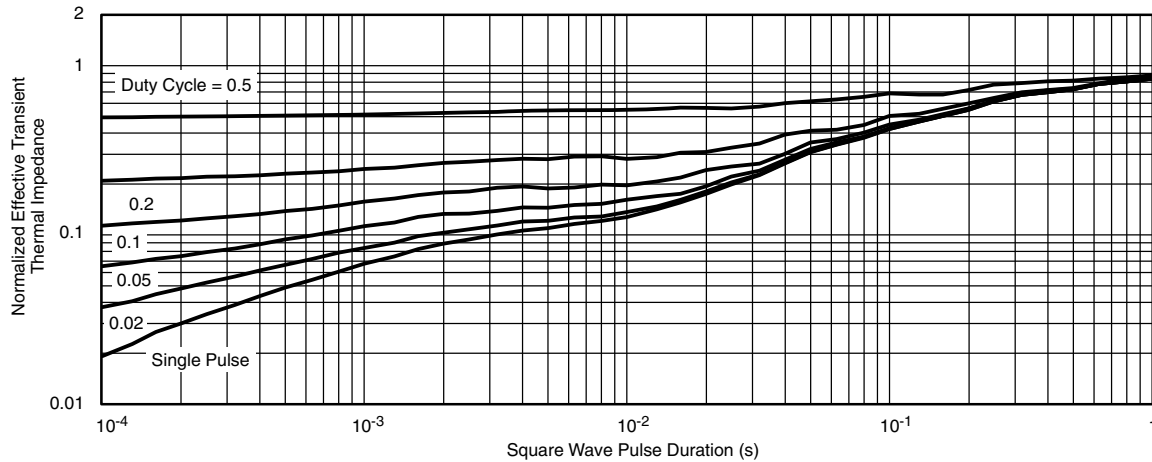
THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)
 are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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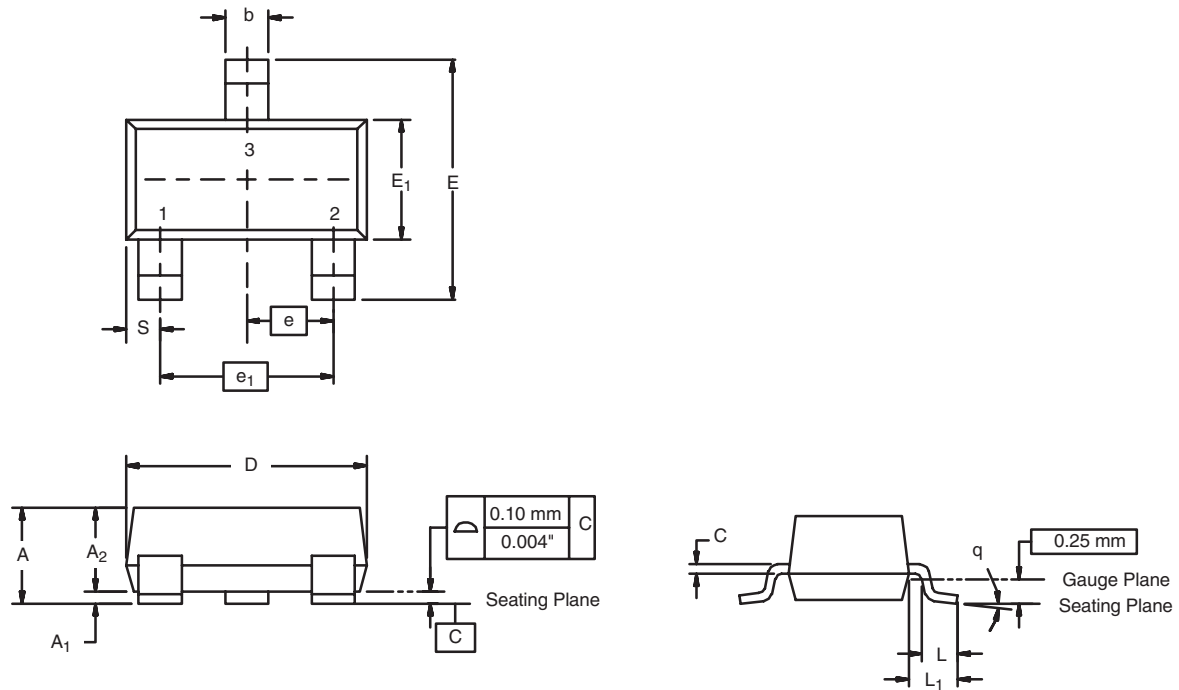


REVISION HISTORY ^a		
REVISION	DATE	DESCRIPTION OF CHANGE
F	04-Apr-14	<ul style="list-style-type: none">• Correction of $R_{DS(on)}$ value used in calculation of maximum continuous drain current and safe operating area curve.• $V_{GS} = 4.5$ V curve added to on-resistance vs. junction temperature graph.

Note

a. As of April 2014

SOT-23 (TO-236): 3-LEAD



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A ₁	0.01	0.10	0.0004	0.004
A ₂	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
c	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E ₁	1.20	1.40	0.047	0.055
e	0.95 BSC		0.0374 Ref	
e ₁	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024
L ₁	0.64 Ref		0.025 Ref	
S	0.50 Ref		0.020 Ref	
q	3°	8°	3°	8°

ECN: S-03946-Rev. K, 09-Jul-01
 DWG: 5479

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads
Dimensions in Inches/(mm)

[Return to Index](#)



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