## SQ2348CES

www.vishay.com

**Vishay Siliconix** 

# Automotive N-Channel 30 V (D-S) 175 °C MOSFET



#### FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 %  $R_q$  and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

D

N-Channel MOSFET



Marking Code: 90YXX

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	30		
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0.024		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0.032		
I <sub>D</sub> (A)	8		
Configuration	Single		

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	SQ2348CES (for detailed order number please see <u>www.vishay.com/doc?79771</u> )

ABSOLUTE MAXIMUM RATING	<b>S</b> (T <sub>C</sub> = 25 °C, unles	s otherwise noted	)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	30	V	
Gate-source voltage		V <sub>GS</sub>	± 20	V	
Continuous drain current	T <sub>C</sub> = 25 °C <sup>a</sup>		8		
Continuous drain current	T <sub>C</sub> = 125 °C		5.3		
Continuous source current (diode conduction)		IS	3.8	А	
Pulsed drain current <sup>b</sup>		I <sub>DM</sub>	32		
Single pulse avalanche current		I <sub>AS</sub>	15.5		
Single pulse avalanche energy	L = 0.1 mH	E <sub>AS</sub>	12	mJ	
Maximum power dissipation	T <sub>C</sub> = 25 °C	D	3	w	
	T <sub>C</sub> = 125 °C	P <sub>D</sub>	1	vv l	
Operating junction and storage temperature	range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	166	°C/W
Junction-to-foot (drain)		R <sub>thJF</sub>	50	0/11

#### Notes

a. Package limited

b. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$ 

c. When mounted on 1" square PCB (FR4 material)

1

www.vishay.com

SQ2348CES

Vishay Siliconix

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							<b></b>
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$		30	-	-	v
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	- V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.5	2.0	2.5	v
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	0 V, $V_{GS} = \pm 20 V$	-	-	± 100	nA
		$V_{GS} = 0 V$	V <sub>DS</sub> = 30 V	-	-	1	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{GS} = 0 V$	$V_{DS} = 30 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	50	μA
		$V_{GS} = 0 V$	V <sub>DS</sub> = 30 V, T <sub>J</sub> = 175 °C	-	-	150	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{GS} = 10 V$	$V_{DS} \ge 5 V$	10	-	-	А
		$V_{GS} = 10 V$	I <sub>D</sub> = 12 A	-	0.020	0.024	
Drain-source on-state resistance <sup>a</sup>	Р	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 12 A, T <sub>J</sub> = 125 °C	-	-	0.036	
Drain-source on-state resistance "	R <sub>DS(on)</sub>	$V_{GS} = 10 V$	I <sub>D</sub> = 12 A, T <sub>J</sub> = 175 °C	-	-	0.042	Ω
		$V_{GS} = 4.5 V$	I <sub>D</sub> = 8 A	-	0.026	0.032	1
Forward transconductance b	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 3 A		-	12	-	S
Dynamic <sup>b</sup>				•	•	•	
Input capacitance	Ciss			-	450	540	
Output capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 15 V, f = 1 MHz	-	95	125	pF
Reverse transfer capacitance	C <sub>rss</sub>			-	39	50	
Total gate charge <sup>c</sup>	Qg			-	8.15	14.5	nC
Gate-source charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5.5 A	-	1.65	-	
Gate-drain charge <sup>c</sup>	Q <sub>gd</sub>			-	1.25	-	
Gate resistance	Rg	f = 1 MHz		8.65	13	27	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>			-	6	7	
Rise time <sup>c</sup>	t <sub>r</sub>	$\label{eq:VDD} \begin{array}{l} V_{\text{DD}} = 15 \; V, \; R_{\text{L}} = 3.4 \; \Omega \\ I_{\text{D}} \cong 4.4 \; A, \; V_{\text{GEN}} = 10 \; V, \; R_{\text{g}} = 1 \; \Omega \end{array}$		-	4	12	- ns
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>			-	18	32	
Fall time <sup>c</sup>	t <sub>f</sub>			-	4	9	
Source-Drain Diode Ratings and Charact	eristics <sup>b</sup>						
Pulsed current <sup>a</sup>	I <sub>SM</sub>			-	-	32	А
Forward voltage	V <sub>SD</sub>	$I_{\rm F} = 3.5$ A, $V_{\rm GS} = 0$ V		-	0.81	1.2	V
Body diode reverse recovery time	t <sub>rr</sub>			-	10	20	ns
Body diode reverse recovery charge	Qrr			-	5	10	nC
Reverse recovery fall time	ta	$I_F = 4$	A, di/dt = 100A/us	-	7	-	-
Reverse recovery rise time	t <sub>b</sub>	1		-	3	-	ns
Body diode peak reverse recovery current	I <sub>RM(REC)</sub>			-	-0.98	-	А

Notes

a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$ 

b. Guaranteed by design, not subject to production testing

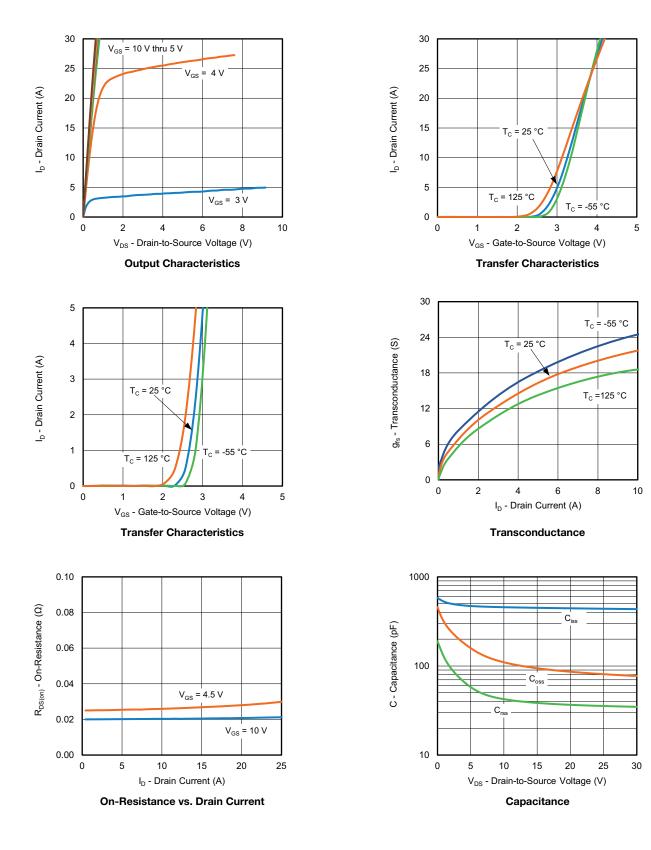
c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2



## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



S22-0592-Rev. A, 18-Jul-2022

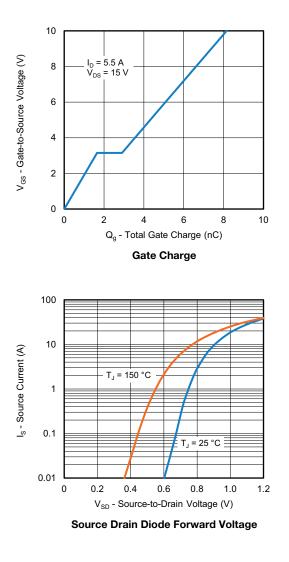
З

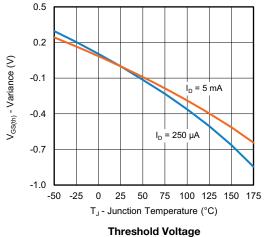
Document Number: 62082

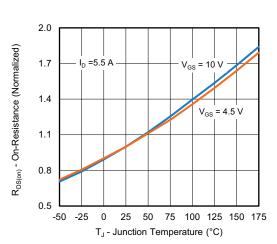
For technical questions, contact: <u>automostechsupport@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



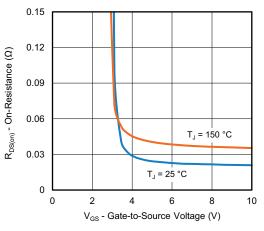
### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



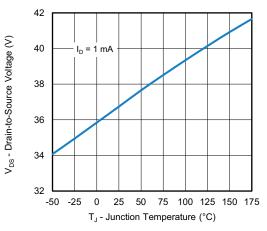




**On-Resistance vs. Junction Temperature** 



On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

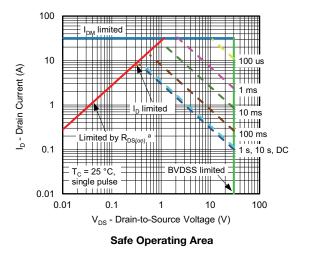
S22-0592-Rev. A, 18-Jul-2022

4

Document Number: 62082



### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

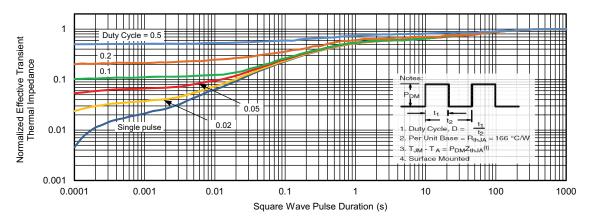


#### Note

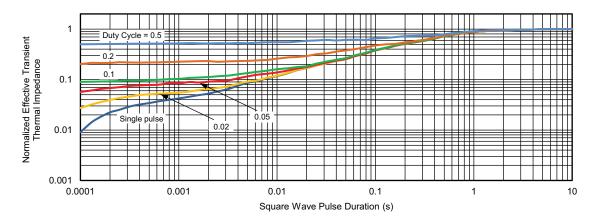
a.  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



#### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?62082">www.vishay.com/ppg?62082</a>.

S22-0592-Rev. A, 18-Jul-2022	6	Document Number: 62082
For tec	hnical questions, contact: automostechsupport@vishay.co	<u>om</u>
	HANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED	
ARE SUBJECT TO	SPECIFIC DISCLAIMERS SET FORTH AT www.vishav.cc	pm/doc?91000



# Package Information

Vishay Siliconix

## SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES			
	Min	Max	Min	Мах		
Α	0.89	1.12	0.035	0.044		
A <sub>1</sub>	0.01	0.10	0.0004	0.004		
A <sub>2</sub>	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E <sub>1</sub>	1.20	1.40	0.047	0.055		
е	0.95	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L <sub>1</sub>	0.6	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref			
q	3°	8°	3°	8°		



# Application Note 826

Vishay Siliconix

#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



Vishay

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.