



## P-Channel Enhancement Mode Power MOSFET

## **Description**

The RM2P60S2 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , This device is suitable for use as a load switch or in PWM applications.

#### **General Features**

•  $V_{DS} = -60V, I_{D} = -1.9A$ 

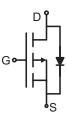
 $R_{DS(ON)} < 260 m\Omega$  @  $V_{GS}$ =-4.5V

 $R_{DS(ON)}$  <215m $\Omega$  @  $V_{GS}$ =-10V

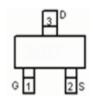
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

## **Application**

- PWM applications
- Load switch
- Power management
- Halogen-free
- P/N suffix V means AEC-Q101 qualified, e.g:RM2P60S2V



### Schematic diagram



Marking and pin Assignment



SOT-23 top view

## **Package Marking and Ordering Information**

<b>Device Marking</b>	Device	Device Package	Reel Size	Tape width	Quantity
2309	RM2P60S2	SOT23	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings (T<sub>A</sub>=25℃unless otherwise noted)

		<u> </u>				
Parame	Symbol	Limit	Unit			
Drain-Source Voltage	V <sub>DS</sub>	-60	V			
Gate-Source Voltage	Vgs	±20				
Drain Current-Continuous	Ta=25°C	I <sub>D</sub>	-1.9	А		
Brain Garrent Gornandous	Ta=70°C	I <sub>D</sub>	-1.5	А		
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	-7.6	А			
Maximum Dower Discipation	Ta=25℃	P <sub>D</sub>	1.4	W		
Maximum Power Dissipation	TA=70°C	P <sub>D</sub>	0.9	W		
Operating Junction and Storage Tem	$T_{J}, T_{STG}$	-55 To 150	°C			

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta,JA}$	90	°C/W
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# Electrical Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

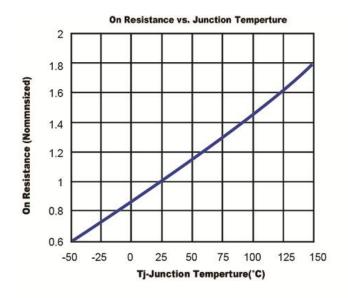
Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA		-	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V,V <sub>GS</sub> =0V	-	-	-1	μΑ	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250A	-1	-	-3	V	
Dunin Course On Chata Desistance	В	V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.8A	-	170	215	mΩ	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.4A	-	200	260	mΩ	
Dynamic Characteristics (Note4)	•			•			
Input Capacitance	C <sub>lss</sub>	\/ - 20\/\/ -0\/	-	358	-	pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-30V, $V_{GS}$ =0V, F=1.0MHz	-	23	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	- F-1.UIVIDZ	-	17	-	pF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t <sub>d(on)</sub>		-	20	-	ns	
Turn-on Rise Time	t <sub>r</sub>	$V_{DS}$ =-30V, $R_L$ =30 $\Omega$	-	33.1	-	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10V, $R_{GEN}$ =3.3 $\Omega$	-	5.2	-	ns	
Turn-Off Fall Time	t <sub>f</sub>	ID=-1A	-	3.8	-	ns	
Total Gate Charge	Qg		-	6.3	-	nC	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-48V,I <sub>D</sub> =-1A,V <sub>GS</sub> =-4.5V	-	2.3	-	nC	
Gate-Drain Charge	$Q_{gd}$	1	-	1.8	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =-1.2A	-	-	-1.2	V	

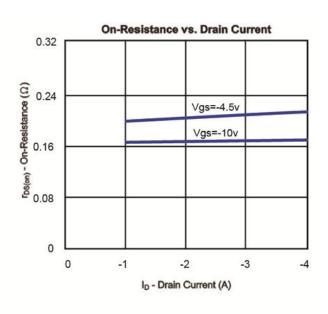
## Notes:

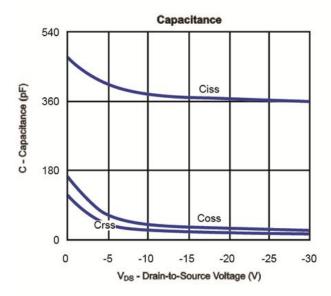
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

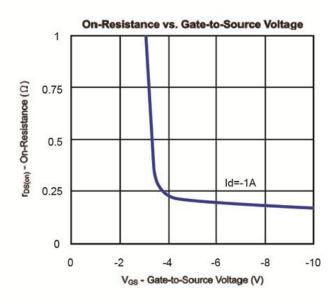


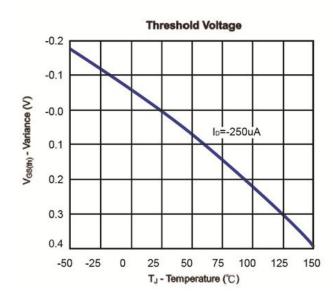
# RATING AND CHARACTERISTICS CURVES (RM2P60S2)

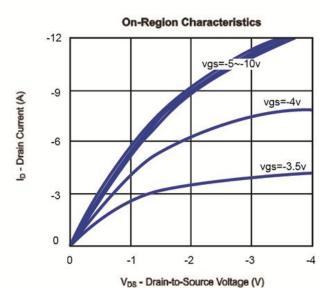






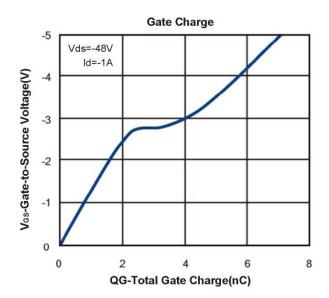


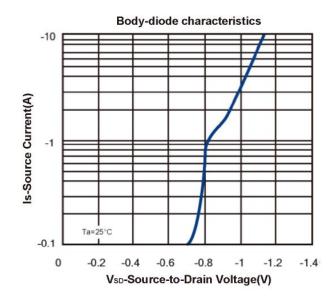


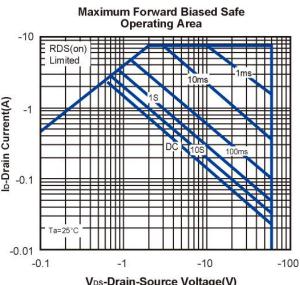


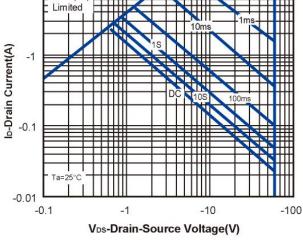


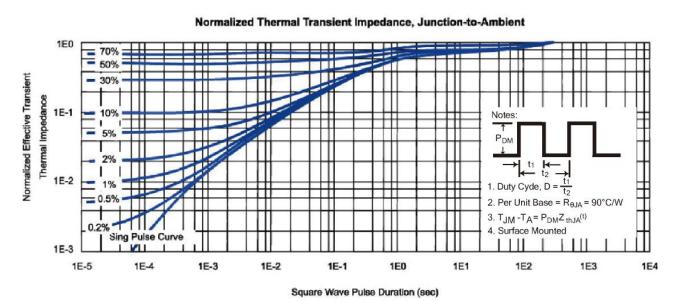
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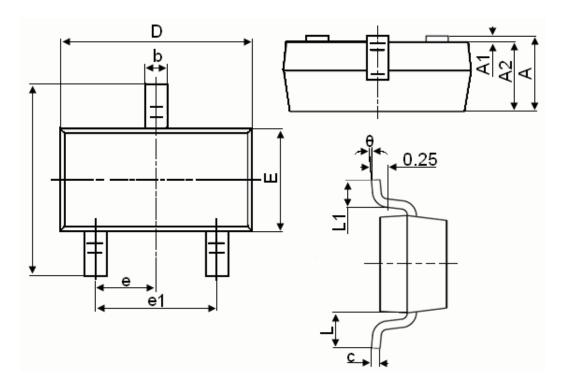








# **SOT-23 Package Information**



Symbol	Dimensions in Millimeters				
Symbol	MIN.	MAX.			
Α	0.900	1.150			
A1	0.000	0.100			
A2	0.900	1.050			
b	0.300	0.500			
С	0.080	0.150			
D	2.800	3.000			
E	1.200	1.400			
E1	2.250	2.550			
е		0.950TYP			
e1	1.800	2.000			
L	0.550REF				
L1	0.300	0.500			
θ	0°	8°			

### Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance  $\pm 0.10$ mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$



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