

N-Channel 30V (D-S) MOSFET , ESD Protected

GENERAL DESCRIPTION

The RM2306E is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits where high-side switching, and low in-line power loss are needed in a very small outline surface mount package.

FEATURES

- $RDS(ON) \leq 31m\Omega@VGS=10V$
- $RDS(ON) \leq 52m\Omega@VGS=4.5V$
- ESD Protected
- Super high density cell design for extremely low RDS(ON)
- Exceptional on-resistance and maximum DC current capability

APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Load Switch

Absolute Maximum Ratings (TA=25°C Unless Otherwise Noted)

Parameter		Symbol	Maximum Ratings	Unit	
Drain-Source Voltage		Vds	30	V	
Gate-Source Voltage		Vgss	±20	V	
Continuous Drain*	TA=25 ℃		5.3	A	
	TA=70°C	- טו	4.2		
Pulsed Drain Current		Ідм	21.2	А	
Maximum Power Dissipation*	Ta=25°C	Da	1.39	w	
	TA=70°C		0.89		
Operating Junction Temperature		TJ	-55 to 150	°C	
Storage Temperature Range		Tstg	-55 to 150	°C	
Thermal Resistance-Junction to Ambient*		Reja	90	°C /W	

*The device mounted on 1in² FR4 board with 2 oz copper

PIN CONFIGURATION



Symbol	Parameter	Limit	Min	Тур	Max	Unit	
STATIC							
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250 μ A	1	1.5	3	V	
lgss	Gate Leakage Current	VDS=0V, VGS=±16V			±10	μA	
IDSS	Zero Gate Voltage Drain Current	VDS=30V, VGS=0V			1	μA	
Rds(on)	Drain-Source On-Resistance ^a	Vgs=10V, Id= 6.7A		26	31	mO	
		Vgs=4.5V, Id= 5.0A			52	111.5.2	
Vsd	Diode Forward Voltage	Is=1.7A, Vgs=0V		0.8	1.2	V	
DYNAMIC							
Ciss	Input Capacitance			370		pF	
Coss	Output Capacitance	Vos=15V, Vos=0V, f=1MHZ		68			
Crss	Reverse Transfer Capacitance			21			
Rg	Gate Resistance	f=1MHz		1.9		Ω	
Qg	Total Gate Charge	VDS=15V, VGS=10V, ID=6.7A		12		- nC	
Qg	Total Gate Charge			5.7			
Qgs	Gate-Source Charge	VDS=15V, VGS=4.5V, ID=6.7A		3.0			
Qgd	Gate-Drain Charge			2.1			
td(on)	Turn-On Delay Time	1/22 = 15 1/10 = 15 0		9.2		- ns	
tr	Turn-On Rise Time	100-100, KL = 1002		13			
td(off)	Turn-Off Delay Time	$\mathbf{B}_{c}=60$		33			
tr	Turn-Off Fall Time	110-012		3.7			

Electrical Characteristics (TA = 25°C Unless Otherwise Specified)

Notes: a. Pulse test: pulse width \leq 300us, duty cycle \leq 2%, Guaranteed by design, not subject to production testing.

b. Matsuki Electric/ Force mos reserves the right to improve product design, functions and reliability without notice.





RATING AND CHARACTERISTICS CURVES (RM2306E)

CRECTRON

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Normalized Thermal Transient Impedance, Junction-to-Ambient



ым	MILLIMETERS (mm)			
DIN	MIN	MAX		
Α	2.800	3.00		
В	1.200	1.70		
С	0.900	1.30		
D	0.350	0.50		
G	1.780	2.04		
н	0.010	0.15		
J	0.085	0.20		
К	0.300	0.65		
L	0.890	1.02		
S	2.100	3.00		
v	0.450	0.60		





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Package	Tube	Tube	Tube	Tape&Reel	Tape&Reel	Tape&Reel
	(pcs/tube)	(pcs/inner box)	(pcs/cartoon)	(pcs/reel)	(pcs/inner box)	(pcs/cartoon)
DFN	100	10,000	100,000	2,500	5,000	40,000
SOP-8	100	10,000	100,000	4,000	4,000	20,000
TSSOP-8	100	32,000	128,000	3,000	6,000	48,000
SOT-23-3L				3,000	30,000	120,000
SOT-23-6L				3,000	30,000	120,000
SOT-23(6R)			_	3,000	30,000	120,000
SOT-363				3,000	30,000	120,000
SOT-523			_	3,000	30,000	120,000
SOT223				2,500	2,500	20,000
TO-220	50	1,000	5,000			
TO-220F	50	1,000	10,000			
TO-247	30	300	1,200			_
TO-251	80	4,000	40,000			
TO-251S(4R)	80	4,000	40,000			
TO-252-2L(4R)	80	4,000	40,000	2,500	2,500	25,000
TO-263-2L	50	1,000	10,000	800	800	8,000
TO-3P	30	300	3,000			
TO-92				1,000(袋装)	10,000	100,000

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