

## P-ChannelEnhancement Mode Power MOSFET

#### **Description**

**General Features** 

•  $V_{DS} = -20V, I_{D} = -4.1A$ 

 $R_{DS(ON)}$  < 95 m $\Omega$  @  $V_{GS}$ =-1.8V

 $R_{DS(ON)}$  < 75m $\Omega$  @  $V_{GS}$ =-2.5V $R_{DS(ON)}$  < 52 m $\Omega$  @  $V_{GS}$ =-4.5V

Lead free product is acquired

Surface mount package

High power and current handing capability

The RM2305B uses advanced trench technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

#### Schematic diagram





#### Marking and pin assignment

# **Application**

- PWM applications
- Load switch
- Power management
- Halogen-free
- P/N suffix V means AEC-Q101 qualified, e.g:RM2305BV



SOT-23 top view

## **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
A5SHB	RM2305B	SOT-23	Ø180mm	8 mm	3000 units

## Absolute Maximum Ratings (T<sub>A</sub>=25℃unless otherwise noted)

Paramete	Symbol	Limit	Unit	
Drain-Source Voltage	VDS	-20	V	
Gate-Source Voltage	Vgs	±12	V	
Continuous Drain Current	T <sub>C</sub> =25°C	- I <sub>D</sub>	-4.1	
	T <sub>C</sub> =70°C		-3.2	_
	T <sub>A</sub> =25°C		-3	A
	T <sub>A</sub> =70°C		-2.3	
Drain Current -Pulsed (Note 1)	I <sub>DM</sub>	-15	А	
Maximum Power Dissipation	P <sub>D</sub>	1.7	W	
Operating Junction and Storage Temper	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$	

## **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	74	°C/W

## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

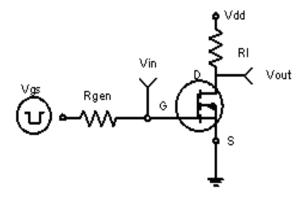
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-20	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-0.45	-0.7	-1.0	V
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4.1A	-	39	52	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3A	-	58	75	mΩ
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-2A	-	88	95	
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-2A	6	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	- V <sub>DS</sub> =-4V,V <sub>GS</sub> =0V,	-	740	-	PF
Output Capacitance	Coss	V <sub>DS</sub> 4V,V <sub>GS</sub> 0V, F=1.0MHz	-	290	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	- F-1.0WI12	-	190	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	12	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-4V, $I_{D}$ =-3.3A ,	-	35	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_L$ =-1.2 $\Omega$ , $V_{GEN}$ =-4.5 $V$ , $R_g$ =1 $\Omega$	-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS
Total Gate Charge	Qg		-	7.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-4V,I <sub>D</sub> =-4.1A,V <sub>GS</sub> =-4.5V	-	1.2	-	nC
Gate-Drain Charge	$Q_{gd}$		-	1.6	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-1.6A	_	-	-1.2	V
Diode Forward Current (Note 2)	Is		-	-	1.6	Α

## Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production



# RATING AND CHARACTERISTICS CURVES (RM2305B)



**Figure 1:Switching Test Circuit** 

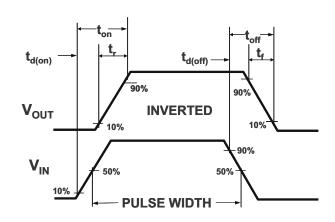


Figure 2:Switching Waveforms

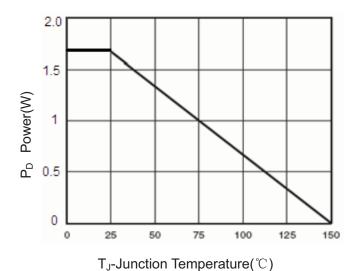
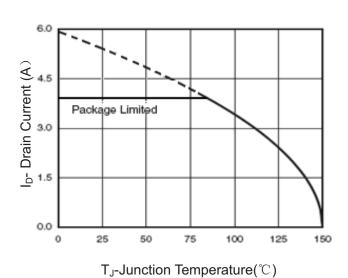
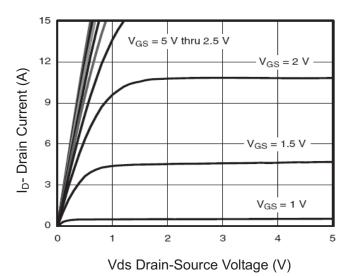


Figure 3 Power Dissipation



**Figure 4 Drain Current** 



**Figure 5 Output Characteristics** 

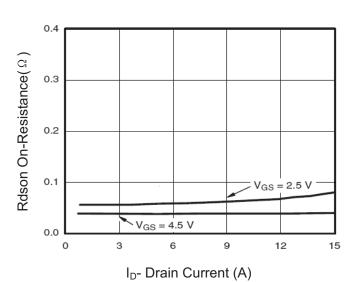
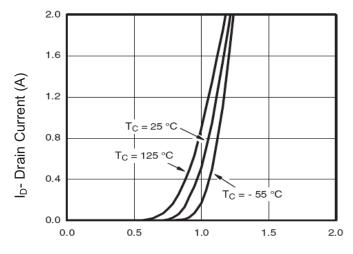


Figure 6 Drain-Source On-Resistance

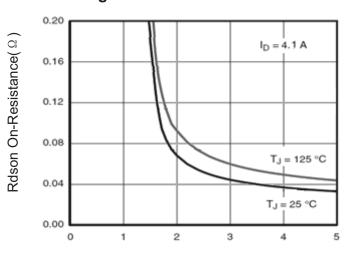


## RATING AND CHARACTERISTICS CURVES (RM2305B)



Vgs Gate-Source Voltage (V)





Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

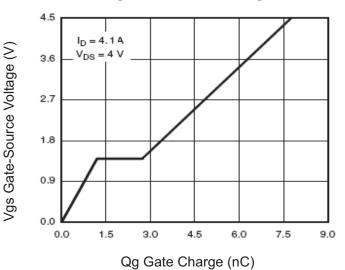


Figure 11 Gate Charge

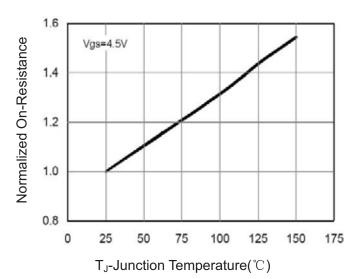


Figure 8 Drain-Source On-Resistance

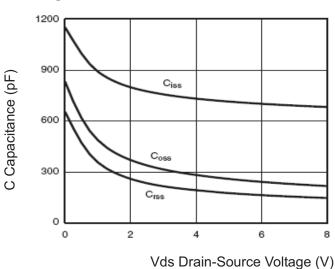
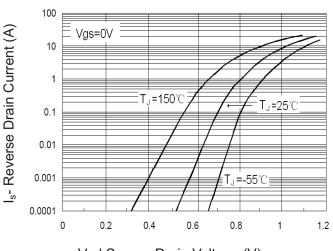


Figure 10 Capacitance vs Vds

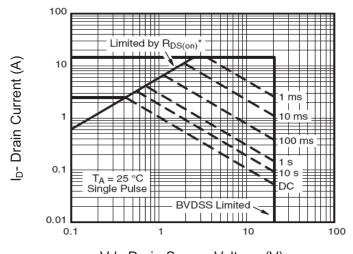


Vsd Source-Drain Voltage (V)

Figure 12 Source- Drain Diode Forward



## RATING AND CHARACTERISTICS CURVES (RM2305B)



Vds Drain-Source Voltage (V)

**Figure 13 Safe Operation Area** 

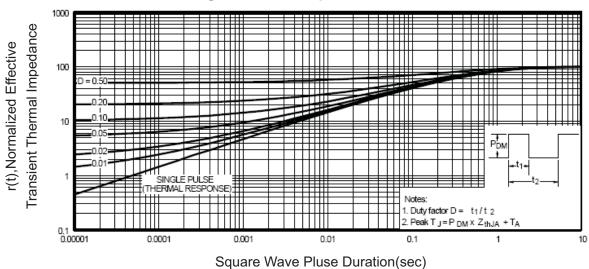
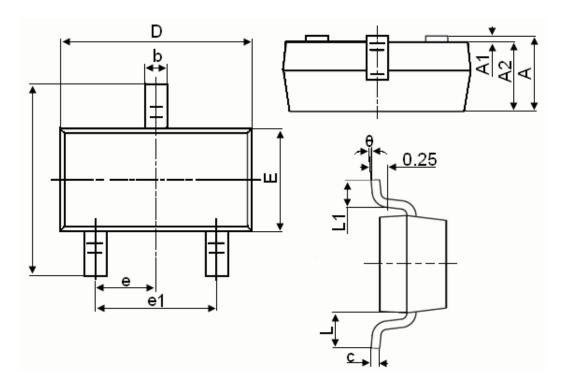


Figure 14 Normalized Maximum Transient Thermal Impedance



## **SOT-23 Package Information**



Symbol	Dimensions in Millimeters				
Symbol	MIN.	MAX.			
А	0.900	1.150			
A1	0.000	0.100			
A2	0.900	1.050			
b	0.300	0.500			
С	0.080	0.150			
D	2.800	3.000			
E	1.200	1.400			
E1	2.250	2.550			
е	0.950TYP				
e1	1.800	2.000			
L	0.550REF				
L1	0.300	0.500			
θ	0°	8°			

#### Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$



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