



N-Channel Enhancement Mode Power MOSFET

Description

The RM10N100S8 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

V_{DS} = 100V,I_D =10A
R_{DS(ON)} < 14mΩ @ V_{GS}=10V
R_{DS(ON)} < 21mΩ @ V_{GS}=4.5V

- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current

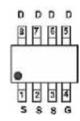
Package Marking and Ordering Information

Application

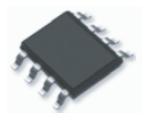
- DC/DC Primary Side Switch
- Telecom/Server
- Synchronous Rectification
- Halogen-free

G S

Schematic diagram



Marking and pin assignment



SOP-8 top view

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
10N100	RM10N100S8	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	100	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι _D	10	A
Drain Current-Continuous(Tc=100℃)	I _D (100℃)	7	A
Pulsed Drain Current	I _{DM}	70	A
Maximum Power Dissipation	PD	3.1	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C
hermal Characteristic		·	
(Note 2)	_		0.0.0.0

Inermal Resistance, Junction-to-Ambient (1997) Reja 40 C/W	Thermal Resistance, Junction-to-Ambient ^(Note 2)	R _{θJA}	40	°C /W
--	---	------------------	----	--------------

Electrical Characteristics (T_A=25 $^\circ\!\mathrm{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V		-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note2)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.0	1.5	2.5	V
Durain Course On State Desistence		V _{GS} =10V, I _D =10A	-	12	14	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =10A	-	18	21	mΩ
Forward Transconductance	g Fs	V _{DS} =10V,I _D =10A	-	10	-	S
Dynamic Characteristics (Note3)	I	1		,		
Input Capacitance	C _{lss}		-	1640	-	PF
Output Capacitance	C _{oss}	V _{DS} =50V,V _{GS} =0V, F=1.0MHz	-	240	-	PF
Reverse Transfer Capacitance	Crss		-	4	-	PF
Switching Characteristics (Note 3)	I	1				
Turn-on Delay Time	t _{d(on)}		-	14.2	-	nS
Turn-on Rise Time	tr	V_{DD} =50V,I _D =1A,R _L =6Ω,	-	20.8	-	nS
Turn-Off Delay Time	t _{d(off)}	R _G =1Ω,V _{GS} =10V	-	42	-	nS
Turn-Off Fall Time	t _f		-	30	-	nS
Total Gate Charge	Qg		-	27.8	-	nC
Gate-Source Charge	Q _{gs}	I _D =10A,V _{DD} =50V,V _{GS} =10V	-	3.5	-	nC
Gate-Drain Charge	Q _{gd}		-	8.8	-	nC
Drain-Source Diode Characteristics		1				
Diode Forward Voltage (Note 2)	V _{SD}	V _{GS} =0V,I _S =10A	-	-	1.0	V

Notes:

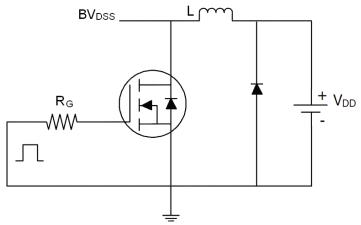
1. Repetitive Rating: Pulse width limited by maximum junction temperature. 2. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

3. Guaranteed by design, not subject to production

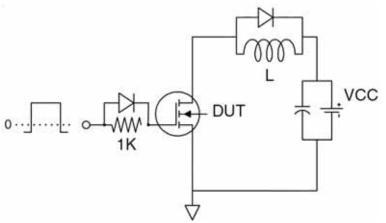


Test Circuit

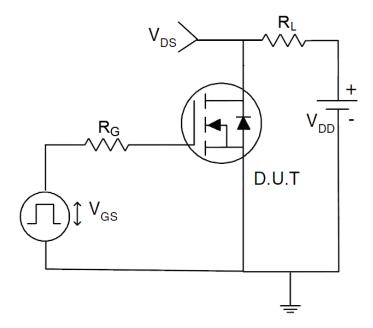
1) E_{AS} test Circuit



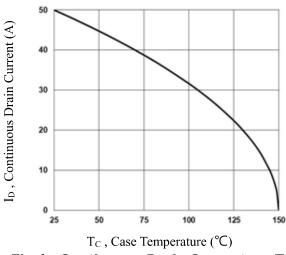
2) Gate charge test Circuit



3) Switch Time Test Circuit



CRECTRON -





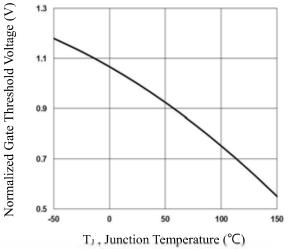


Fig.3 Normalized Vth vs. T

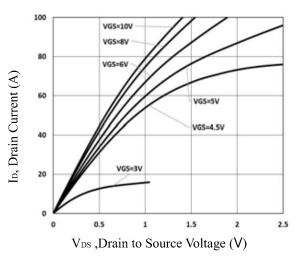


Fig.5 Typical Output Characteristics

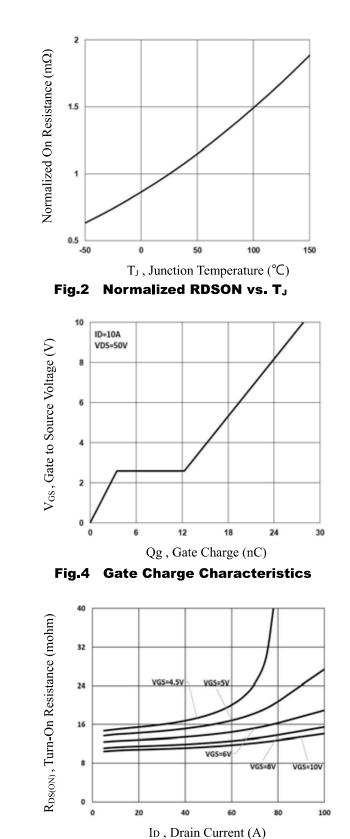
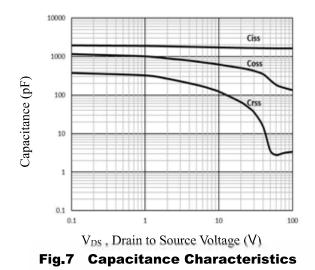


Fig.6 Turn-On Resistance vs. ID



RATING AND CHARACTERISTICS CURVES (RM10N100S8)



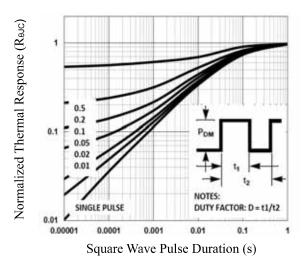


Fig.8 Normalized Transient Impedance

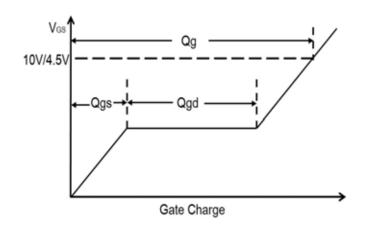


Fig.11 Gate Charge Waveform

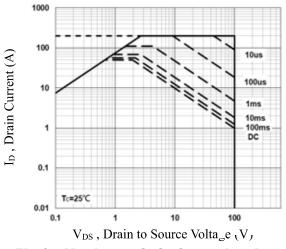


Fig.9 Maximum Safe Operation Area

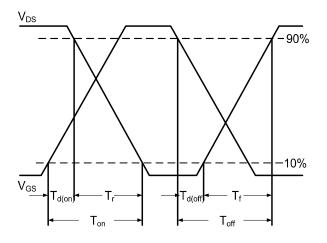
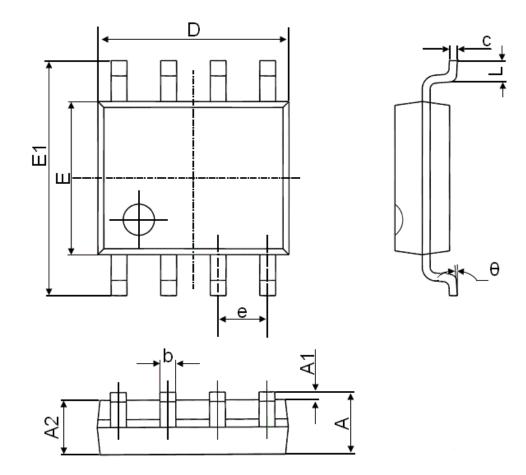


Fig.10 Switching Time Waveform



SOP-8 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max. Min.	Max.		
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



DISCLAIMER NOTICE

Rectron Inc reserves the right to make changes without notice to any product specification herein, to make corrections, modifications, enhancements or other changes. Rectron Inc or anyone on its behalf assumes no responsibility or liability for any errors or inaccuracies. Data sheet specifications and its information contained are intended to provide a product description only. "Typical" parameters which may be included on RECTRON data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. Rectron Inc does not assume any liability arising out of the application or use of any product or circuit.

Rectron products are not designed, intended or authorized for use in medical, life-saving implant or other applications intended for life-sustaining or other related applications where a failure or malfunction of component or circuitry may directly or indirectly cause injury or threaten a life without expressed written approval of Rectron Inc. Customers using or selling Rectron components for use in such applications do so at their own risk and shall agree to fully indemnify Rectron Inc and its subsidiaries harmless against all claims, damages and expenditures.

