



# N-Channel Enhancement Mode Power MOSFET

### Description

The RM10N100S8 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### **General Features**

V<sub>DS</sub> = 100V,I<sub>D</sub> =10A
R<sub>DS(ON)</sub> < 14mΩ @ V<sub>GS</sub>=10V
R<sub>DS(ON)</sub> < 21mΩ @ V<sub>GS</sub>=4.5V

- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current

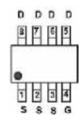
Package Marking and Ordering Information

### Application

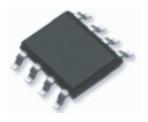
- DC/DC Primary Side Switch
- Telecom/Server
- Synchronous Rectification
- Halogen-free

# G S

Schematic diagram



Marking and pin assignment



SOP-8 top view

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
10N100	RM10N100S8	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	100	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι <sub>D</sub>	10	A
Drain Current-Continuous(Tc=100℃)	I <sub>D</sub> (100℃)	7	A
Pulsed Drain Current	I <sub>DM</sub>	70	A
Maximum Power Dissipation	PD	3.1	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	°C
hermal Characteristic		·	
(Note 2)	_		0.0.0.0

Inermal Resistance, Junction-to-Ambient (1997) Reja 40 C/W	Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	R <sub>θJA</sub>	40	°C <b>/W</b>
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# Electrical Characteristics (T\_A=25 $^\circ\!\mathrm{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	100	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V,V <sub>GS</sub> =0V		-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note2)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0	1.5	2.5	V
Durain Course On State Desistence		V <sub>GS</sub> =10V, I <sub>D</sub> =10A	-	12	14	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	18	21	mΩ
Forward Transconductance	<b>g</b> Fs	V <sub>DS</sub> =10V,I <sub>D</sub> =10A	-	10	-	S
Dynamic Characteristics (Note3)	I	1		,		
Input Capacitance	C <sub>lss</sub>		-	1640	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V, F=1.0MHz	-	240	-	PF
Reverse Transfer Capacitance	Crss		-	4	-	PF
Switching Characteristics (Note 3)	I	1				
Turn-on Delay Time	t <sub>d(on)</sub>		-	14.2	-	nS
Turn-on Rise Time	tr	$V_{DD}$ =50V,I <sub>D</sub> =1A,R <sub>L</sub> =6Ω,	-	20.8	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> =1Ω,V <sub>GS</sub> =10V	-	42	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	30	-	nS
Total Gate Charge	Qg		-	27.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>	I <sub>D</sub> =10A,V <sub>DD</sub> =50V,V <sub>GS</sub> =10V	-	3.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	8.8	-	nC
Drain-Source Diode Characteristics		1				
Diode Forward Voltage (Note 2)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =10A	-	-	1.0	V

### Notes:

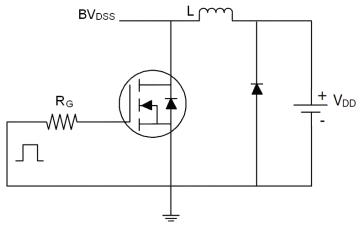
1. Repetitive Rating: Pulse width limited by maximum junction temperature. 2. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.

3. Guaranteed by design, not subject to production

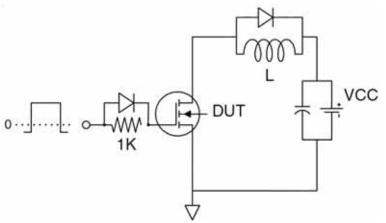


## **Test Circuit**

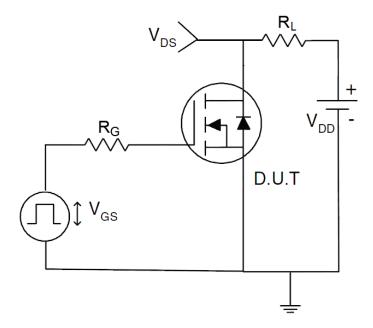
1) E<sub>AS</sub> test Circuit



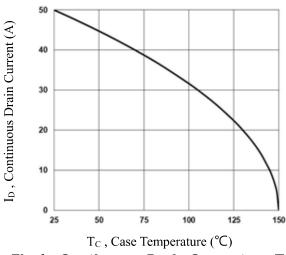
2) Gate charge test Circuit



3) Switch Time Test Circuit



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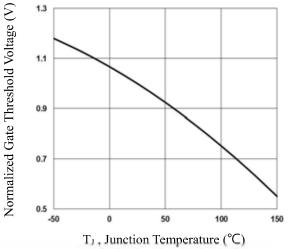


Fig.3 Normalized Vth vs. T

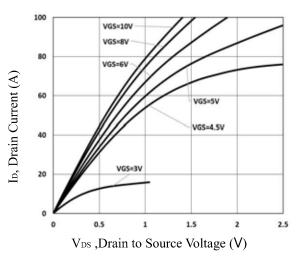


Fig.5 Typical Output Characteristics

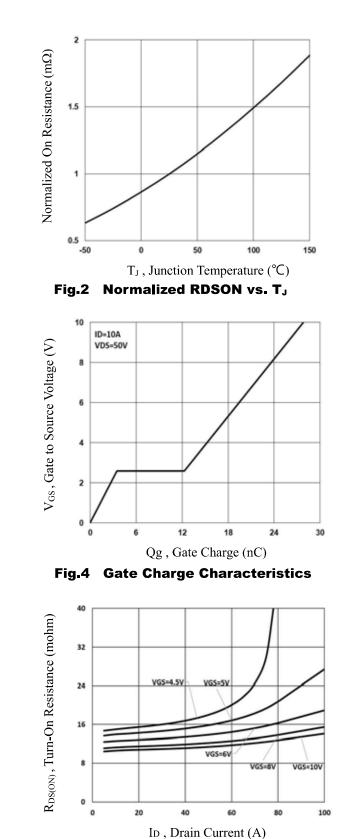
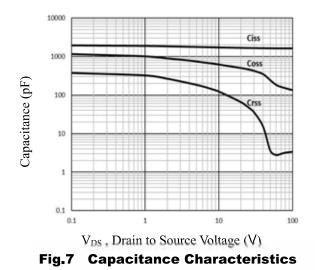


Fig.6 Turn-On Resistance vs. ID



# **RATING AND CHARACTERISTICS CURVES (RM10N100S8)**



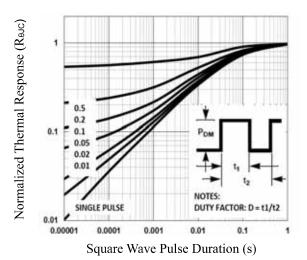


Fig.8 Normalized Transient Impedance

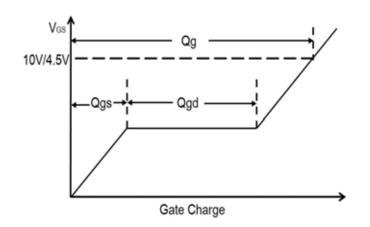


Fig.11 Gate Charge Waveform

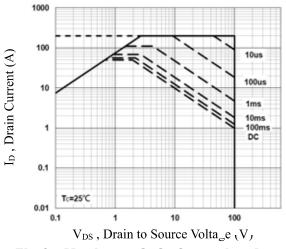


Fig.9 Maximum Safe Operation Area

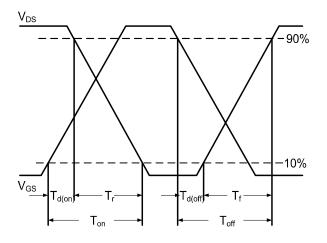
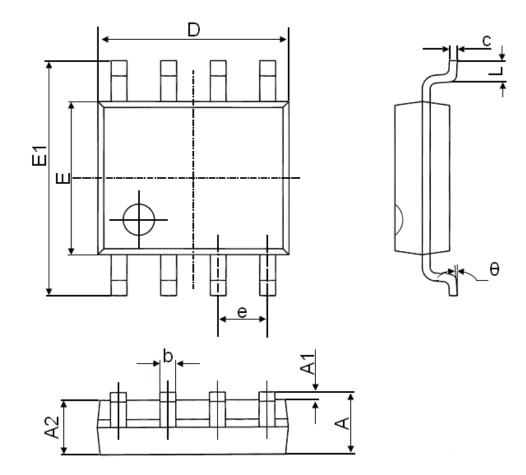


Fig.10 Switching Time Waveform



# SOP-8 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max. Min.	Max.		
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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