

# NSS12501UW3T2G

## 12 V, 7.0 A, Low $V_{CE(sat)}$ NPN Transistor

ON Semiconductor's e<sup>2</sup>PowerEdge family of low  $V_{CE(sat)}$  transistors are miniature surface mount devices featuring ultra low saturation voltage ( $V_{CE(sat)}$ ) and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical applications are DC-DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e<sup>2</sup>PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

### Features

- This is a Pb-Free Device

### MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	$V_{CEO}$	12	Vdc
Collector-Base Voltage	$V_{CBO}$	12	Vdc
Emitter-Base Voltage	$V_{EBO}$	6.0	Vdc
Collector Current - Continuous	$I_C$	5.0	Adc
Collector Current - Peak	$I_{CM}$	7.0	A
Electrostatic Discharge	ESD	HBM Class 3B MM Class C	

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$ (Note 1)	875 7.0	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$ (Note 1)	143	$^\circ\text{C}/\text{W}$
Total Device Dissipation, $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$ (Note 2)	1.5 11.8	W mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$ (Note 2)	85	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Lead #1	$R_{\theta JL}$ (Note 2)	23	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

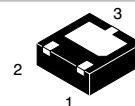
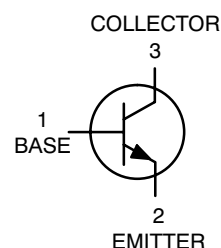
1. FR-4 @ 100 mm<sup>2</sup>, 1 oz copper traces.
2. FR-4 @ 500 mm<sup>2</sup>, 1 oz copper traces.



ON Semiconductor®

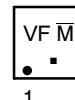
<http://onsemi.com>

## 12 VOLTS, 7.0 AMPS NPN LOW $V_{CE(sat)}$ TRANSISTOR EQUIVALENT $R_{DS(on)}$ 31 m $\Omega$



WDFN3  
CASE 506AU

### MARKING DIAGRAM



VF = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

Device	Package	Shipping†
NSS12501UW3T2G	WDFN3 (Pb-Free)	3000/ Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NSS12501UW3T2G

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typical	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	12	-	-	Vdc
Collector-Base Breakdown Voltage (I <sub>C</sub> = 0.1 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	12	-	-	Vdc
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 0.1 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	6.0	-	-	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 12 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	-	0.1	μAdc
Emitter Cutoff Current (V <sub>EB</sub> = 6.0 Vdc)	I <sub>EBO</sub>	-	-	0.1	μAdc

## ON CHARACTERISTICS

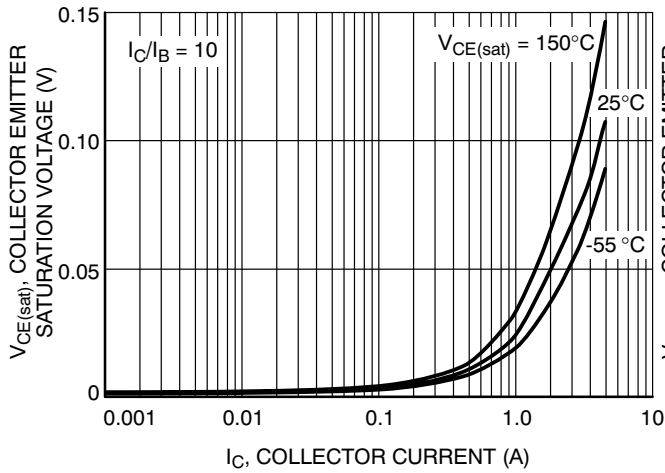
DC Current Gain (Note 3) (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 2.0 V) (I <sub>C</sub> = 500 mA, V <sub>CE</sub> = 2.0 V) (I <sub>C</sub> = 1.0 A, V <sub>CE</sub> = 2.0 V) (I <sub>C</sub> = 2.0 A, V <sub>CE</sub> = 2.0 V) (I <sub>C</sub> = 3.0 A, V <sub>CE</sub> = 2.0 V)	h <sub>FE</sub>	200 200 200 200 200	- - 345 330 315	- - - - -	
Collector-Emitter Saturation Voltage (Note 3) (I <sub>C</sub> = 0.1 A, I <sub>B</sub> = 0.010 A) (I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 0.100 A) (I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 0.010 A) (I <sub>C</sub> = 2.0 A, I <sub>B</sub> = 0.020 A) (I <sub>C</sub> = 3.0 A, I <sub>B</sub> = 0.030 A) (I <sub>C</sub> = 4.0 A, I <sub>B</sub> = 0.400 A)	V <sub>CE(sat)</sub>	- - - - - -	0.007 0.031 0.045 0.070 0.100 0.100	0.008 0.035 0.060 0.100 0.120 0.120	V
Base-Emitter Saturation Voltage (Note 3) (I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 0.01 A)	V <sub>BE(sat)</sub>	-	0.760	0.900	V
Base-Emitter Turn-on Voltage (Note 3) (I <sub>C</sub> = 2.0 A, V <sub>CE</sub> = 2.0 V)	V <sub>BE(on)</sub>	-	0.730	0.900	V
Cutoff Frequency (I <sub>C</sub> = 100 mA, V <sub>CE</sub> = 5.0 V, f = 100 MHz)	f <sub>T</sub>	150	-	-	MHz
Input Capacitance (V <sub>EB</sub> = 0.5 V, f = 1.0 MHz)	C <sub>ibo</sub>	-	-	650	pF
Output Capacitance (V <sub>CB</sub> = 3.0 V, f = 1.0 MHz)	C <sub>obo</sub>	-	-	120	pF

## SWITCHING CHARACTERISTICS

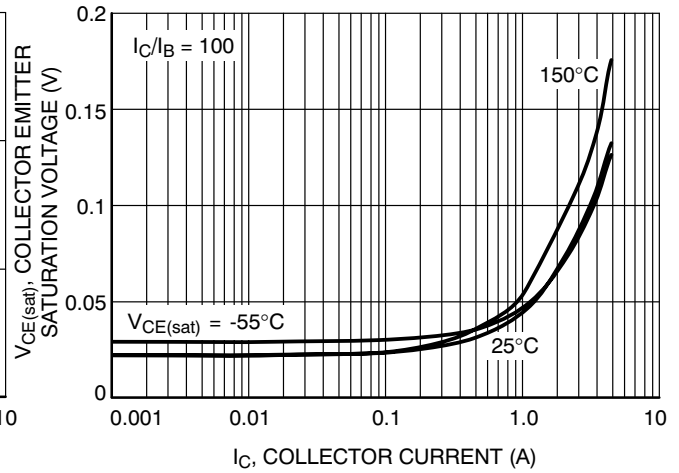
Delay (V <sub>CC</sub> = 10 V, I <sub>C</sub> = 750 mA, I <sub>B1</sub> = 15 mA)	t <sub>d</sub>	-	-	90	ns
Rise (V <sub>CC</sub> = 10 V, I <sub>C</sub> = 750 mA, I <sub>B1</sub> = 15 mA)	t <sub>r</sub>	-	-	100	ns
Storage (V <sub>CC</sub> = 10 V, I <sub>C</sub> = 750 mA, I <sub>B1</sub> = 15 mA)	t <sub>s</sub>	-	-	320	ns
Fall (V <sub>CC</sub> = 10 V, I <sub>C</sub> = 750 mA, I <sub>B1</sub> = 15 mA)	t <sub>f</sub>	-	-	100	ns

3. Pulsed Condition: Pulse Width = 300 μsec, Duty Cycle ≤ 2%.

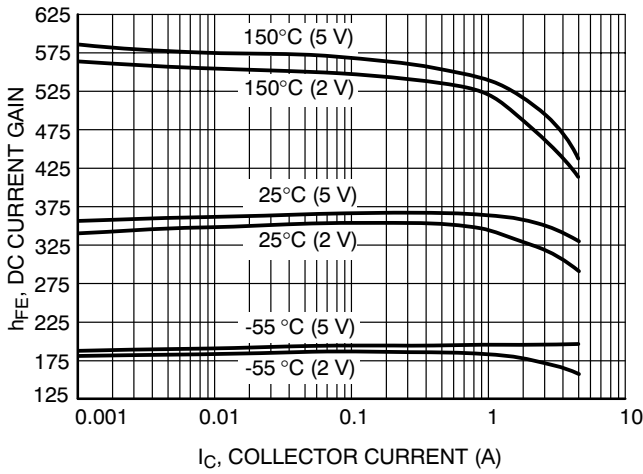
# NSS12501UW3T2G



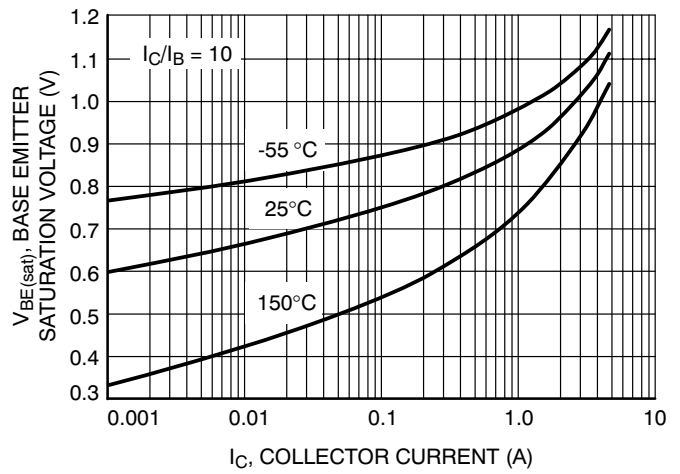
**Figure 1. Collector Emitter Saturation Voltage vs. Collector Current**



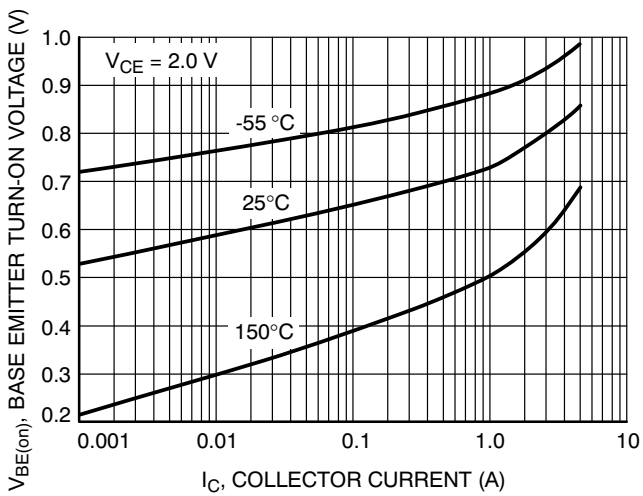
**Figure 2. Collector Emitter Saturation Voltage vs. Collector Current**



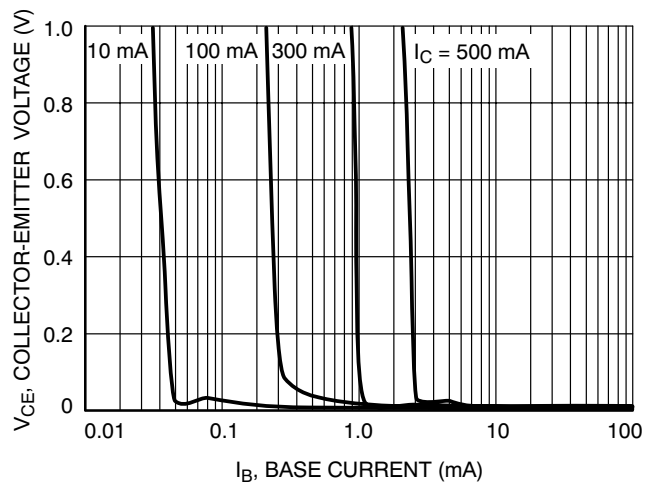
**Figure 3. DC Current Gain vs. Collector Current**



**Figure 4. Base Emitter Saturation Voltage vs. Collector Current**

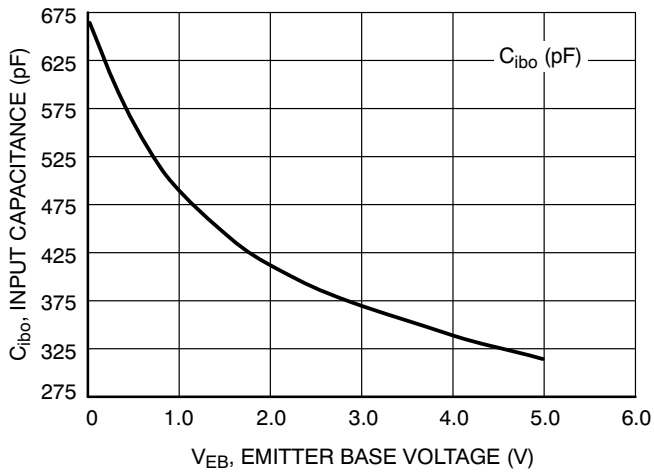


**Figure 5. Base Emitter Turn-On Voltage vs. Collector Current**

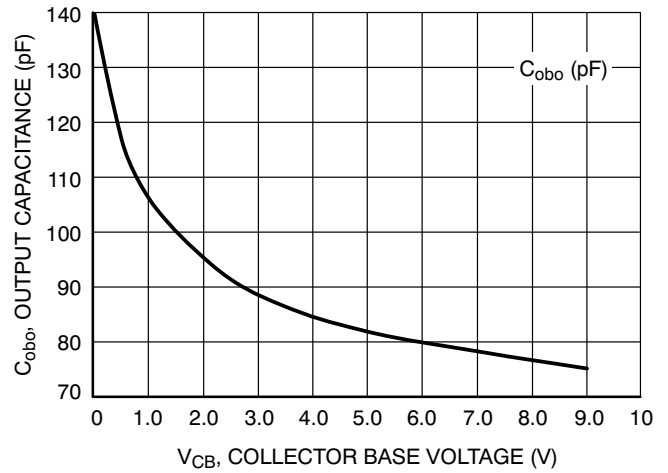


**Figure 6. Saturation Region**

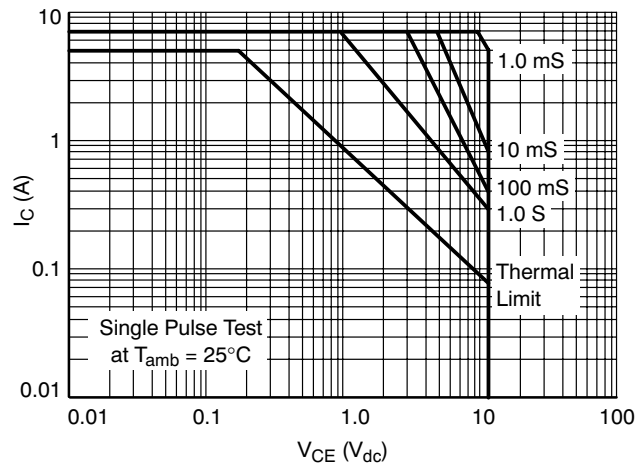
# NSS12501UW3T2G



**Figure 7. Input Capacitance**



**Figure 8. Output Capacitance**



**Figure 9. Safe Operating Area**

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®



### WDFN3 2x2, 1.3P CASE 506AU ISSUE A

DATE 18 AUG 2016



SCALE 4:1



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994 .
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM TERMINAL.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.75	0.80	0.028	0.030	0.031
A1	0.00		0.05	0.000		0.002
A3		0.20 REF			0.008 REF	
b	0.25	0.30	0.35	0.010	0.012	0.014
D		2.00 BSC			0.079 BSC	
D2	1.40	1.50	1.60	0.055	0.059	0.063
E		2.00 BSC			0.079 BSC	
E2	0.90	1.00	1.10	0.035	0.039	0.043
e		1.30 BSC			0.051 BSC	
K		0.35 REF			0.014 REF	
L	0.35	0.40	0.45	0.014	0.016	0.018

### GENERIC MARKING DIAGRAM\*

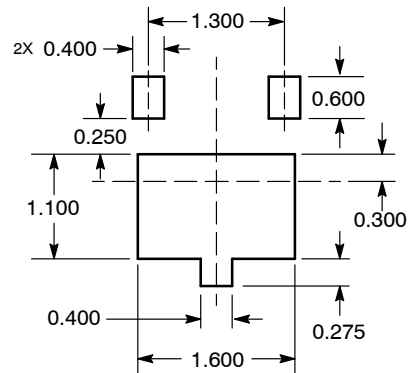


XX = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot "•", may or may not be present.

### SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

<b>DOCUMENT NUMBER:</b>	<b>98AON21416D</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>WDFN3 2X2, 1.3P</b>	<b>PAGE 1 OF 1</b>

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at [www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)