

The MPC94551 is a CMOS 1:4 fanout buffer. The MPC94551 is ideal for applications requiring lower voltage.

Features

- 1:4 CMOS fanout buffer
- 300 ps output to output skew
- I/O frequency up to 160 MHz operation
- Non-inverting output clock
- 3.3 V supply voltage
- Output Enable mode tri-states outputs
- -40°C to 85°C industrial temperature range
- 8-lead SOIC package, Pb-free
- For drop in replacement use 551MI

MPC94551

1:4 LVC MOS CLOCK BUFFER



EF SUFFIX
8-LEAD SOIC PACKAGE
Pb-FREE PACKAGE
CASE 751-06

ORDERING INFORMATION

Device	Package
MPC94551EF	SO-8 (Pb-FREE)
MPC94551EFR2	SO-8 (Pb-FREE)

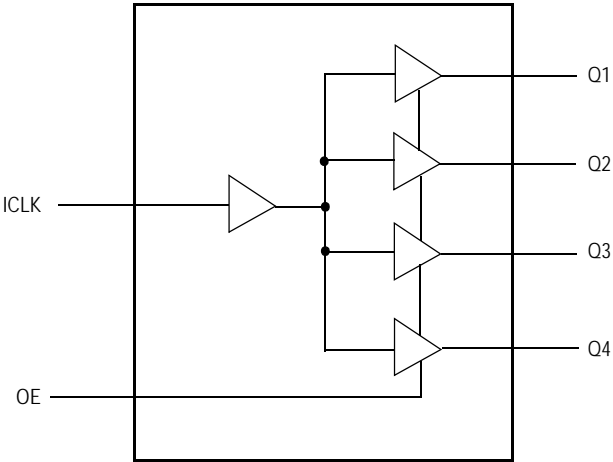


Figure 1. Logic Diagram

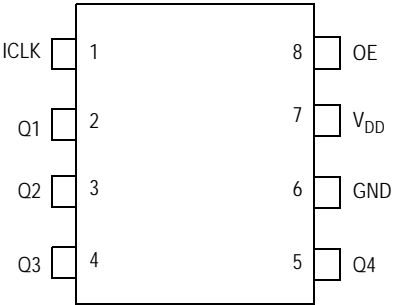


Figure 2. Pin Assignment

Table 4. AC Characteristics ($V_{DD} = 3.3 \text{ V} \pm 5\%$; Ambient Temperature = -40°C to 85°C)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Input Frequency			0		160	MHz
Output Frequency ⁽¹⁾		15 pF load			160	MHz
Output Clock Rise Time	t_{OR}	0.8 V to 2.0 V			1.5	ns
Output Clock Fall Time	t_{OF}	2.0 V to 0.8 V			1.5	ns
Propagation Delay ⁽²⁾		135 MHz	1.5	4	5	ns
Output to Output Skew ⁽³⁾		Rising edges at $V_{DD}/2$			300	ps

1. Measured with an external series resistor of 33Ω positioned close to each output pin
2. Measured with rail to rail input clock
3. Measured between any 2 outputs with equal loading

Revision History Sheet

Rev	Table	Page	Description of Change	Date
4		1	NRND – Not Recommend for New Designs	12/21/12
4		1	Removed NRND	5/5/15
4		1	Product Discontinuation Notice - Last time buy expires September 7, 2016. PDN N-16-02	3/15/16

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