## HDU, HDE Series

Vishay Roederstein

RoHS

COMPLIAN<sup>®</sup>

### Ceramic Singlelayer DC Disc Capacitors, 4 kV<sub>DC</sub> General Purpose



www.vishay.com

QUICK REFERENCE DATA				
DESCRIPTION	VALUE			
Ceramic Class	1	2		
Ceramic Dielectric	N750, Y5U			
Voltage (V <sub>DC</sub> )	4000			
Min. Capacitance (pF)	10	33		
Max. Capacitance (pF)	470	4700		
Mounting	Radial			

### **OPERATING TEMPERATURE RANGE**

-40 °C to +85 °C <sup>(1)</sup>

#### Note

(1) For explanation about the difference of operating temperature range and temperature characteristic of capacitance please see <u>www.vishay.com/doc?48299</u>

### **TEMPERATURE CHARACTERISTICS**

Class 1: N750 Class 2: Y5U

### SECTIONAL SPECIFICATIONS

Climatic category (according to EN 60068-1): 40 / 085 / 21

### FEATURES

- High capacitance in small sizes
- Low losses
- Wide range of different lead styles
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### APPLICATIONS

- Lighting ballasts
- Switching power supplies
- Bypassing, coupling and decoupling
- DC blocking

### DESIGN

The capacitors consist of a ceramic disc which is silver plated on both sides. Connection leads are made of tinned copper having diameters of 0.6 mm or 0.8 mm.

The capacitors may be supplied with straight or kinked leads having a lead spacing of 10.0 mm or 12.5 mm.

Coating is made of blue colored flame retardant epoxy resin in accordance with UL 94 V-0.

### CAPACITANCE RANGE

10 pF to 4.7 nF

### RATED VOLTAGE

4 kV<sub>DC</sub>

### **DIELECTRIC STRENGTH**

6000 V<sub>DC</sub>, 2 s Component test

### INSULATION RESISTANCE AT 500 VDC

 $\geq$  10 000 M $\Omega$  (60 s)

### **TOLERANCE ON CAPACITANCE**

± 10 %, ± 20 %

### **DISSIPATION FACTOR**

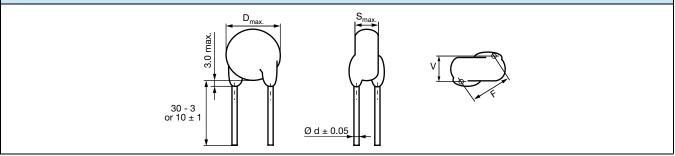
Class 1: C < 30 pF:  $\left(\frac{100 \text{ pF}}{\text{C}} + 0.7\right) \times 10^{-4} \text{ max.} (1 \text{ MHz})$ C ≥ 30 pF: max. 0.1 % (1 MHz) Class 2: max. 2.5 % (1 kHz)

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### **DIMENSIONS** in millimeters



ORDERING I	NFORMATIO	N					
CAPACITANCE (pF)	TOLERANCE (%)	BODY	BODY THICKNESS S <sub>max.</sub> (mm)	LEAD SPACING <sup>(1)</sup> F (mm) ± 1 mm	LEAD DIAMETER <sup>(1)</sup> d (mm) ± 0.05 mm	WIDTH <sup>(1)</sup> V (mm) ± 0.5 mm	ORDERING CODE
		DIAMETER D <sub>max.</sub> (mm)					MISSING DIGITS SEE ORDERING CODE BELOW
N750							
10		7.0	4.3		0.6	1.7	HDU100KBD###KR
15							HDU150KBD###KR
22							HDU220KBD###KR
33						1.9	HDU330KBD###KF
47		9.5		10.0			HDU470KBD###KF
68	± 10	9.0					HDU680KBD###KF
82	± 10	11.0					HDU820KBD###KR
100		11.0					HDU101KBD###KF
150		13.0	4.7		0.8		HDU151KBD###KF
220	-	15.0					HDU221KBD###KF
330		17.0					HDU331KBD###KF
470		20.0					HDU471KBD###KF
Y5U							
33			4.5	12.5	0.6	1.9 2.3 2.5 2.7	HDE330#BD###KR
47							HDE470#BD###KF
68							HDE680#BD###KF
100		8.0					HDE101#BD###KF
150		0.0					HDE151#BD###KF
220			5.0				HDE221#BD###KF
330	± 20 <sup>(2)</sup>						HDE331#BD###KF
470	± 20 (=/						HDE471#BD###KF
680	· · · · · ·	9.0					HDE681#BD###KF
1000		10.0			0.8		HDE102#BD###KF
1500		12.0					HDE152#BD###KF
2200		13.0					HDE222#BD###KF
3300		15.0	-				HDE332#BD###KF
4700	]	18.0					HDE472#BD###KF

#### Notes

<sup>(1)</sup> Standard lead configuration, other lead spacing and diameter available on request

<sup>(2)</sup> ± 10 % available on request

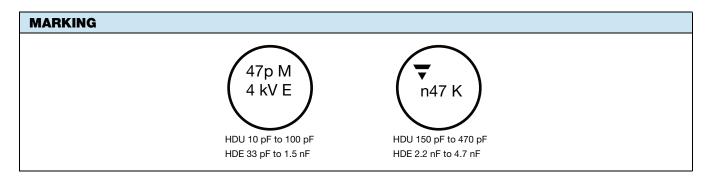
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ORDERING CODE							
#	7 <sup>th</sup> digit	Capacitance tolerance		± 10 % = K, ± 20 % = M			
###	10 <sup>th</sup> to 12 <sup>th</sup> digit	Lead configuration		See "General Information" www.vishay.com/doc?22001			<u>01</u>
Example	HDE	100	М	BD	EF0	К	R
	Series	Capacitance value	Tolerance code	Voltage code	Lead configuration	Internal code	RoHS compliant



### STORAGE

The capacitors must not be stored in a corrosive atmosphere, where sulphide or chloride gas, acid, alkali or salt are present. Exposure of the components to moisture, should be avoided. The solderability of the leads is not affected by storage of up to 24 months (temperature +10 °C to +35 °C, relative humidity up to 60 %). Class 2 ceramic dielectric capacitors are also subject to aging, see <u>www.vishay.com/doc?22001</u>.

### SOLDERING

SOLDERING SPECIFICATIONS				
Soldering test for capacitors with wire leads: (according to IEC 60068-2-20, solder bath method)				
	SOLDERABILITY	RESISTANCE TO SOLDERING HEAT		
Soldering temperature	235 °C ± 5 °C	260 °C ± 5 °C		
Soldering duration	2 s ± 0.5 s	10 s ± 1 s		
Distance from component body	≥2 mm	≥ 5 mm		

### SOLDERING RECOMMENDATIONS

Soldering of the component should be achieved using a Sn60/40 type or a silver-bearing Sn62/36/2Ag type solder. Ceramic capacitors are very sensitive to rapid changes in temperature (thermal shock) therefore the solder heat resistance specification (see Soldering Specifications table) should not be exceeded. Subjecting the capacitor to excessive heating may result in thermal shocks that can crack the ceramic body. Similarly, excessive heating can cause the internal solder junction to melt.

### CLEANING

The components should be cleaned immediately following the soldering operation with vapor degreasers.

### SOLVENT RESISTANCE

The coating and marking of the capacitors are resistant to the following test method: IEC 60068-2-45 (method XA).

### MOUNTING

If a defined product stop is required for mounting on a PCB, a mechanically formed product stop (kinked or inline wire) or a mounting tool should be used.

We do not recommend modifying the lead terminals, e.g. bending or cropping. This action could break the coating or crack the ceramic insert. If however, the lead must be modified in any way, we recommend support of the lead with a clamping fixture next to the coating.

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### **OPERATING VOLTAGE**

In case the voltage is applied to the circuit, starting as well as stopping, may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

### **OPERATING TEMPERATURE AND SELF-GENERATED HEAT**

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency, pulse, or similar application, it may have self-generated heat due to dielectric dissipation.

Temperature increase due to self-generated heating should not exceed 20 °C while operating at an atmosphere temperature of 25 °C.

When measuring, the surface temperature, make sure that the capacitor is not affected by radiant, conductive and convective heat by its surroundings. Excessive heat may lead to thermo-mechanical deterioration of the capacitor's characteristics and reliability.

RELATED DOCUMENTS	
General Information	www.vishay.com/doc?22001



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