

MOSFET - N-Channel, POWERTRENCH®

150 V, 25 A, 34 m Ω

FDMC86260ET150

General Description

This N-Channel MOSFET is produced using **onsemi**'s advanced POWERTRENCH process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- Extended T_J Rating to 175°C
- Max $R_{DS(on)} = 34 \text{ m}\Omega$ at $V_{GS} = 10 \text{ V}$, $I_D = 5.4 \text{ A}$
- Max $R_{DS(on)} = 44 \text{ m}\Omega$ at $V_{GS} = 6 \text{ V}$, $I_D = 4.8 \text{ A}$
- High Performance Technology for Extremely Low R_{DS(on)}
- 100% UIL Tested
- Pb-Free, Halide Free and RoHS Compliant

Applications

• DC-DC Conversion

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V _{DS}	Drain to Source Voltage	150	V
V _{GS}	Gate to Source Voltage	±20	V
I _D	Drain Current: Continuous, $T_C = 25^{\circ}C$ (Note 5) Continuous, $T_C = 100^{\circ}C$ (Note 5) Continuous, $T_A = 25^{\circ}C$ (Note 1a) Pulsed (Note 4)	25 18 5.4 116	Α
E _{AS}	Single Pulse Avalanche Energy (Note 3)	121	mJ
P _D	Power Dissipation: T _C = 25°C T _A = 25°C (Note 1a)	65 2.8	W
T _J , T _{STG}	Operating and Storage Junction Temperature Range	–55 to +175	°C

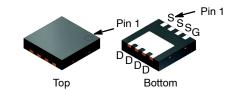
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

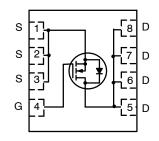
Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case (Note 1)	2.3	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	53	°C/W

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V _{DS}	R _{DS(ON)} MAX	I _D MAX
150 V	34 mΩ @ 10 V	25 A
	44 mΩ @ 6 V	



WDFN8 3.3 × 3.3, 0.65P (Power 33) CASE 483AW



N-CHANNEL MOSFET

MARKING DIAGRAM

ZXYYKK FDMC 86260ET O

Z = Assembly Plant Code

XYY = 3-Digit Date Code Format

KK = 2-Alphanumeric Lot Run

Traceability Code

FDMC86260ET = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping [†]
FDMC86260ET150	PQFN8 (Pb-Free, Halide Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

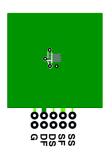
Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
OFF CHARA	ACTERISTICS	•			•	
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	150	_	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, referenced to 25°C	-	110	_	mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 120 V, V _{GS} = 0 V	_	-	1	μΑ
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V	_	-	±100	nA
ON CHARA	CTERISTICS	•			•	
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	2	2.7	4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I _D = 250 μA, referenced to 25°C	-	-9	_	mV/°C
R _{DS(on)} Sta	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 5.4 A	_	27	34	mΩ
		V _{GS} = 6 V, I _D = 4.8 A	_	31	44	
		V _{GS} = 10 V, I _D = 5.4 A, T _J = 125°C	_	55	69	
9FS	Forward Transconductance	V _{DD} = 10 V, I _D = 5.4 A	_	19	-	S
YNAMIC C	HARACTERISTICS	•				
C _{iss}	Input Capacitance	V _{DS} = 75 V, V _{GS} = 0 V, f = 1 MHz	_	1000	1330	pF
C _{oss}	Output Capacitance		_	105	140	pF
C _{rss}	Reverse Transfer Capacitance		_	4.8	10	pF
R_g	Gate Resistance		0.1	0.6	1.8	Ω
WITCHING	CHARACTERISTICS	•				
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 75 \text{ V}, I_D = 5.4 \text{ A}, V_{GS} = 10 \text{ V},$	_	9.5	19	ns
t _r	Rise Time	$R_{GEN} = 6 \Omega$	_	2	10	ns
t _{d(off)}	Turn-Off Delay Time		_	17	30	ns
t _f	Fall Time		_	3.3	10	ns
Q _{g(TOT)}	Total Gate Charge	V _{GS} = 0 V to 10 V, V _{DD} = 75 V, I _D = 5.4 A	-	15	21	nC
		$V_{GS} = 0 \text{ V to } 6 \text{ V}, V_{DD} = 75 \text{ V}, I_D = 5.4 \text{ A}$	-	9.7	14	nC
Q _{gs}	Total Gate Charge	V _{DD} = 75 V, I _D = 5.4 A	-	4.0	-	nC
Q _{gd}	Gate to Drain "Miller" Charge	V _{DD} = 75 V, I _D = 5.4 A	_	3.1	-	nC

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted) (continued)

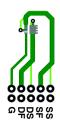
Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS						
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _S = 5.4 A (Note 2)	-	0.77	1.3	V
		V _{GS} = 0 V, I _S = 1.9 A (Note 2)	-	0.72	1.2	
t _{rr}	Reverse Recovery Time	I _F = 5.4 A, di/dt = 100 A/μs	-	64	102	ns
Q _{rr}	Reverse Recovery Charge		-	85	137	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. R_{0,JA} is determined with the device mounted on a 1 in2 pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0,CA} is determined by the user's board design.



a) 53°C/W when mounted on a 1 in² pad of 2 oz copper.



b) 125°C/W when mounted on a minimum pad of 2 oz copper.

- 2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%.
- Fulse Toda: Take What T Code µd, Baty Gyard 120%.
 E_{AS} of 121 mJ is based on starting T_J = 25°C, L = 3 mH, I_{AS} = 9 A, V_{DD} = 150 V, V_{GS} = 10 V. 100% test at L = 0.1 mH, I_{AS} = 22 A.
 Pulsed Id please refer to Figure 11 SOA graph for more details.
- 5. Computed continuous current limited to Max Junction Temperature only, actual continuous current will be limited by thermal & electro-mechanical application board design.

TYPICAL CHARACTERISTICS

 $(T_J = 25^{\circ}C \text{ unless otherwise noted})$

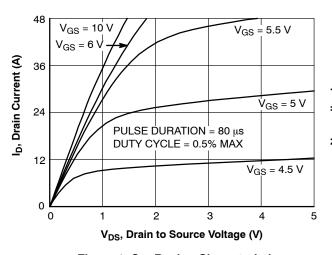


Figure 1. On-Region Characteristics

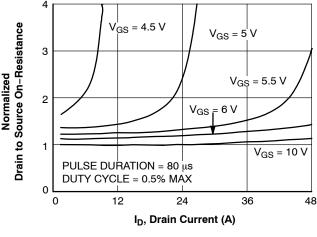


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

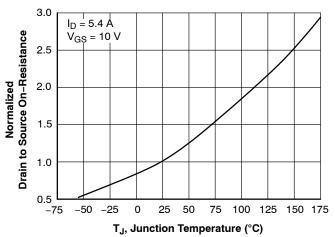


Figure 3. Normalized On–Resistance vs. Junction Temperature

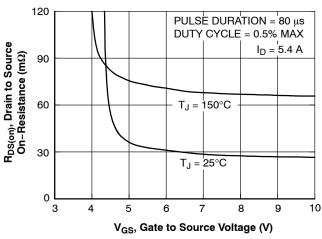


Figure 4. On-Resistance vs. Gate to Source Voltage

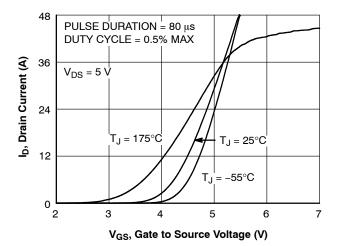


Figure 5. Transfer Characteristics

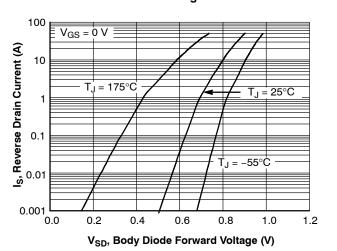


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

TYPICAL CHARACTERISTICS (continued)

(T_J = 25°C unless otherwise noted)

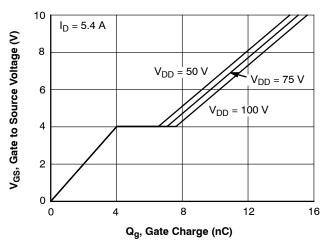


Figure 7. Gate Charge Characteristics

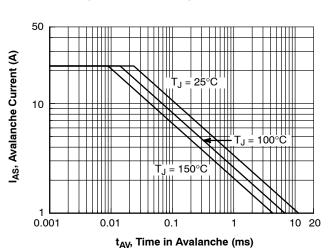


Figure 9. Unclamped Inductive Switching Capability

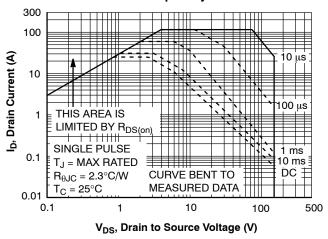


Figure 11. Forward Bias Safe Operating Area

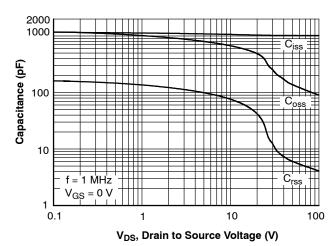


Figure 8. Capacitance vs. Drain to Source Voltage

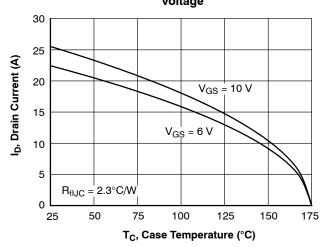


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

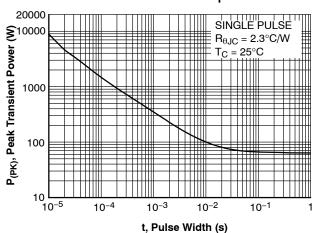


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS (continued)

 $(T_J = 25^{\circ}C \text{ unless otherwise noted})$

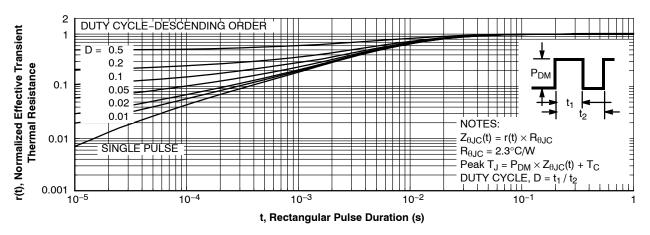


Figure 13. Junction-to-Case Transient Thermal Response Curve

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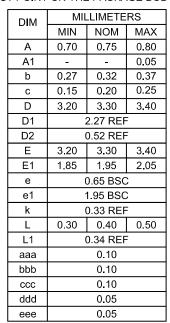


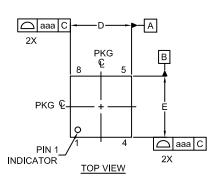
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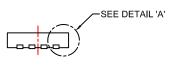
DATE 10 SEP 2019

NOTES:

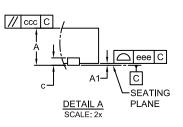
- 1. CONTROLLING DIMENSION: MILLIMETERS.
- 2. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.
- 3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- 4. SEATING PLANE IS DEFINED BY THE TERMINALS. 'A1' IS DEFINED AS THE DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.

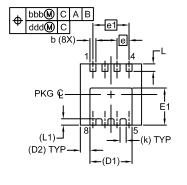






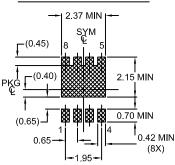
FRONT VIEW





BOTTOM VIEW

LAND PATTERN RECOMMENDATION*



*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC MARKING DIAGRAM*

XXXX AYWW XXXX = Specific Device Code A = Assembly Location

Y = Year

WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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