

# MOSFET – N-Channel, POWERTRENCH®

**30 V, 14.8 A, 7.2 mΩ**

## FDMC7680

### General Description

This N-Channel MOSFET is produced using onsemi's advanced POWERTRENCH process that has been especially tailored to minimize the on-state resistance. This device is well suited for Power Management and load switching applications common in Notebook Computers and Portable Battery Packs.

### Features

- Max  $R_{DS(on)}$  = 7.2 mΩ at  $V_{GS} = 10\text{ V}$ ,  $I_D = 14.8\text{ A}$
- Max  $R_{DS(on)}$  = 9.5 mΩ at  $V_{GS} = 4.5\text{ V}$ ,  $I_D = 12.4\text{ A}$
- High Performance Technology for Extremely Low  $R_{DS(on)}$
- Pb-Free, Halide Free and RoHS Compliant

### Applications

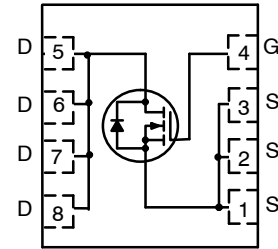
- DC-DC Buck Converters
- Notebook Battery Power Management
- Load Switch in Notebook

### MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

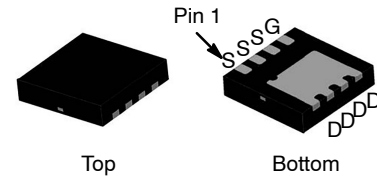
Symbol	Parameter	Value	Unit
$V_{DS}$	Drain to Source Voltage	30	V
$V_{GS}$	Gate to Source Voltage	±20	V
$I_D$	Drain Current:		A
	Continuous, $T_C = 25^\circ\text{C}$	18	
	Continuous, $T_A = 25^\circ\text{C}$ (Note 1a)	14.8	
	Pulsed	45	
$E_{AS}$	Single Pulse Avalanche Energy (Note 3)	72	mJ
$P_D$	Power Dissipation:		W
	$T_C = 25^\circ\text{C}$	31	
	$T_A = 25^\circ\text{C}$ (Note 1a)	2.3	
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

$V_{DS}$	$R_{DS(on)}$ MAX	$I_D$ MAX
30 V	7.2 mΩ @ 10 V	14.8 A
	9.5 mΩ @ 4.5 V	

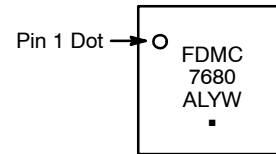


N-CHANNEL MOSFET



WDFN8 3.3 × 3.3, 0.65P  
CASE 511DH

### MARKING DIAGRAM



FDMC7680 = Specific Device Code  
A = Assembly Site  
L = Wafer Lot Number  
YW = Assembly Start Week  
▪ = Pb-Free Package

### ORDERING INFORMATION

Device	Package	Shipping†
FDMC7680	WDFN8 (Pb-Free, Halide Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, [BRD8011/D](#).

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## THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	4.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	53	

## ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain to Source Breakdown Voltage	$I_D = 250 \mu\text{A}, V_{GS} = 0 \text{ V}$	30	–	–	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , referenced to $25^\circ\text{C}$	–	15	–	mV/°C
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$	–	–	1	$\mu\text{A}$
		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 125^\circ\text{C}$	–	–	250	
$I_{GSS}$	Gate to Source Leakage Current	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$	–	–	100	nA

### ON CHARACTERISTICS

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu\text{A}$	1.2	2.0	3.0	V
$\Delta V_{GS(th)} / \Delta T_J$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , referenced to $25^\circ\text{C}$	–	–6	–	mV/°C
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 14.8 \text{ A}$	–	5.8	7.2	m $\Omega$
		$V_{GS} = 4.5 \text{ V}, I_D = 12.4 \text{ A}$	–	7.3	9.5	
		$V_{GS} = 10 \text{ V}, I_D = 14.8 \text{ A}, T_J = 125^\circ\text{C}$	–	7.4	9.2	
$g_{FS}$	Forward Transconductance	$V_{DD} = 5 \text{ V}, I_D = 14.8 \text{ A}$	–	68	–	S

### DYNAMIC CHARACTERISTICS

$C_{iss}$	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	–	2145	2855	pF
$C_{oss}$	Output Capacitance		–	770	1020	pF
$C_{rss}$	Reverse Transfer Capacitance		–	75	115	pF
$R_g$	Gate Resistance	$f = 1 \text{ MHz}$	–	0.5	1.6	$\Omega$

### SWITCHING CHARACTERISTICS

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 15 \text{ V}, I_D = 14.8 \text{ A}, V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$	–	12	22	ns
$t_r$	Rise Time		–	4	10	ns
$t_{d(off)}$	Turn-Off Delay Time		–	25	40	ns
$t_f$	Fall Time		–	3	10	ns
$Q_{g(TOT)}$	Total Gate Charge	$V_{GS} = 0 \text{ V to } 10 \text{ V}, V_{DD} = 15 \text{ V}, I_D = 14.8 \text{ A}$	–	30	42	nC
		$V_{GS} = 0 \text{ V to } 4.5 \text{ V}, V_{DD} = 15 \text{ V}, I_D = 14.8 \text{ A}$	–	14	19	nC
$Q_{gs}$	Gate to Source Charge	$V_{DD} = 15 \text{ V}, I_D = 14.8 \text{ A}$	–	7	–	nC
$Q_{gd}$	Gate to Drain “Miller” Charge	$V_{DD} = 15 \text{ V}, I_D = 14.8 \text{ A}$	–	4	–	nC

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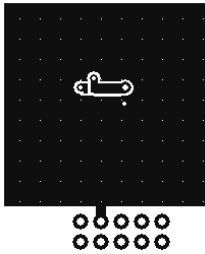
## ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise noted) (continued)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
$V_{SD}$	Source to Drain Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 14.8\text{ A}$ (Note 2)	–	0.84	1.2	V
		$V_{GS} = 0\text{ V}, I_S = 1.9\text{ A}$ (Note 2)	–	0.73	1.2	
$t_{rr}$	Reverse Recovery Time	$I_F = 14.8\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	–	34	54	ns
$Q_{rr}$	Reverse Recovery Charge		–	15	24	nC

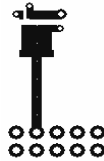
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### NOTES:

- $R_{\theta JA}$  is determined with the device mounted on a 1 in<sup>2</sup> pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material.  $R_{\theta JC}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.



a) 53°C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper.



b) 125°C/W when mounted on a minimum pad of 2 oz copper.

- Pulse Test: Pulse Width < 300  $\mu\text{s}$ , Duty cycle < 2.0%.
- $E_{AS}$  of 72 mJ is based on starting  $T_J = 25^\circ\text{C}$ ,  $L = 1\text{ mH}$ ,  $I_{AS} = 12\text{ A}$ ,  $V_{DD} = 27\text{ V}$ ,  $V_{GS} = 10\text{ V}$ .

TYPICAL CHARACTERISTICS

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

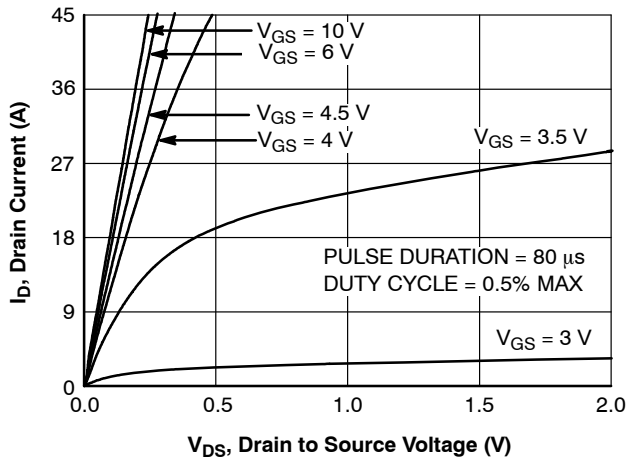


Figure 1. On-Region Characteristics

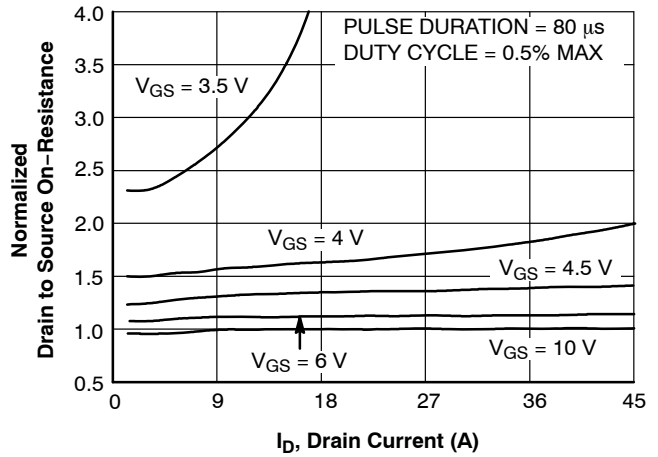


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

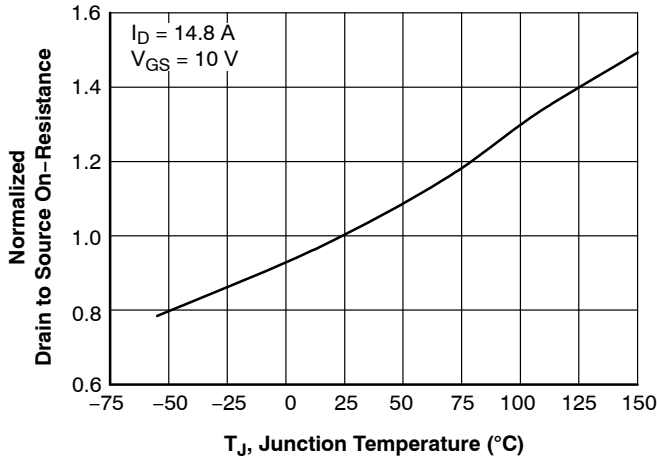


Figure 3. Normalized On-Resistance vs. Junction Temperature

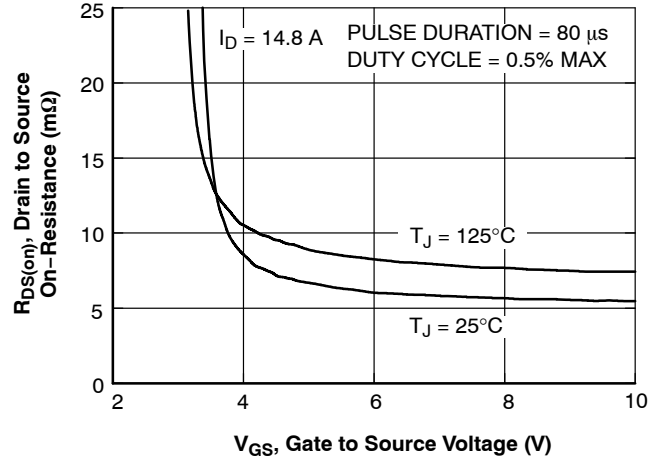


Figure 4. On-Resistance vs. Gate to Source Voltage

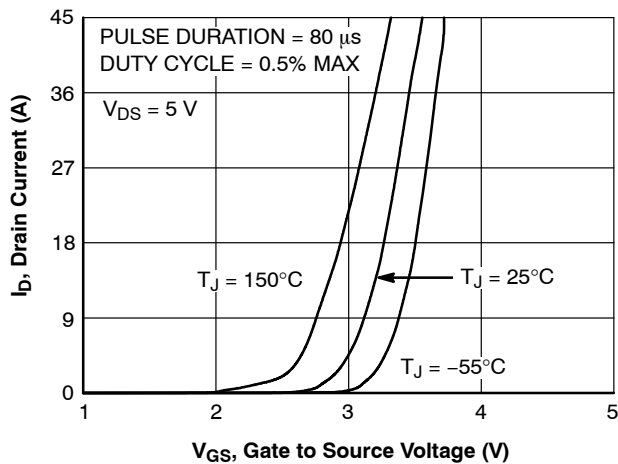


Figure 5. Transfer Characteristics

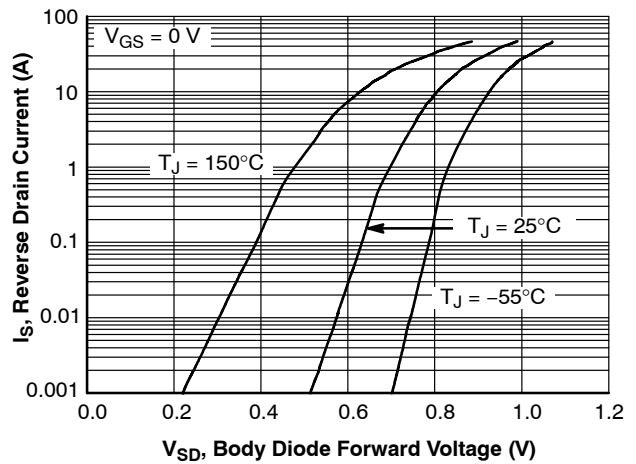


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

TYPICAL CHARACTERISTICS (continued)

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

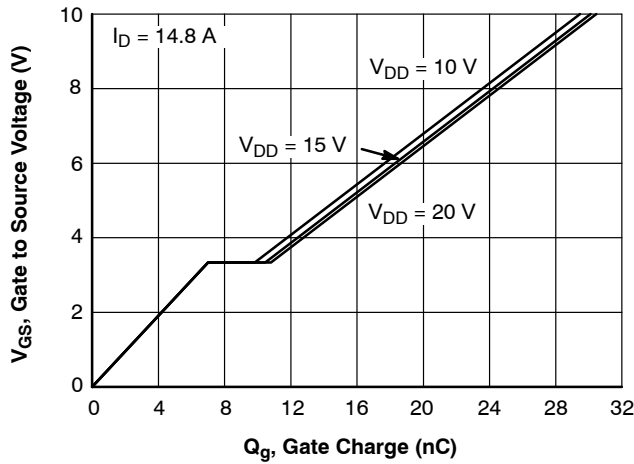


Figure 7. Gate Charge Characteristics

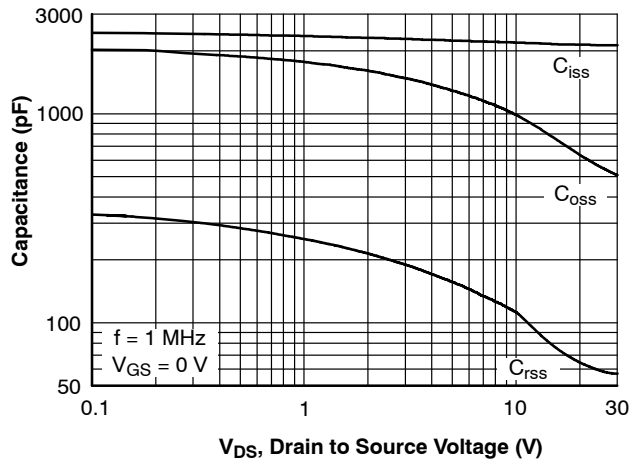


Figure 8. Capacitance vs. Drain to Source Voltage

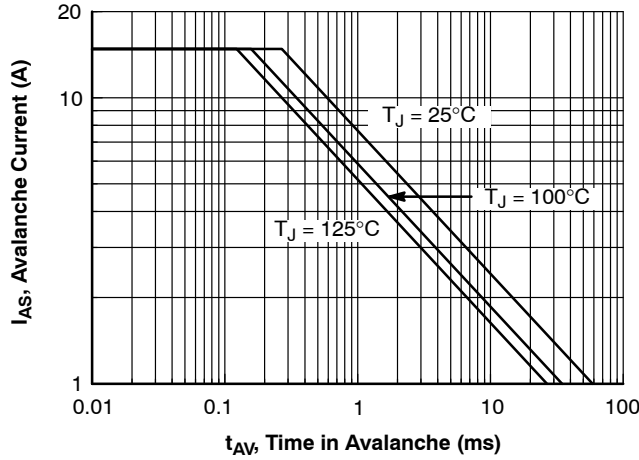


Figure 9. Unclamped Inductive Switching Capability

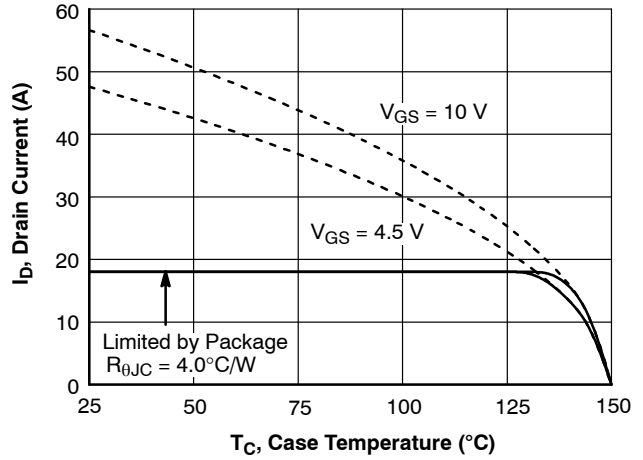


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

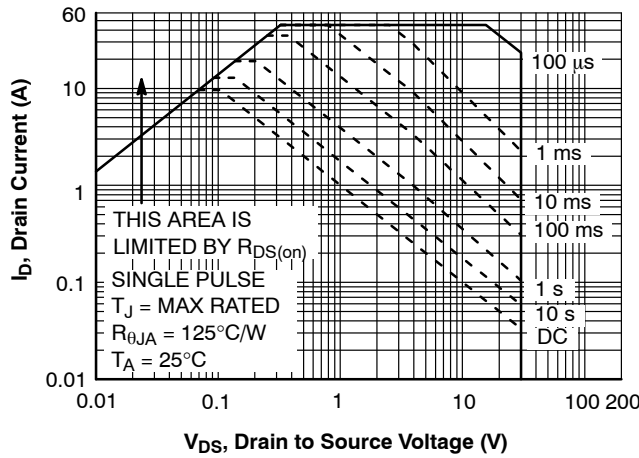


Figure 11. Forward Bias Safe Operating Area

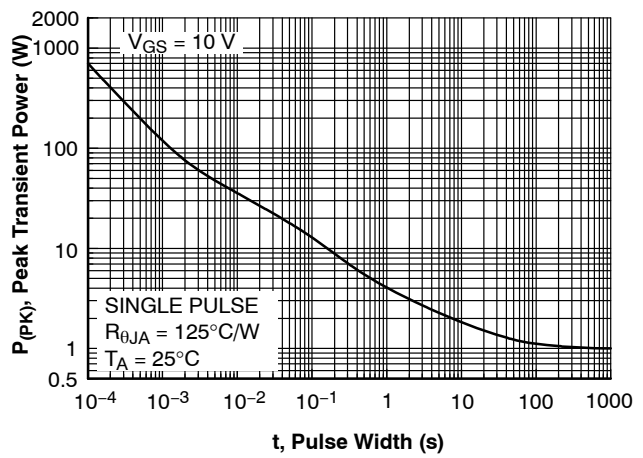


Figure 12. Single Pulse Maximum Power Dissipation

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## TYPICAL CHARACTERISTICS (continued)

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

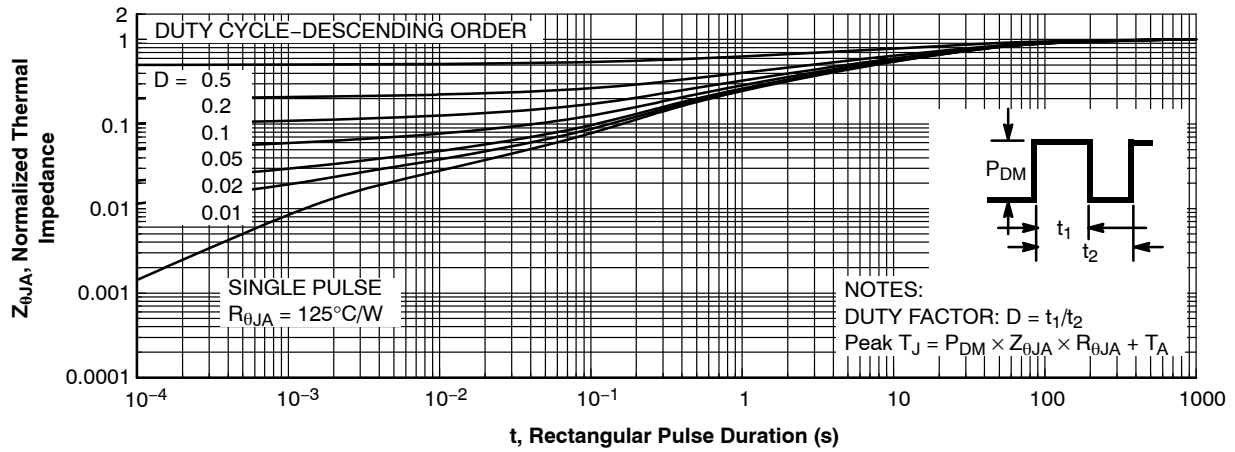


Figure 13. Transient Thermal Response Curve

# MECHANICAL CASE OUTLINE

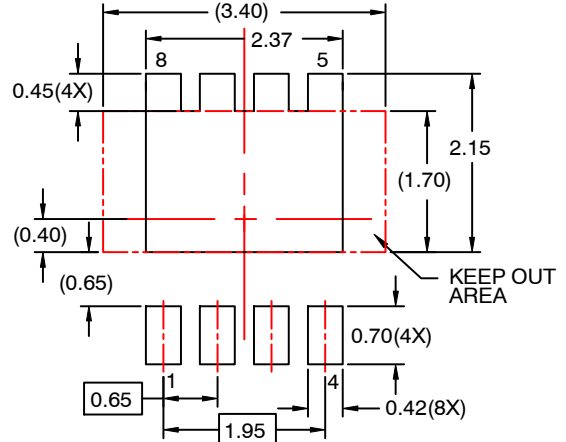
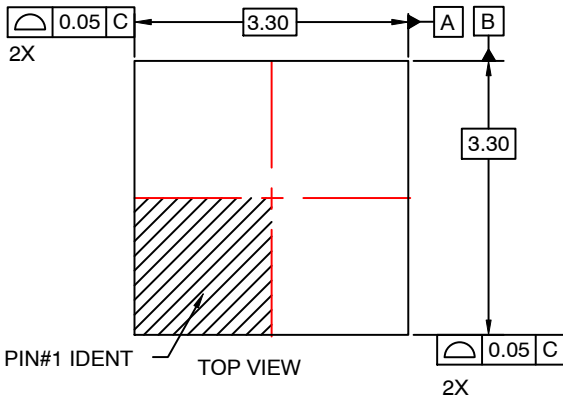
## PACKAGE DIMENSIONS

ON Semiconductor®



WDFN8 3.3x3.3, 0.65P  
CASE 511DH  
ISSUE O

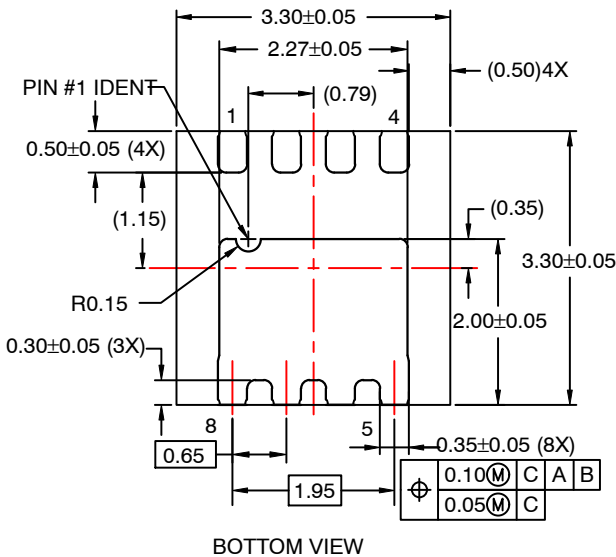
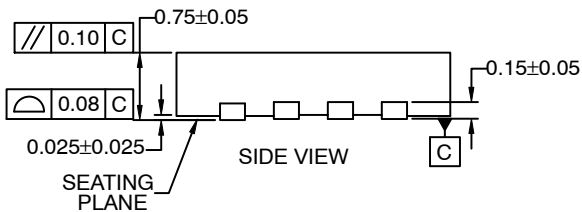
DATE 31 JUL 2016



RECOMMENDED LAND PATTERN

NOTES:

- A. DOES NOT CONFORM TO JEDEC REGISTRATION MO-229
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- D. LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.



BOTTOM VIEW

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