

Data Sheet

Features

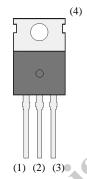
- $R_{DS(ON)}$ ------ 28.8 m Ω max. ($V_{GS} = 10 \text{ V}$, $I_D = 17.1 \text{ A}$)
- Q_g -----16.9 nC (V_{GS} = 4.5 V, V_{DS} = 50 V, I_D = 17.1 A)
- Low Total Gate Charge
- High Speed Switching
- Low On-Resistance
- Capable of 4.5 V Gate Drive
- 100 % UIL Tested
- RoHS Compliant

Applications

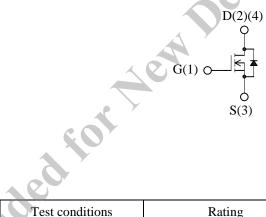
- DC-DC converters
- Synchronous Rectification
- Power Supplies

Package

• TO220-3L



Not to scale



Absolute Maximum Ratings

• Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Test conditions	Rating	Unit
Drain to Source Voltage	V_{DS}		100	V
Gate to Source Voltage	V_{GS}		± 20	V
Continuous Drain Current	I_{D}	T _C = 25 °C	34	A
Pulsed Drain Current	I_{DM}	$PW \le 100 \mu s \\ Duty \ cycle \le 1 \ \%$	68	A
Continuous Source Current (Body Diode)	I_S		34	A
Pulsed Source Current (Body Diode)	I_{SM}	PW ≤ 100μs Duty cycle ≤ 1 %	68	A
Single Pulse Avalanche Energy	E _{AS}	$V_{DD} = 50 \text{ V}, L = 1 \text{ mH},$ $I_{AS} = 9.4 \text{ A, unclamped},$ $R_G = 4.7 \Omega$ Refer to Figure 1	89	mJ
Avalanche Current	I_{AS}		16.7	A
Power Dissipation	P_{D}	T _C = 25 °C	90	W
Operating Junction Temperature	T_{J}		150	°C
Storage Temperature Range	T_{STG}		- 55 to 150	°C

EKI10300

Thermal Characteristics

• Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Thermal Resistance (Junction to Case)	$R_{ heta JC}$		-	-	1.4	°C/W
Thermal Resistance (Junction to Ambient)	$R_{\theta JA}$		-	_	62.5	°C/W

Electrical Characteristics

• Unless otherwise specified, $T_A = 25$ °C

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Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 100 \ \mu A, \ V_{GS} = 0 \ V$	100	-0		V
Drain to Source Leakage Current	I_{DSS}	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$			100	μΑ
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20 \text{ V}$	-<	_	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 650 \mu A$	1.0	2.0	2.5	V
Static Drain to Source	R _{DS(ON)}	$I_D = 17.1 \text{ A}, V_{GS} = 10 \text{ V}$	O -	20.2	28.8	mΩ
On-Resistance		$I_D = 8.6 \text{ A}, V_{GS} = 4.5 \text{ V}$	-	21.6	30.0	mΩ
Gate Resistance	R_{G}	f = 1 MHz	_	1.5	_	Ω
Input Capacitance	C _{iss}	V _{DS} = 25 V	_	2540	_	pF
Output Capacitance	C_{oss}	$V_{GS} = 0 V$	_	195	_	
Reverse Transfer Capacitance	C_{rss}	f = 1 MHz	_	88	_	
Total Gate Charge (V _{GS} = 10 V)	Q_{g1}	$V_{DS} = 50 \text{ V}$ $I_{D} = 17.1 \text{ A}$	_	36.5	_	nC
Total Gate Charge (V _{GS} = 4.5 V)	Q_{g2}		_	16.9	_	
Gate to Source Charge	Q_{gs}		_	6.4	_	
Gate to Drain Charge	Q_{gd}		_	4.8	_	
Turn-On Delay Time	$t_{d(on)}$		1	4.7	_	ns
Rise Time	t _r	$\begin{split} V_{DD} &= 50 \text{ V} \\ I_D &= 17.1 \text{ A} \\ V_{GS} &= 10 \text{ V}, R_G = 4.7 \Omega \\ \text{Refer to Figure 2} \end{split}$	-	4.4	_	
Turn-Off Delay Time	$t_{d(off)}$		-	21.9		
Fall Time	t_{f}		-	9.4	_	
Source to Drain Diode Forward Voltage	V_{SD}	$I_S = 17.1 \text{ A}, V_{GS} = 0 \text{ V}$	_	0.9	1.5	V
Source to Drain Diode Reverse Recovery Time	t _{rr}	$I_F = 17.1 \text{ A}$ di/dt = 100 A/\mus	_	44.6	_	ns
Source to Drain Diode Reverse Recovery Charge	Q_{rr}	Refer to Figure 3	_	82.5	_	nC

Test Circuits and Performance Curves

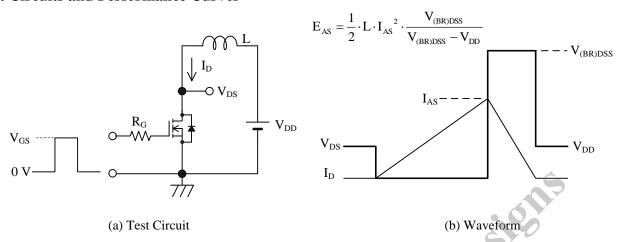


Figure 1. Unclamped Inductive Switching

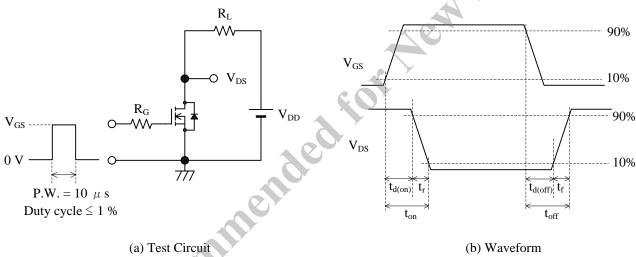


Figure 2. Switching Time

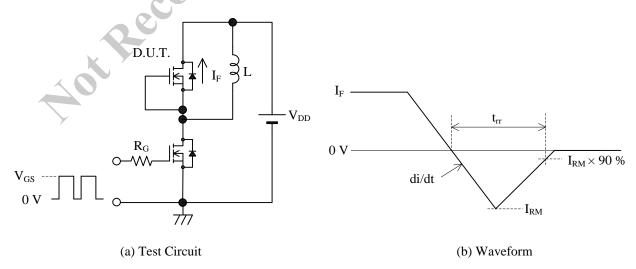
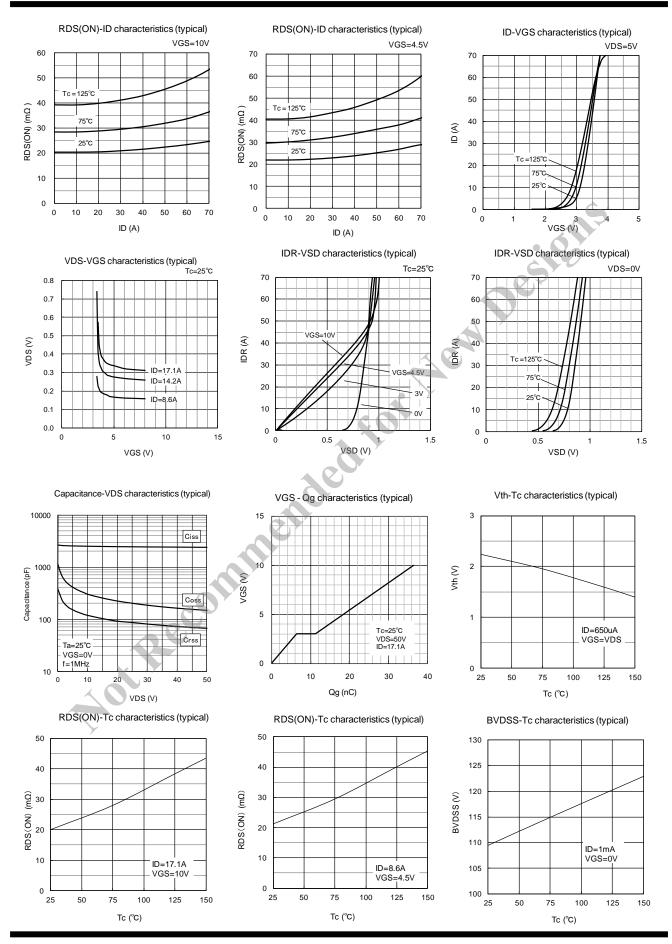
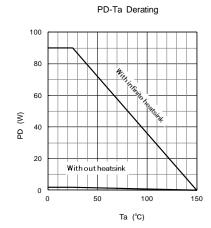
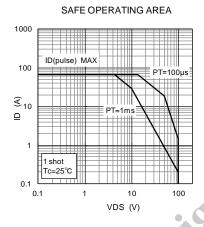
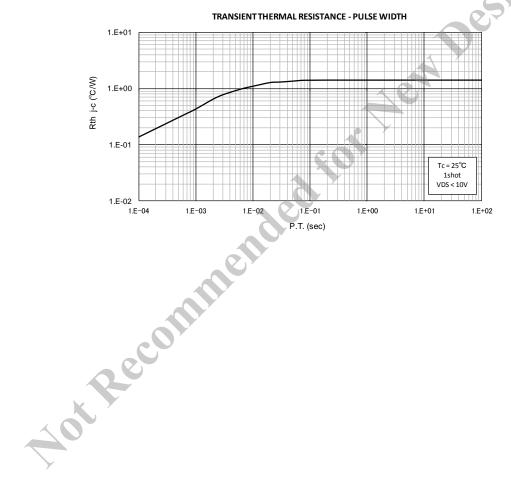


Figure 3. Diode Reverse Recovery Time



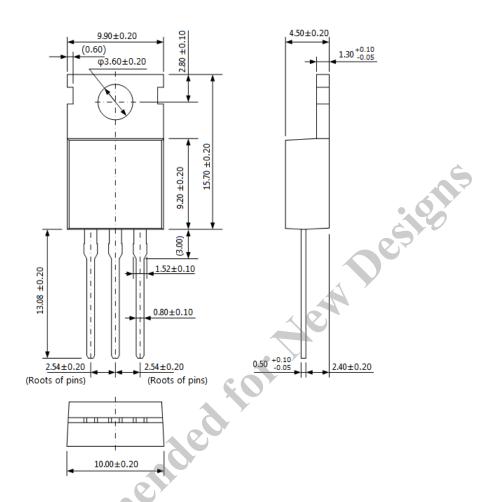






Physical Dimensions

• TO220-3L



NOTES:

- Dimensions in millimeters
- Maximum gate burr height is 0.3 mm.
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time, within the following limits:

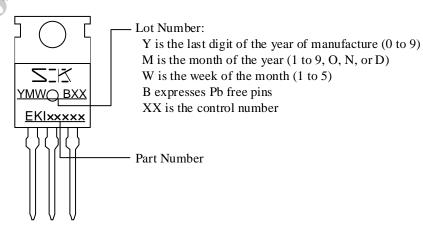
Flow: $260 \pm 5 \, ^{\circ}\text{C} / 10 \pm 1 \, \text{s}, 2 \, \text{times}$

Soldering Iron: 380 ± 10 °C / 3.5 ± 0.5 s, 1 time

Soldering should be at a distance of at least 1.5 mm from the body of the product.

- Recommended screw torque for TO220: 0.490 N·m to 0.686 N·m (5 kgf·cm to 7 kgf·cm)

Marking Diagram



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