

# BUL642D2

## High Speed, High Gain Bipolar NPN Transistor with Integrated Collector-Emitter and Built-in Efficient Antisaturation Network

The BUL642D2 is a state-of-the-art High Speed High Gain Bipolar Transistor (H2BIP). Tight dynamic characteristics and lot to lot minimum spread (150 ns on storage time) make it ideally suitable for Light Ballast Application. A new development process brings avalanche energy capability, making the device extremely rugged.

### Features

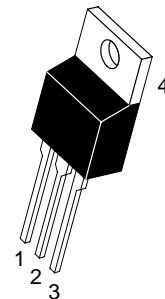
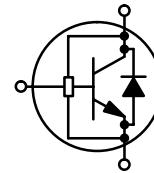
- Low Base Drive Requirement
- High Peak DC Current Gain (55 Typical) @  $I_C = 300 \text{ mA}/5 \text{ V}$
- Extremely Low Storage Time Min/Max Guarantees Due to the H2BIP Structure which Minimizes the Spread
- Integrated Collector-Emitter Free Wheeling Diode
- Fully Characterized Dynamic  $V_{CEsat}$
- "Six Sigma" Process Providing Tight and Reproducible Parameter Spreads
- Avalanche Energy 20 mJ Typical Capability
- Pb-Free Package is Available\*



ON Semiconductor®

<http://onsemi.com>

**3 AMPERES  
825 VOLTS  
75 WATTS  
POWER TRANSISTOR**



TO-220AB  
CASE 221A  
STYLE 1

### MARKING DIAGRAM



BUL642D2 = Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
G = Pb-Free Package

### ORDERING INFORMATION

Device	Package	Shipping
BUL642D2	TO-220	50 Units/Rail
BUL642D2G	TO-220 (Pb-Free)	50 Units/Rail

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Sustaining Voltage	$V_{CEO}$	440	Vdc
Collector–Base Breakdown Voltage	$V_{CES}$	825	Vdc
Emitter–Base Voltage	$V_{EBO}$	11	Vdc
Collector Current – Continuous – Peak (Note 1)	$I_C$ $I_{CM}$	3.0 8.0	Adc
Base Current – Continuous – Peak (Note 1)	$I_B$ $I_{BM}$	2.0 4.0	Adc
*Total Device Dissipation @ $T_C = 25^\circ\text{C}$ *Derate above $25^\circ\text{C}$	$P_D$	75 0.6	W W/ $^\circ\text{C}$
Operating and Storage Temperature	$T_J, T_{stg}$	–65 to +150	$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

## TYPICAL GAIN

Rating	Symbol	Value	Unit
Typical Gain @ $I_C = 1\text{ A}$ , $V_{CE} = 2\text{ V}$ Typical A, $V_{CE} = 1\text{ V}$	$h_{FE}$ $h_{FE}$	45 50	– –

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction–to–Case	$R_{\theta JC}$	1.6	$^\circ\text{C/W}$
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes: 1/8 in. from Case for 5 seconds	$T_L$	260	$^\circ\text{C}$

1. Pulse Test: Pulse Width = 5.0 ms, Duty Cycle = 10%

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## ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage (I <sub>C</sub> = 200 mA, L = 25 mH)	V <sub>CEO(sus)</sub>	440	-	-	Vdc	
Collector-Base Breakdown Voltage (I <sub>CBO</sub> = 1 mA)	V <sub>CBO</sub>	825	-	-	Vdc	
Emitter-Base Breakdown Voltage (I <sub>EBO</sub> = 1 mA)	V <sub>EBO</sub>	11	-	-	Vdc	
Collector Cutoff Current (V <sub>CE</sub> = Rated V <sub>CEO</sub> , I <sub>B</sub> = 0)	I <sub>CEO</sub>	@ T <sub>C</sub> = 25°C	-	-	200	μAdc
		@ T <sub>C</sub> = 125°C	-	-	1000	
Collector Cutoff Current (V <sub>CE</sub> = Rated V <sub>CES</sub> , V <sub>EB</sub> = 0)	I <sub>CES</sub>	@ T <sub>C</sub> = 25°C	-	-	100	μAdc
		@ T <sub>C</sub> = 125°C	-	-	1000	
Emitter-Cutoff Current (V <sub>EB</sub> = 10 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	-	-	100	μAdc	

### ON CHARACTERISTICS

Base-Emitter Saturation Voltage (I <sub>C</sub> = 0.5 Adc, I <sub>B</sub> = 100 mAdc I <sub>C</sub> = 1 Adc, I <sub>B</sub> = 0.2 Adc)	V <sub>BE(sat)</sub>	-	-	1.1 1.5	Vdc
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 0.5 Adc, I <sub>B</sub> = 50 mAdc I <sub>C</sub> = 2 Adc, I <sub>B</sub> = 0.2 Adc)	V <sub>CE(sat)</sub>	-	-	0.5 1.5	Vdc
DC Current Gain (I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 1 Vdc I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 3 Vdc)	h <sub>FE</sub>	16 18	-	-	-

### DYNAMIC SATURATION VOLTAGE

Dynamic Saturation Voltage:	I <sub>C</sub> = 0.5 Adc I <sub>B1</sub> = 50 mAdc V <sub>CC</sub> = 125 Vdc	@ 1 μs	@ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 125°C	V <sub>CE(dsat)</sub>	-	2.0	-	V
		@ 3 μs	@ T <sub>C</sub> = 25°C		-	5.0	-	
	I <sub>C</sub> = 1 Adc I <sub>B1</sub> = 100 mAdc V <sub>CC</sub> = 300 Vdc	@ 1 μs	@ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 125°C		-	0.2	-	
		@ 3 μs	@ T <sub>C</sub> = 25°C		-	1.3	-	
		@ 1 μs	@ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 125°C		-	4.5	-	
		@ 3 μs	@ T <sub>C</sub> = 25°C		-	10	-	
				-	1.0	-		
				-	3.0	-		

### DYNAMIC CHARACTERISTICS

Current Gain Bandwidth I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 10 Vdc, f = 1 MHz	f <sub>T</sub>	-	13	-	MHz
Output Capacitance @ V <sub>cb</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	C <sub>ob</sub>	-	70	150	pF
Input Capacitance @ V <sub>EB</sub> = 8 V, f = 1 MHz	C <sub>ib</sub>	-	500	1000	pF

### DIODE CHARACTERISTICS

Forward Diode Voltage (I <sub>EC</sub> = 0.5 Adc) (I <sub>EC</sub> = 1.0 Adc)	V <sub>EC</sub>	-	0.8 1.0	1.5 2.0	V
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### SWITCHING CHARACTERISTICS: Resistive Load (D.C. ≤ 10%, Pulse Width = 70 μs)

Delay Time	I <sub>C</sub> = 0.5 Adc	t <sub>d</sub>	-	60	400	ns
Rise Time	I <sub>B1</sub> = 45 mA	t <sub>r</sub>	-	160	1100	ns
Storage Time	I <sub>B2</sub> = 500 mA	t <sub>s</sub>	-	0.5	1400	μs
Fall Time	V <sub>CC</sub> = 125 V	t <sub>f</sub>	-	0.4	600	ns

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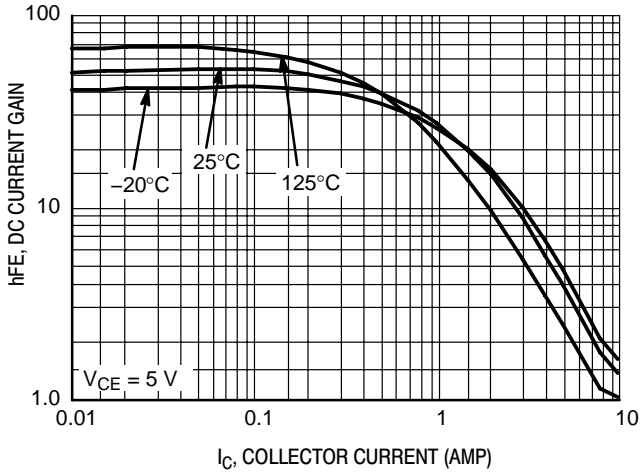


Figure 1. DC Current Gain

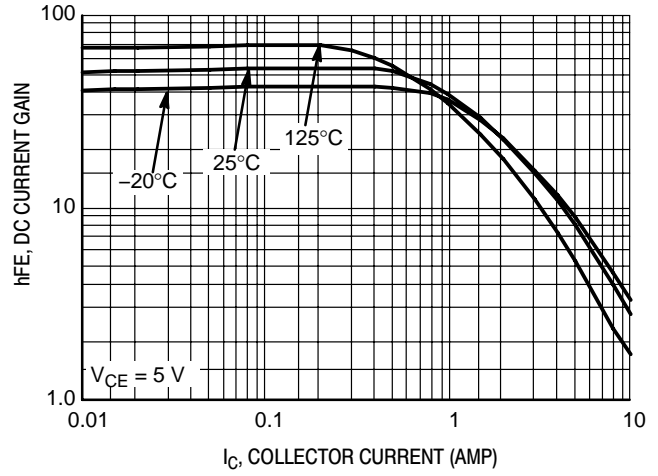


Figure 2. DC Current Gain

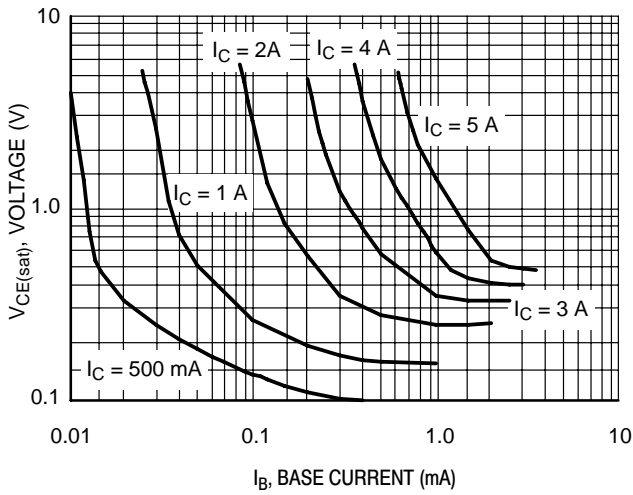


Figure 3. Collector Saturation Region

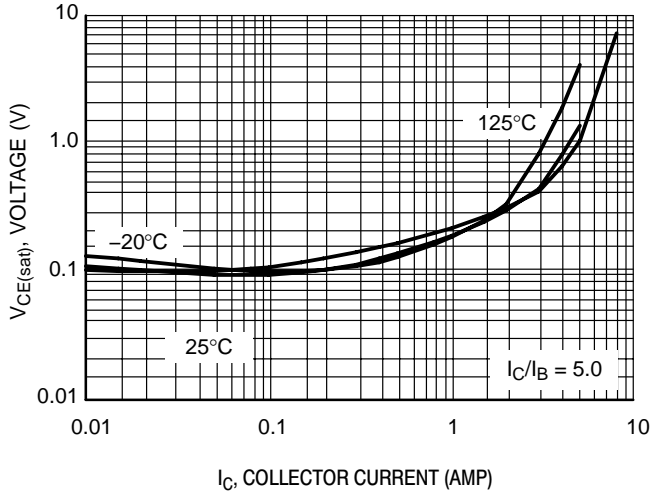


Figure 4. Collector-Emitter Saturation Voltage

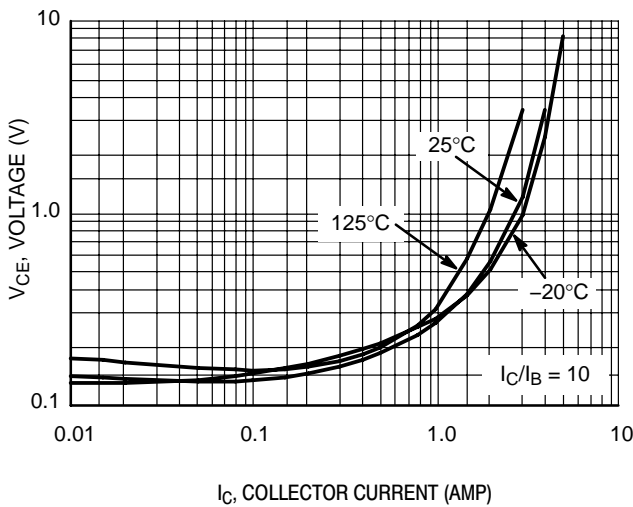


Figure 5. Collector-Emitter Saturation Voltage

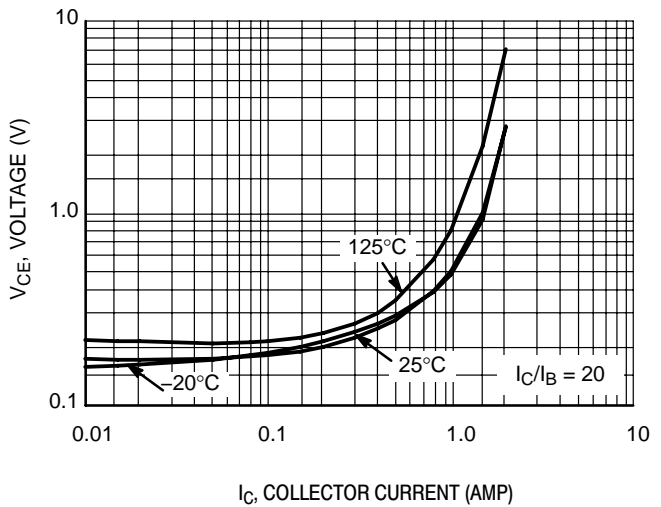
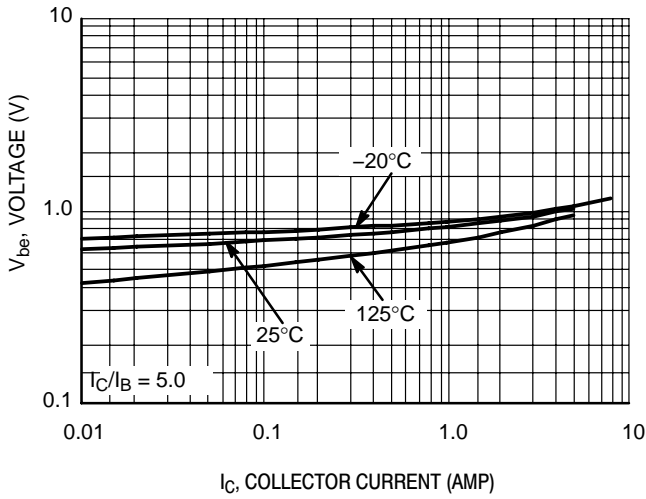
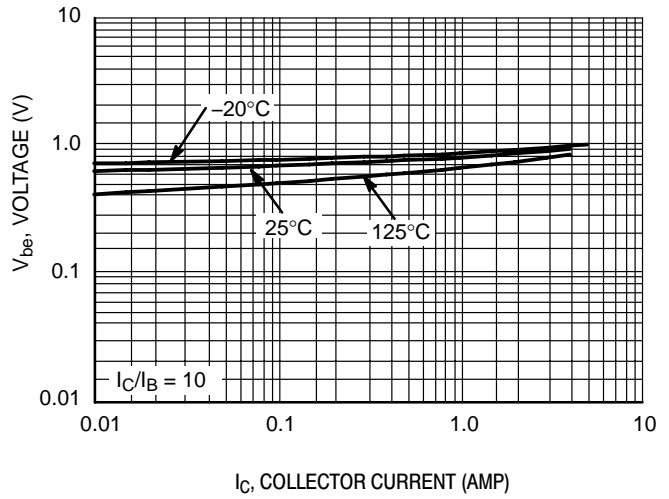


Figure 6. Collector-Emitter Saturation Voltage

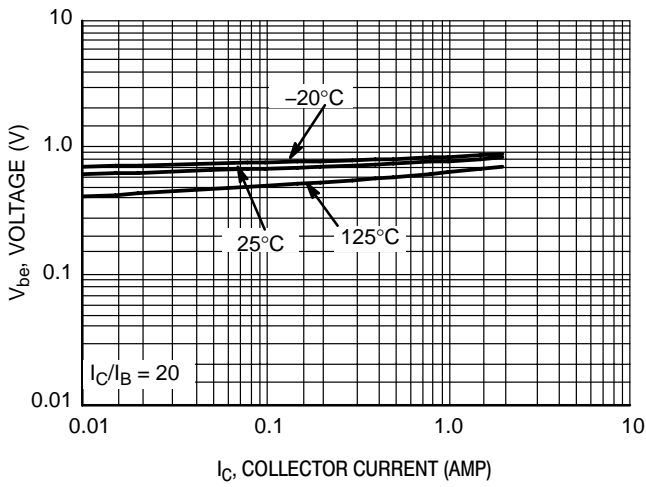
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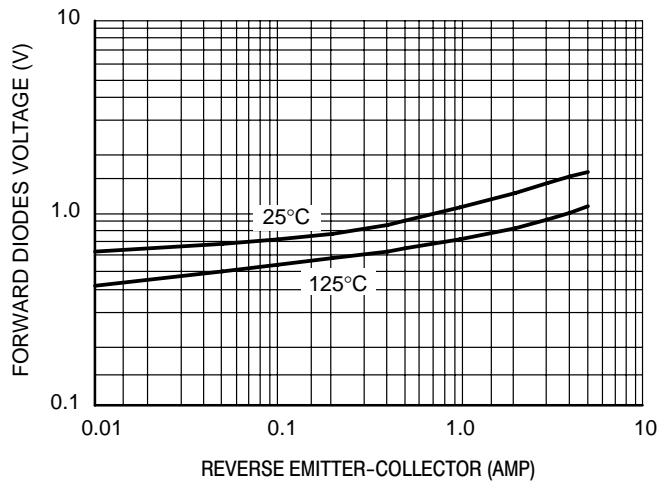
**Figure 7. Base-Emitter Saturation Voltage**



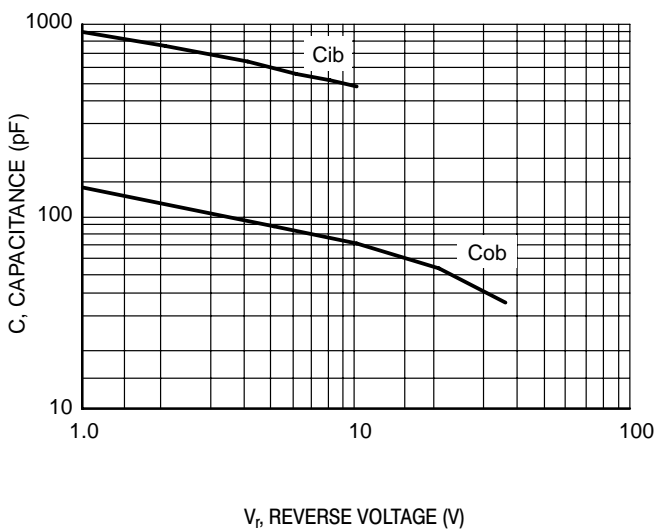
**Figure 8. Base-Emitter Saturation Voltage**



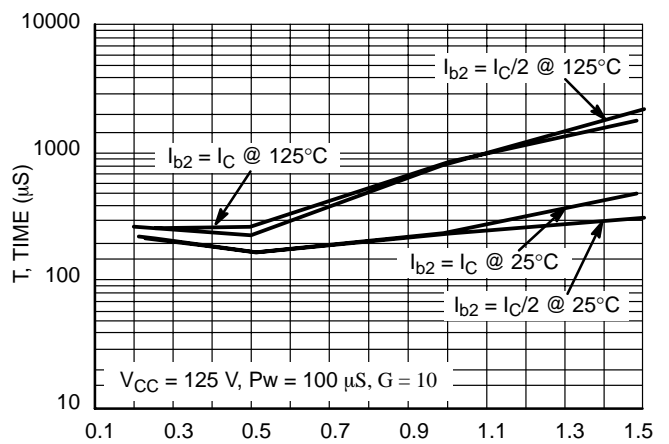
**Figure 9. Base-Emitter Saturation Voltage**



**Figure 10. Forward Diode Voltage**

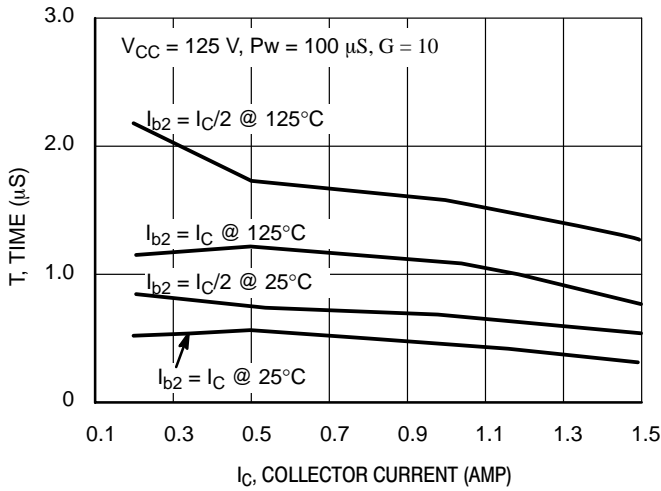


**Figure 11. Capacitance**

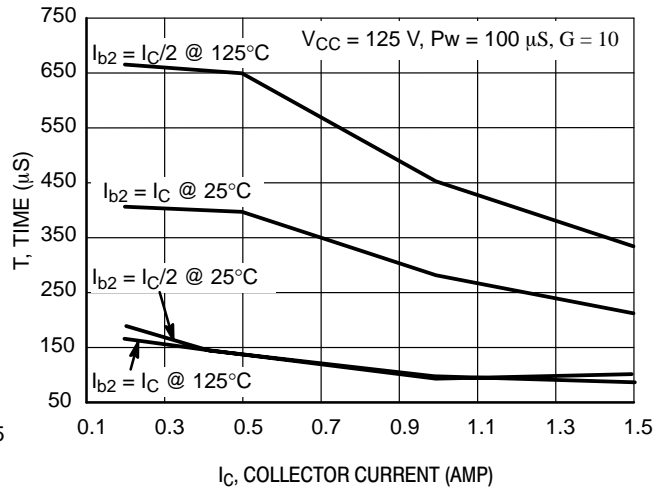


**Figure 12. Resistive Switch Time, Storage Time  $T_{ON}$**

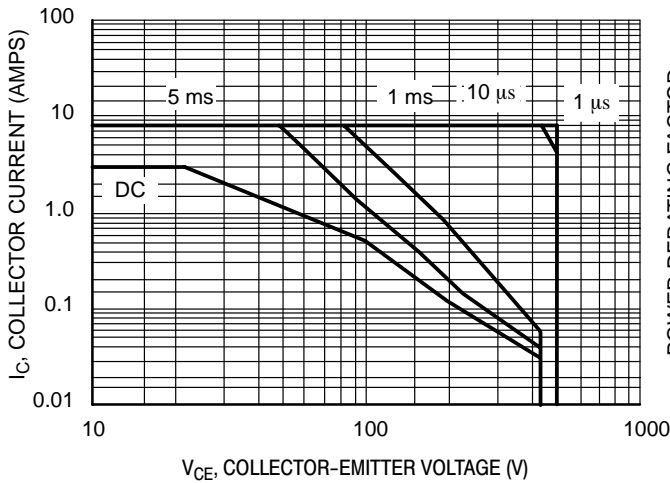
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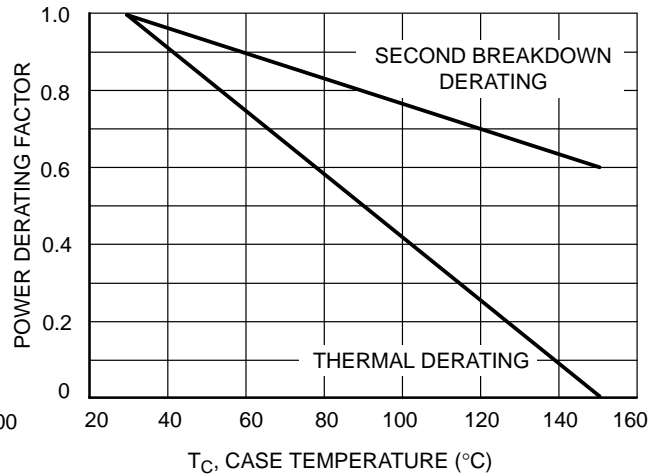
**Figure 13. Resistive Switch Time, Storage Time**



**Figure 14. Resistive Switch Time, Fall Time**



**Figure 15.**



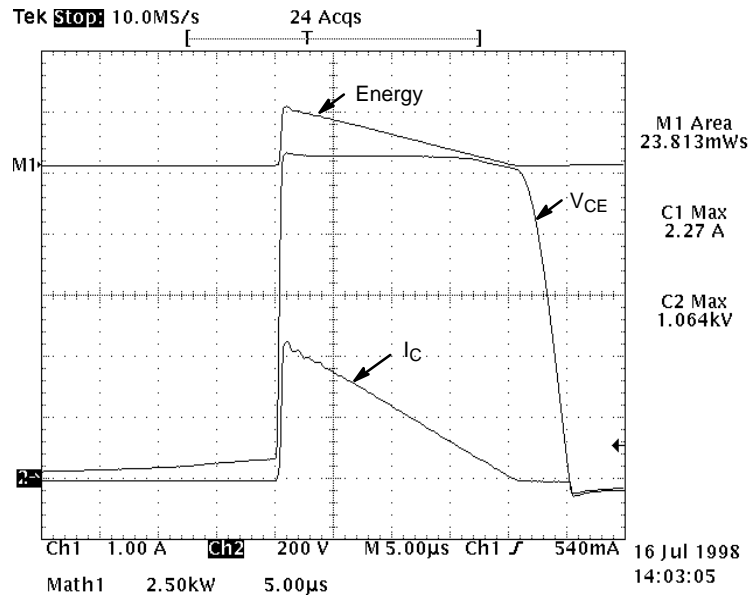
**Figure 16. Power Derating**

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C$ - $V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 15 is based on  $T_C = 25^\circ\text{C}$ ;  $T_{j(pk)}$  is variable depending on power level. Second breakdown pulse limits do not derate like

thermal limitations. Allowable current at the voltages shown on Figure 10 may be found at any case temperature by using the appropriate curve on Figure 16.

$T_{j(pk)}$  may be calculated from the data in Figure 18. At any case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

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Figure 17. Typical Avalanche Energy Test/Waveforms

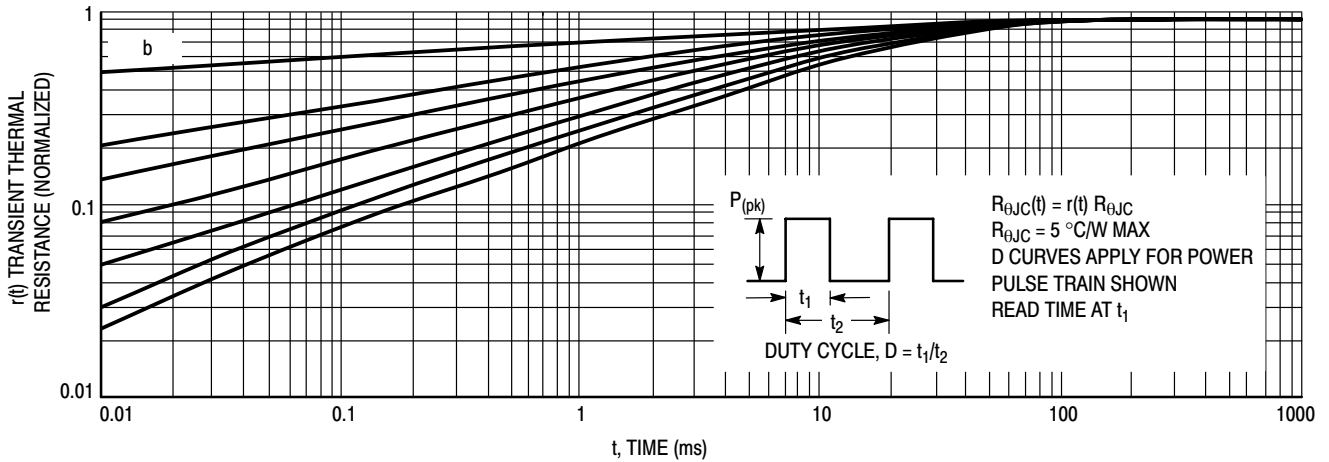


Figure 18. Thermal Response

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