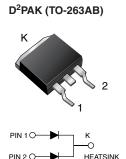
Vishay General Semiconductor

# **Dual Common Cathode Schottky Rectifier**



www.vishay.com

### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 8 A			
V <sub>RRM</sub>	40 V			
I <sub>FSM</sub>	250 A			
V <sub>F</sub>	0.55 V			
T <sub>J</sub> max.	125 °C			
Package	D <sup>2</sup> PAK (TO-263AB)			
Circuit configuration	Common cathode			

#### **FEATURES**

- Power pack
- · Guardring for overvoltage protection
- Low power loss, high efficiency
- Low forward voltage drop
- High forward surge capability
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in low voltage, high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters, and polarity protection application.

### **MECHANICAL DATA**

Case: D<sup>2</sup>PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating

Base P/NHM3 - RoHS-compliant, halogen-free, AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

HM3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

<b>MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	SBLB1640CT	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	40	v	
Working peak reverse voltage		V <sub>RWM</sub>	28		
aximum DC blocking voltage		V <sub>DC</sub>	40		
Maximum average forward rectified current at $T_{C}$ = 95 $^{\circ}\mathrm{C}$	total device	I <sub>F(AV)</sub>	16	A	
	per diode		8.0		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	250		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +125	°C	



COMPLIANT HALOGEN



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# SBLB1640CT

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_c = 25 \ ^{\circ}C$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUE	UNIT	
Maximum instantaneous forward voltage per diode	V <sub>F</sub> <sup>(1)</sup>	8.0 A		0.55	V	
Maximum instantaneous reverse current at DC blocking voltage per diode	I <sub>R</sub> <sup>(2)</sup>	Rated V <sub>R</sub>	T <sub>C</sub> = 25 °C	0.5	- mA	
			T <sub>C</sub> = 100 °C	50		

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_c = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	SBLB1640CT	UNIT		
Typical thermal resistance from junction to case per diode	$R_{ extsf{ heta}JC}$	2.0	°C/W		

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AB	SBLB1640CTHM3/P <sup>(1)</sup>	1.35	Р	50/tube	Tube
TO-263AB	SBLB1640CTHM3/I <sup>(1)</sup>	1.35	l	800/reel	Tape and reel

Note

(1) AEC-Q101 qualified



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### **RATINGS AND CHARACTERISTICS CURVES** ( $T_C = 25$ °C unless otherwise noted)

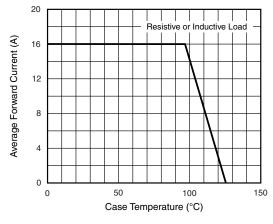


Fig. 1 - Forward Current Derating Curve

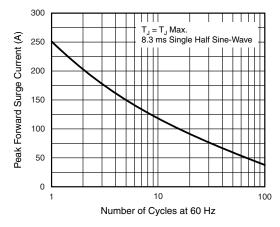


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

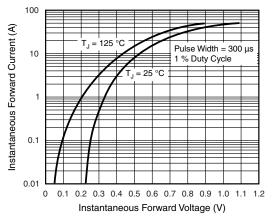


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

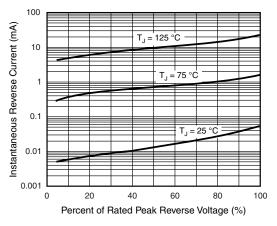


Fig. 4 - Typical Reverse Characteristics Per Diode

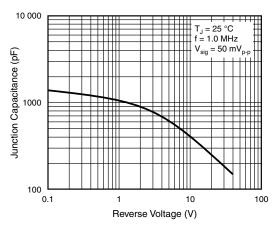


Fig. 5 - Typical Junction Capacitance Per Diode

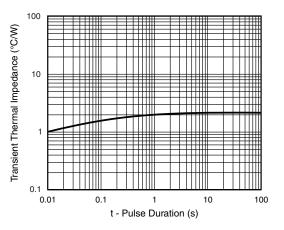


Fig. 6 - Typical Transient Thermal Impedance Per Diode

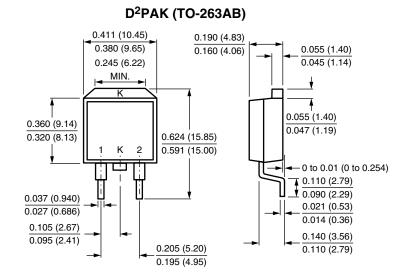
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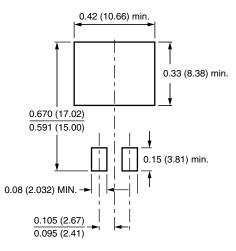


### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



### **Mounting Pad Layout**

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