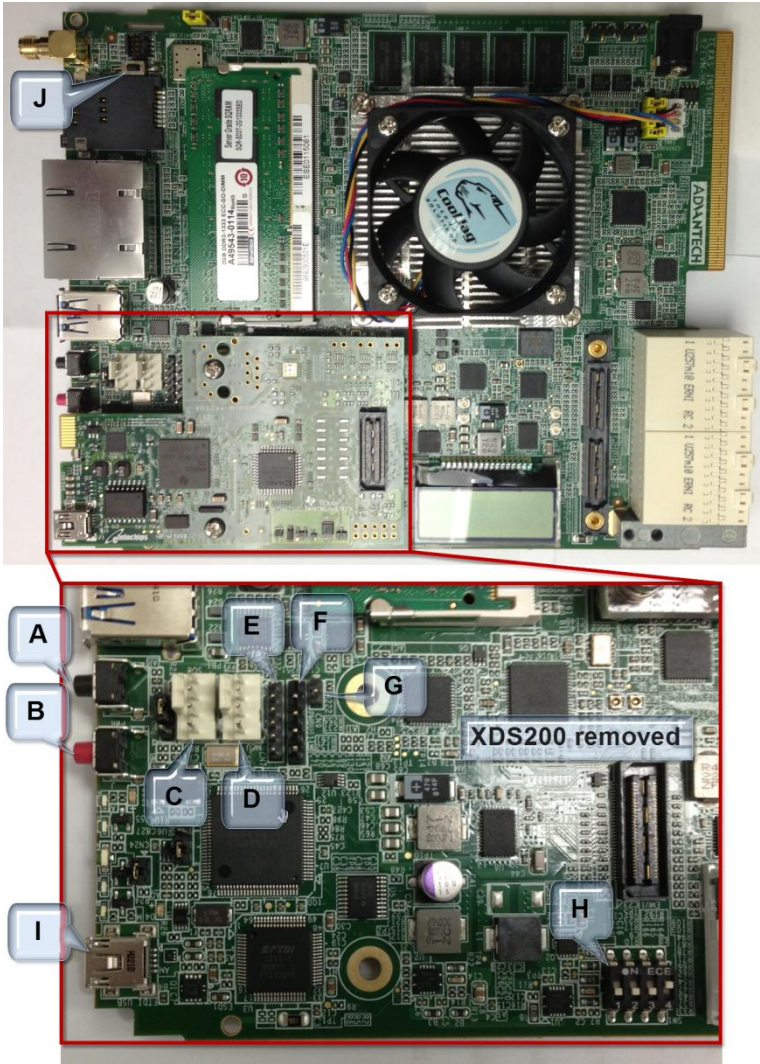




XTCIEVMK2X Evaluation Module Quick Start Guide

XTCIEVMK2X Evaluation Module



| | | | |
|---|---|---|--|
| A | No Functionality | F | Reserve for factory programming |
| B | 1 press: safe shutdown of SOC; 2 presses within 0.5sec: warm reset; 3 presses with 0.5 sec: full reset; 4 presses with 0.5 sec: cancel reset | G | MCU Reset Jumper for BMC field update |
| C | COM2: SoC UART Console | H | Dip switch for boot configuration: 0001: No Boot/JTAG DSP Little Endian Boot mode 0010: Uboot mode |
| D | COM1: MCU UART Console | I | Provide 2 console ports in USB interface (same as "C" and "D") |
| E | Reserve for factory programming | J | MCU Reset: Resets the microcontroller and will reset the entire board |

Two boot procedures are described below, one for Uboot and one for CCS.

1) Uboot Steps:



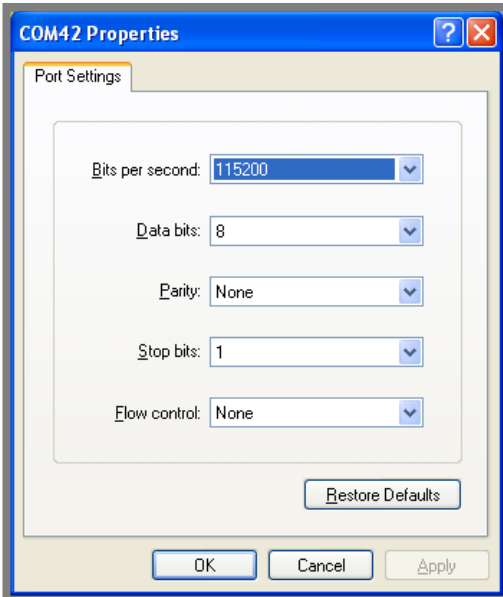
1

Ensure EVM configuration switch are set as shown for NOR SPI Boot.



2

Connect the USB mini-B cable to FTDI_USB.



3

Open HyperTerminal, select COM port, configure settings.



4

Insert DC-in cable to apply 12V power.

```

File Edit Setup Control Window Help
U-Boot 2013.01-00007-g4454041-dirty <Feb 04 2013 - 11:49:45>
I2C: ready
DRAM: 128 MiB
WARNING: Caches not enabled
NAND: 512 MiB
*** Warning - bad CRC, using default environment

Net: Ethernet PHY: 88E1111 @ 0x01
TCI6614-EMAC
Warning: failed to set MAC address

Hit any key to stop autoboot: 0
TCI6638 EUM #

```

5

Hit any key to stop auto-boot when prompted. EVM is now ready for Uboot applications.

MCSDK Downlink Page:

http://software-dl.ti.com/sdoemb/sdoemb_public_sw/mcsdk/latest/index_FDS.html

2) CCS Steps:



1

Update or install CCS (V5.3.0.00090 or later) from MCSDK download page.



2

Ensure EVM configuration switch are set as shown for No Boot/JTAG DSP Little Endian Boot mode.



3

Connect the USB mini-B cable to XDS200.



4

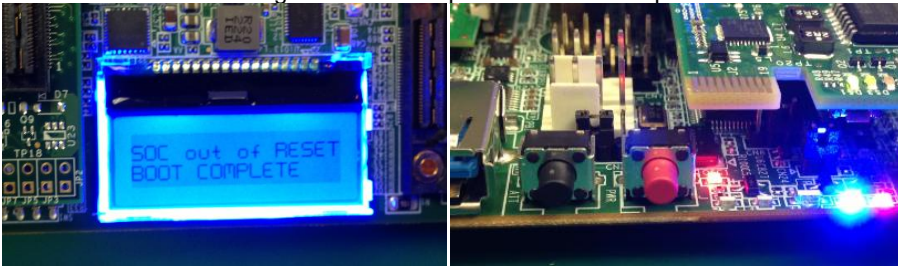
Insert DC-in cable to apply 12V power.

5

Launch CCS. After CCS is opened, EVM is now ready for use.

Note:

1. Refer to the following LCD and LED pictures for correct power-on behavior.



Getting Started:

Please see the 'Getting Started' chapter in the MCSDK User Guide:

<http://processors.wiki.ti.com/index.php/MC>

[SDK User Guide for KeyStone II.](#)

Important Notes when handling the EVM:

- Plug in all the cables before powering ON the EVM to ensure proper grounding.
- Do not unnecessarily flex the boards, while inserting cables. Excessive flexing can break the PCB traces.
- Use proper ESD procedures when handling the EVM.
- Warning: Some components on the EVM may be too hot to touch with your bare hand when power is on.

Online Documentation

- TMS320C663X processor website
<http://www.ti.com/product/tci6634k2k>
<http://www.ti.com/product/tci6636k2h>
<http://www.ti.com/product/tci6638k2k>
- MCSDK website for updates
http://processors.wiki.ti.com/index.php/MCSDK_User_Guide_for_KeyStone_II
- CCS v5
http://processors.wiki.ti.com/index.php/Category:Code_Composer_Studio_v5

C663X EVM Information:

<http://www.advantech.com/Support/TI-EVM/default.aspx>

This link contains:

- Hardware & Software details
- User Guide
- Technical Reference Manual

TCIEVMK2X EVM Support:

Please write to EVM667xsupport@advantech.com.tw for technical queries related to this EVM.



TI Worldwide Technical Support

Internet

TI Semiconductor Product Information Center Home Page
support.ti.com

TI Semiconductor KnowledgeBase Home page
support.ti.com/sc/knowledgebase

Product Information Centers

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Note: The European Free Call (Toll Free) number is not active in all countries. If you have technical difficulty calling the free call number, please use the international number above.

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India 1-800-425-7888 Singapore 800-886-1028

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