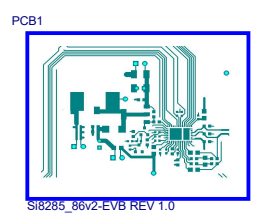
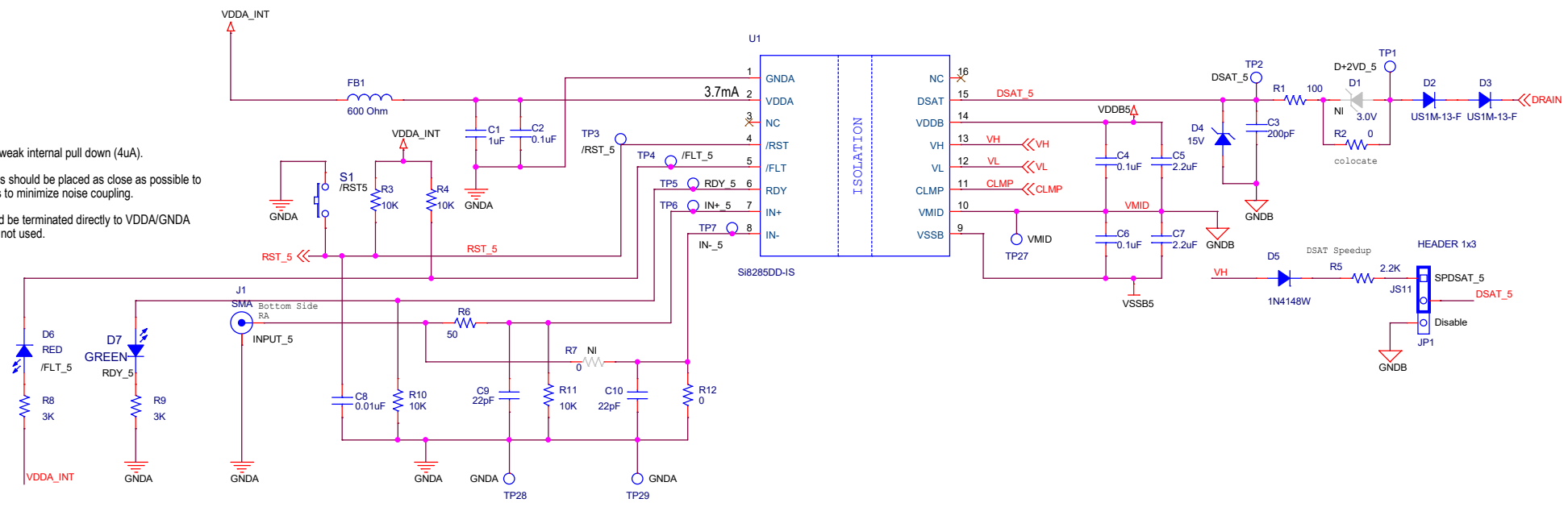



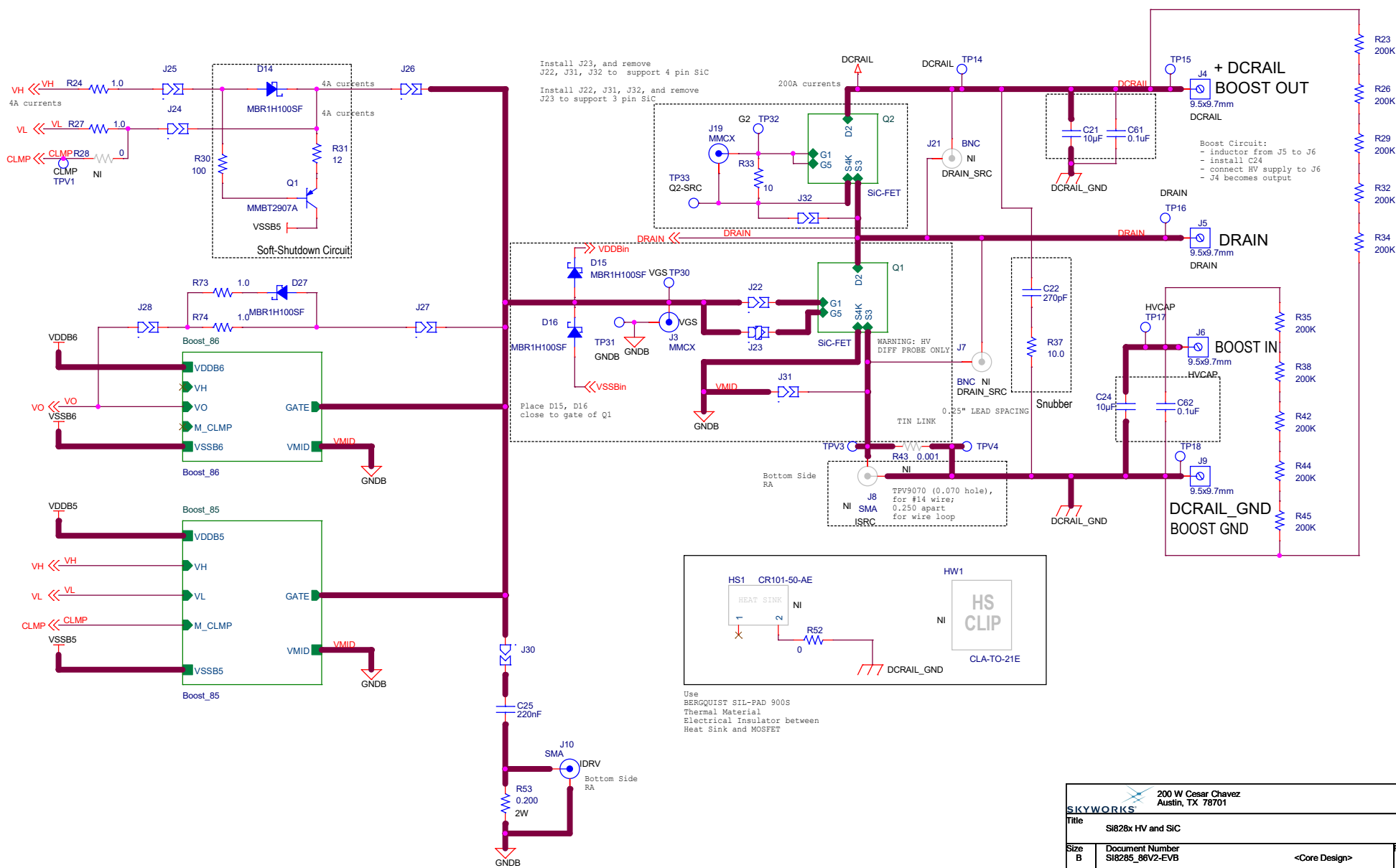
IN+/IN- have weak internal pull down (4uA).

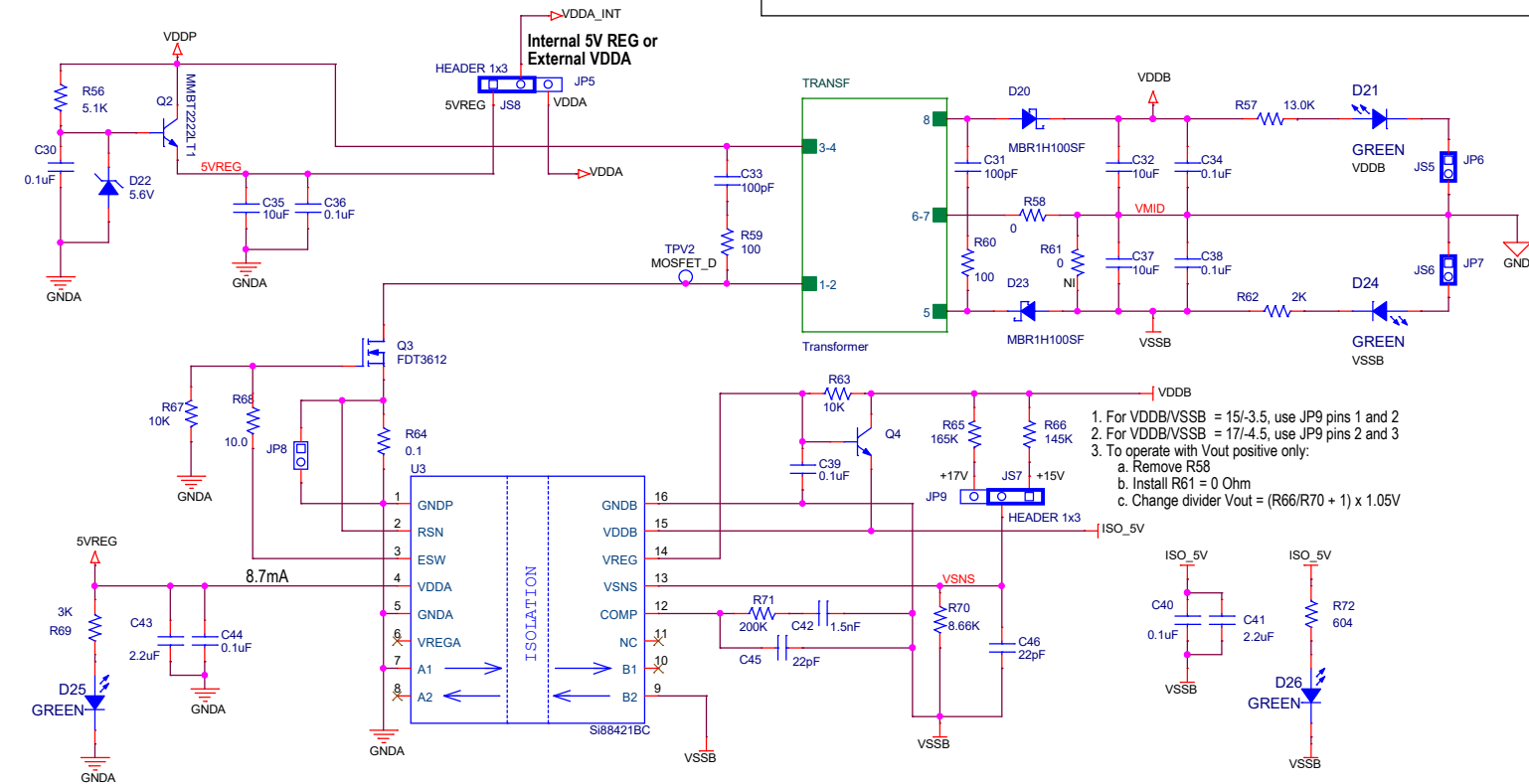
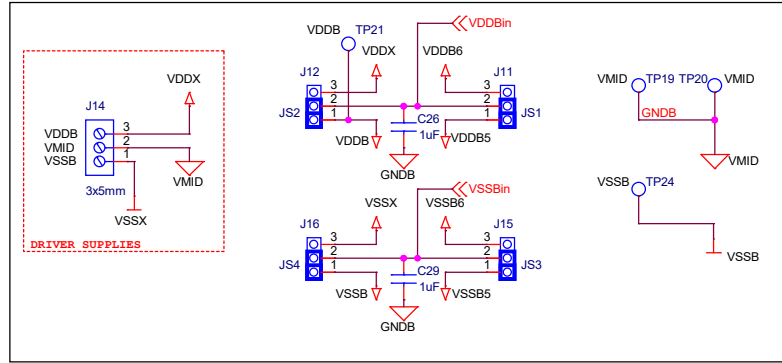
Signal sources should be placed as close as possible to IN+/IN- inputs to minimize noise coupling.

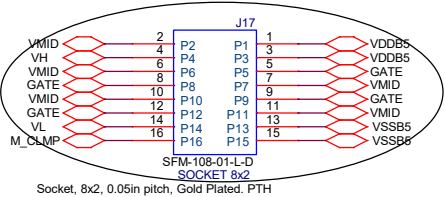
IN+/IN- should be terminated directly to VDDA/GNDA accordingly if not used.




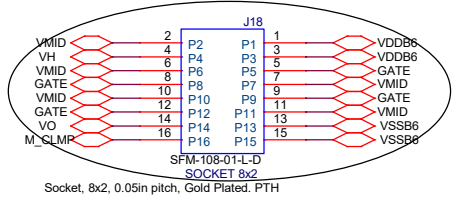
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Size B	Document Number Si8285_86V2-EVB	<Core Design>	Rev 2.0
Date:	Thursday, January 13, 2022	Sheet 1 of 11	




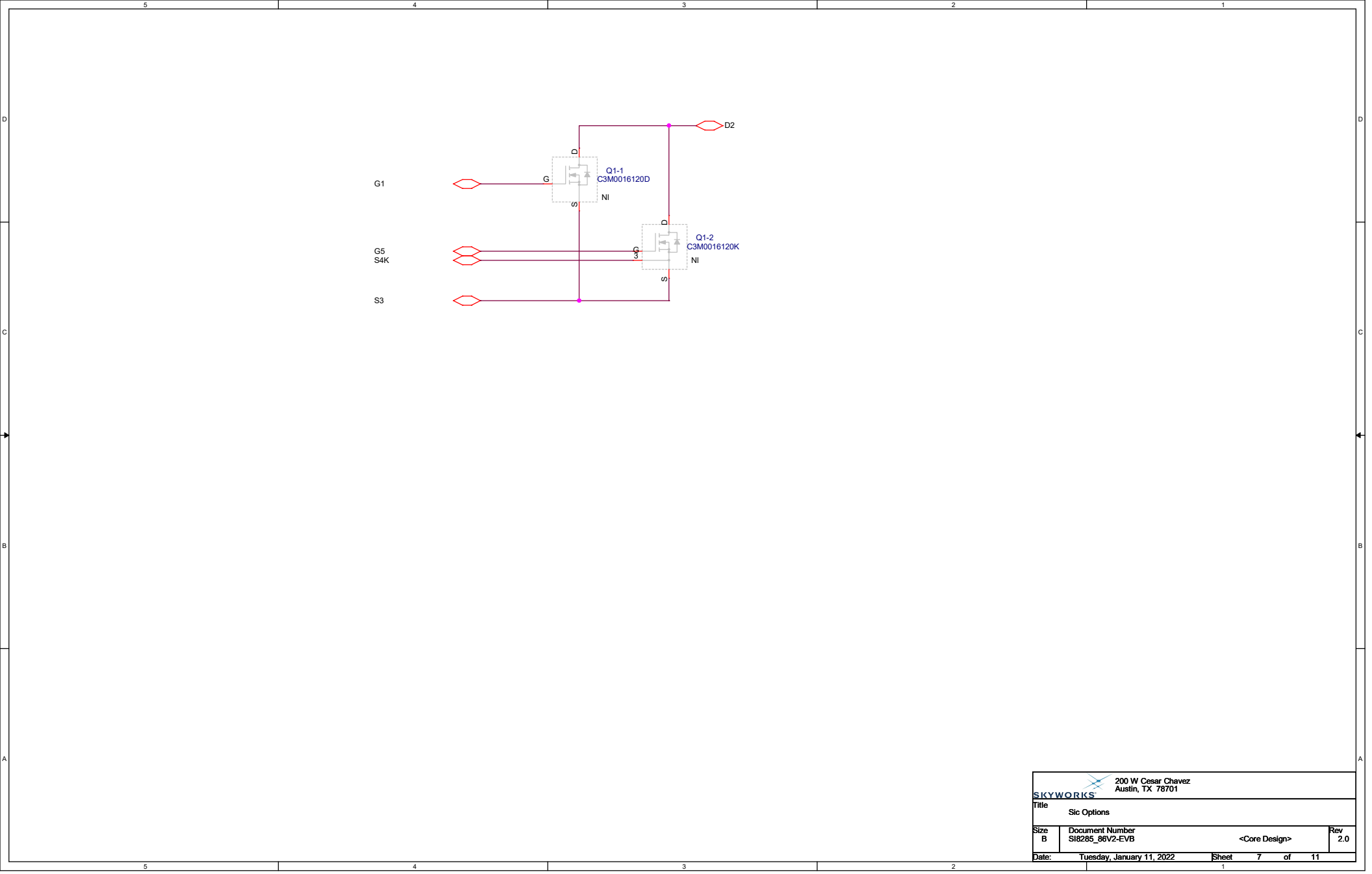




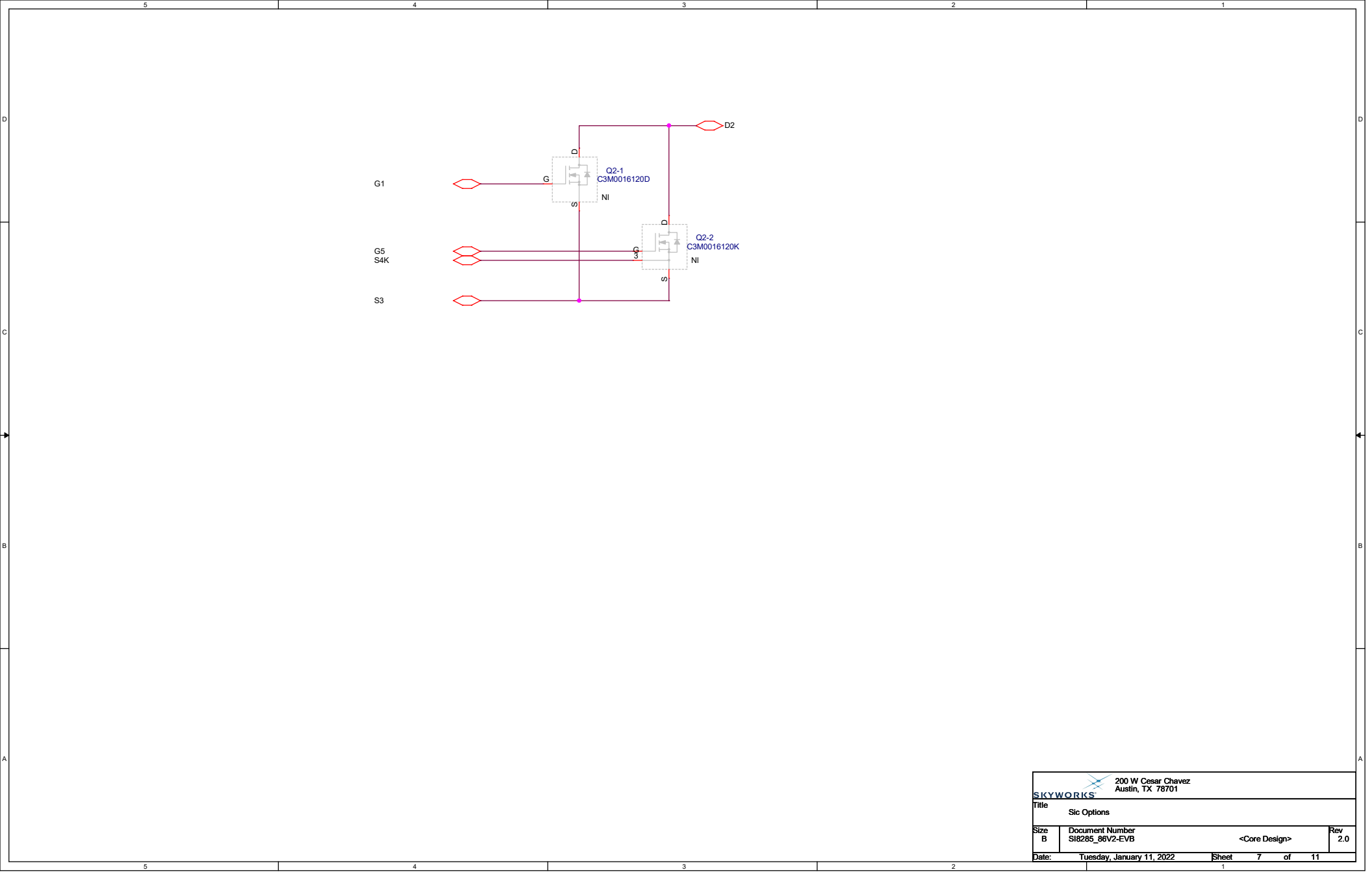
 200 W Cesar Chavez Austin, TX 78701			
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Size B	Document Number SI8285_86V2-EVB	<Core Design>	Rev 2.0
Date:	Thursday, October 07, 2021	Sheet 5	of 11



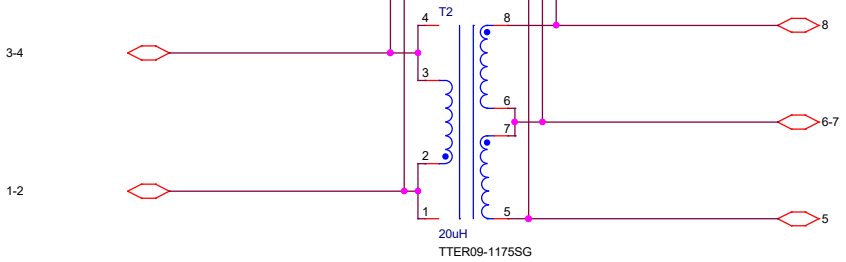
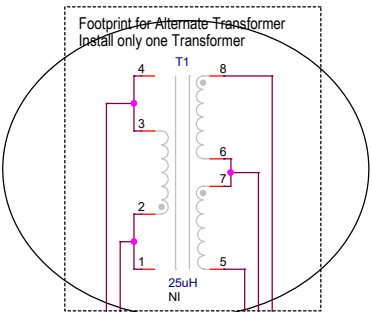
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Title			
SI8286 Boost Circuit Socket			
Size	Document Number	<Core Design>	Rev
B	SI8285_86V2-EVB		2.0
Date:	Thursday, October 07, 2021	Sheet	6 of 11




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Sic Options			
Size B	Document Number SI8285_86V2-EVB	<Core Design>	Rev 2.0
Date:	Tuesday, January 11, 2022	Sheet 7	of 11



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Size B	Document Number SI8285_86V2-EVB	<Core Design>	Rev 2.0
Date:	Tuesday, January 11, 2022	Sheet 7	of 11



 200 W Cesar Chavez Austin, TX 78701			
Title Transformer Options			
Size B	Document Number SI8285_86V2-EVB	<Core Design>	Rev 2.0
Date:	Wednesday, January 12, 2022	Sheet 9	of 11