



# PJL9808

## 30V Dual N-Channel Enhancement Mode MOSFET

**Voltage**

**30 V**

**Current**

**8 A**

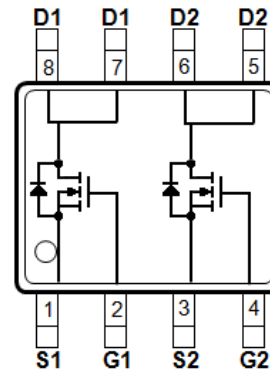
### Features

- $R_{DS(ON)}, V_{GS}@10V, I_D@8A < 19m\Omega$
- $R_{DS(ON)}, V_{GS}@4.5V, I_D@5A < 29m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive)
- Green molding compound as per IEC61249 Std.. (Halogen Free)

### Mechanical Data

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams

SOP-8



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	+20	V
Continuous Drain Current	$T_A=25^\circ\text{C}$	$I_D$	8.0	A
	$T_A=70^\circ\text{C}$		6.0	
Pulsed Drain Current <sup>(Note 1)</sup>		$I_{DM}$	32	
Power Dissipation	$T_A=25^\circ\text{C}$	$P_D$	1.25	W
	$T_A=70^\circ\text{C}$		0.8	
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	$^\circ\text{C}$
Typical Thermal Resistance - Junction to Ambient <sup>(Note 5)</sup>		$R_{\theta JA}$	100	$^\circ\text{C/W}$



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## Electrical Characteristics (T<sub>A</sub>=25<sup>o</sup>C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.2	1.7	2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =8A	-	16	19	mΩ
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	23	29	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>Dynamic</b> (Note 6)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =8A, V <sub>GS</sub> =4.5V (Note 2,3)	-	4.3	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.3	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.6	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHZ	-	392	-	pF
Output Capacitance	C <sub>oss</sub>		-	76	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	54	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω (Note 2,3)	-	5.9	-	ns
Turn-On Rise Time	tr		-	11	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	17	-	
Turn-Off Fall Time	tf		-	3.8	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	8.0	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1.0A, V <sub>GS</sub> =0V	-	0.73	1.0	V

**NOTES :**

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature T<sub>J</sub>(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
5. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.



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## TYPICAL CHARACTERISTIC CURVES

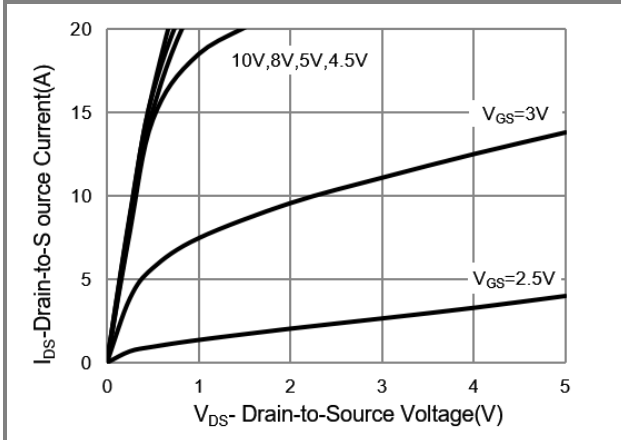


Fig.1 On-Region Characteristics

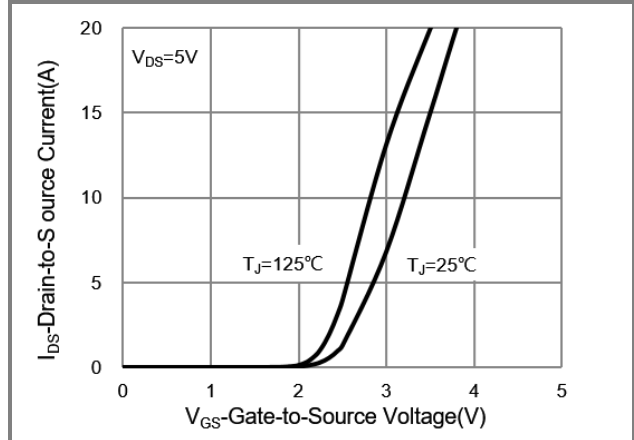


Fig.2 Transfer Characteristics

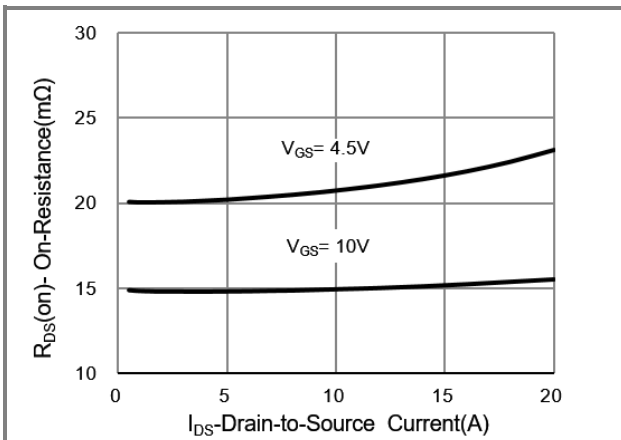


Fig.3 On-Resistance vs. Drain Current

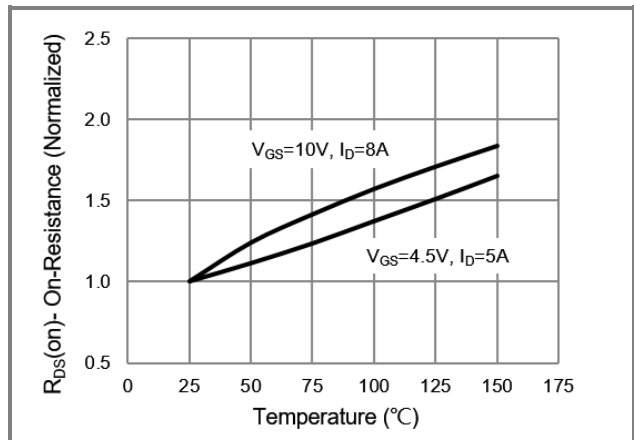


Fig.4 On-Resistance vs. Junction temperature

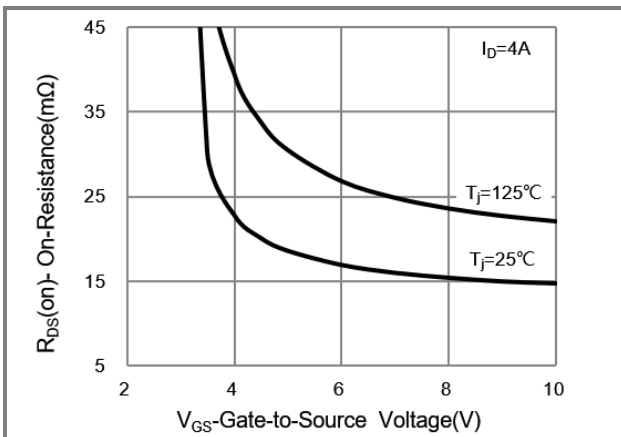


Fig.5 On-Resistance Variation with VGS.

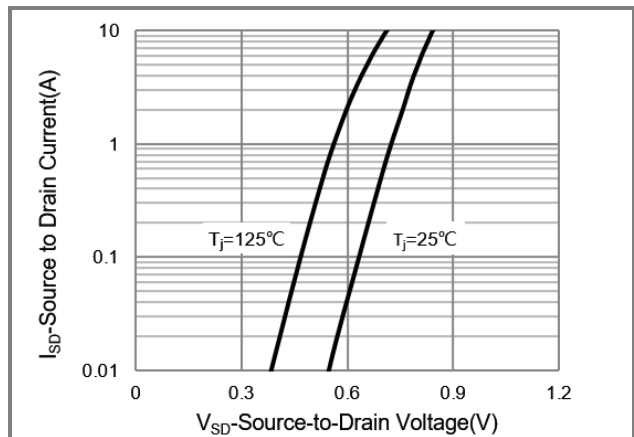


Fig.6 Body Diode Characteristics



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## TYPICAL CHARACTERISTIC CURVES

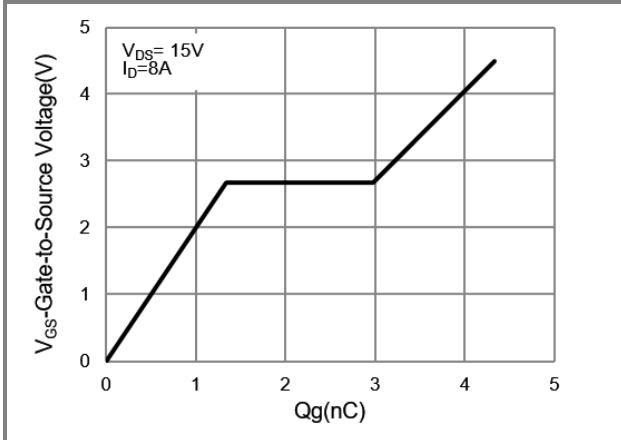


Fig.7 Gate-Charge Characteristics

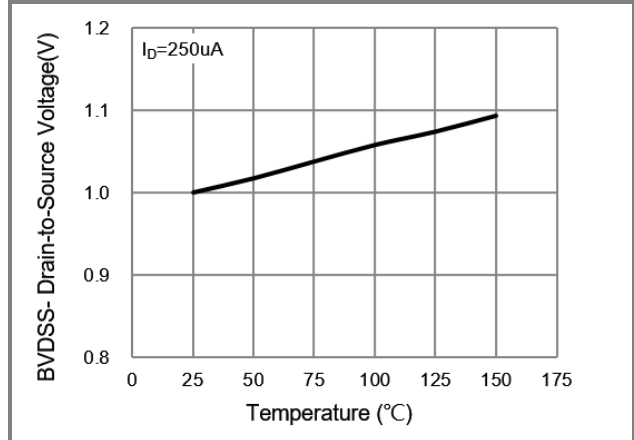


Fig.8 Breakdown Voltage Variation vs. Temperature

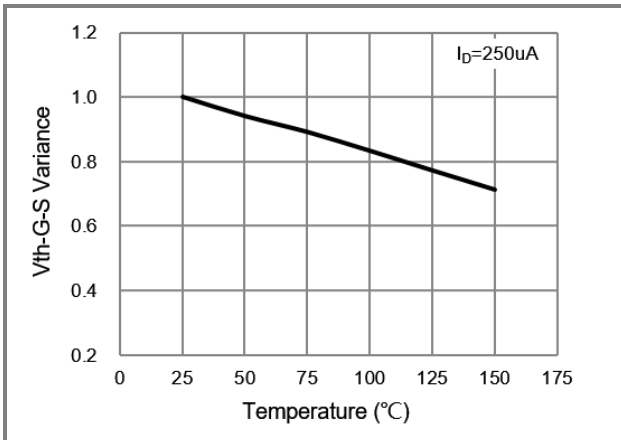


Fig.9 Threshold Voltage Variation with Temperature.

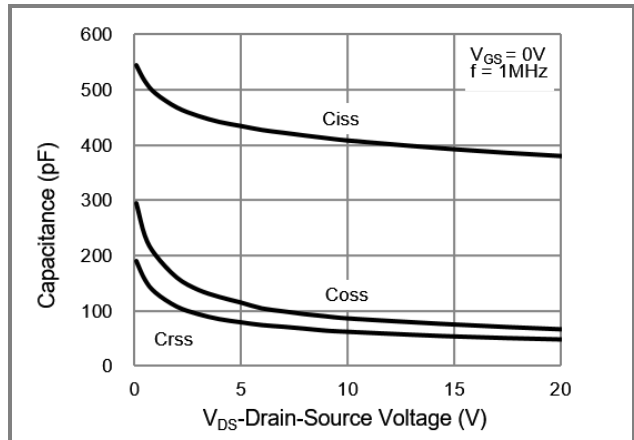


Fig.10 Capacitance vs. Drain-Source Voltage.

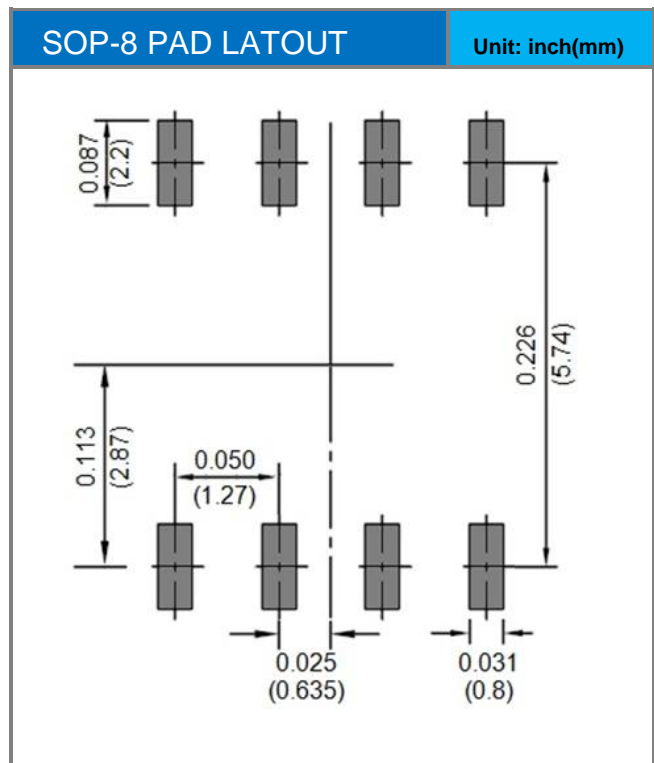
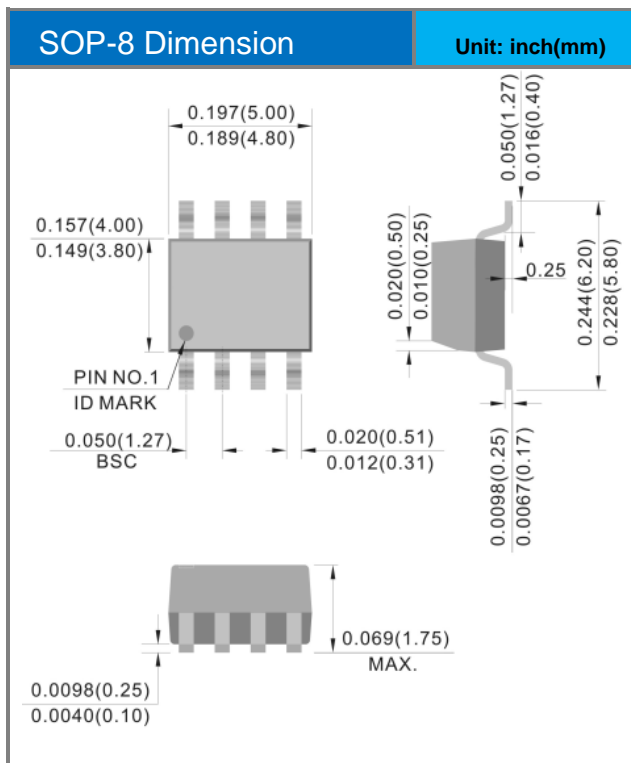


# PJL9808

## Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9808_R2_00001	SOP-8	2.5K pcs / 13" reel	L9808	Halogen free

## Packaging Information & Mounting Pad Layout





## PJL9808

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