



High Speed USB 2.0 1:2 Mux/DeMux

Features

- Differential Bidirectional 2:1 Mux/DeMux
- Wide Input Voltage Range: 0 to 3.6V
- Mobile Hi-Definition Link (MHL) Switch:
 - □ Bandwidth (-3dB): 5.3GHz
 - R_{ON} (Typical): 5.7Ω
 - C_{ON} (Typical): 1.5pF @ 240MHz
- USB Switch:
 - □ Bandwidth (-3dB): 5.5GHz
 - R_{ON} (Typical): 4.6Ω
 - C_{ON} (Typical): 1.5pF @ 240MHz
- Low Propagation Delay, 0.1ns typ
- Low Off-Isolation: -34dB @ 240MHz
- Low Crosstalk: -37dB @ 240MHz
- Low Power Consumption: 35µA typical
- Wide Supply Voltage: 1.8V to 5.5V
- Supports 1.8V Logic on Control Pins
- Protection Feature
 - Power-off protection for minimizing current leakage in power-down mode
 - Connector pins are high voltage tolerated
 - D± tolerance to 9V
 - Overvoltage protection at D±
- Wide Temperature Range: -40°C to 85°C
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.
 - https://www.diodes.com/quality/product-definitions/
- Packaging (Pb-free & Green):
 - □ 10-contact, UQFN (ZUA), 1.5x2mm, 0.5mm(H), 0.6mm pitch

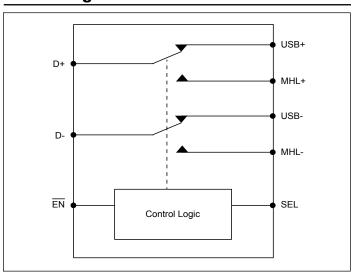
Description

The PI3USB3000 is a 2-to-1 differential channel multiplexer/demultiplexer switch. The D \pm pins can tolerate voltages up to 9V. Overvoltage protection (OVP) is implemented at 4.75V to immediately switch off the channels when an overvoltage condition is detected. The PI3USB3000 can pass USB 2.0 and MHL signals with a bandwidth of 5.5GHz to provide excellent signal integrity and eye diagram opening.

Applications

- Smart Phones
- USB-C* Applications
- Tablets
- NBs
- PCs

Block Diagram



USB Type-C is a registered trademark of USB Implementers Forum.

Notes:

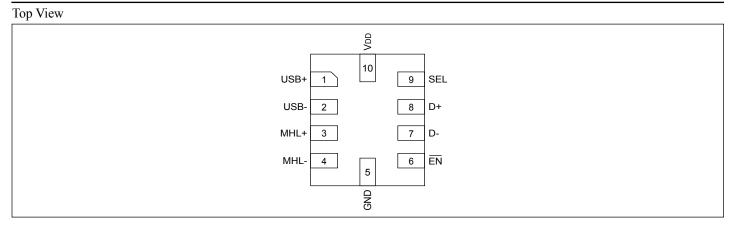
- $1.\ No\ purposely\ added\ lead.\ Fully\ EU\ Directive\ 2002/95/EC\ (RoHS),\ 2011/65/EU\ (RoHS\ 2)\ \&\ 2015/863/EU\ (RoHS\ 3)\ compliant.$
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

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Pin Configuration



Pin Description

Pin#	Pin Name	Signal Type	Description					
8,	D+,	I/O	ional I/O Common Post					
7	D-	1/0	Signal I/O, Common Port					
3,	MHL+,	I/O	Signal I/O MIII Channal					
4	MHL-	I/O	Signal I/O, MHL Channel					
1,	USB+,	I/O	Signal I/O LISP Channel					
2	USB-	I/O	Signal I/O, USB Channel					
9	SEL	I	Operation Mode Select (when SEL = L: D \pm \Rightarrow USB \pm , when SEL = H: D \pm \Rightarrow MHL \pm)					
10	V_{DD}	PWR	Positive Supply Voltage					
5	GND	PWR	Power Ground					
6	EN	I	$\overline{\rm EN}$ = 1, Chip is Power Down. $\overline{\rm EN}$ = 0, Chip is Enabled, Please refer to Truth Table.					

Truth Table

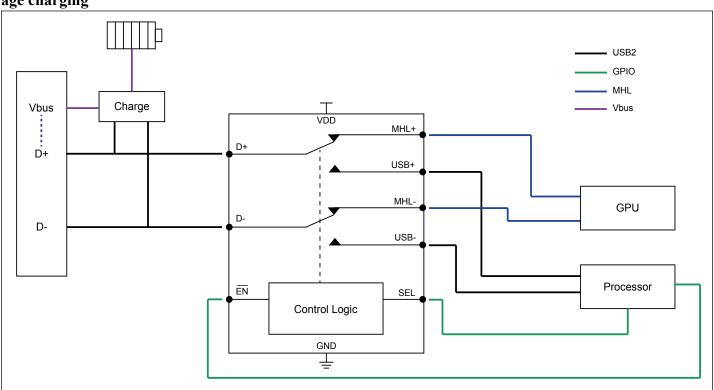
Function	SEL
D+/- to USB+/-	L
D+/- to MHL+/-	Н

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PI3USB3000 Application in MHL Switching and provide overvoltage protection for D+/- when high voltage charging







Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C
Supply Voltage (VDD) to Ground Potential0.3V to +6V
Channel Input/Output Voltage (USB±/MHL±)0.3V to +5V
Channel Input/Output Voltage (D±)0.3V to +9V
Control Pins Input Voltage (EN/SEL)0.3V to +6V
ESD (All Pins)2KV (HBM) and 1KV (CDM)
Channel Input/Output Current (D±→USB±, D±→MHL±)±20mA
Junction Temperature
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Note

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions

Symbol	Description	Test Conditions	Min.	Тур.	Max.	Units
V_{DD}	Power Supply		1.8	3.3	5.5	V
V _{I/O}	Analog Voltage Range		0		3.6	V
$V_{\rm I}$	Voltage Range for Control Pins		0		5.5	V
I_{DD}	Current Consumption in Normal Operation	$V_{\rm IO}$ =0V, SEL = GND or $V_{\rm DD}$, chip enabled		35	55	μА
I _{DD_OVP}	Current Consumption in OVP	$V_{D\pm}$ = 5.5V, SEL = GND or V_{DD} , chip enabled		35		μА
I_{DDQ}	Chip Disabled Current Consumption	V_{IO} = 0V, SEL = GND or V_{DD} , \overline{EN} = High		1	2	μА
T_A	Operating Temperature Range		-40		85	°C

DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}\text{C} \text{ to } 85^{\circ}\text{C}, \text{ Typical values are at V}_{DD} = 3.3\text{V}, T_A = 25^{\circ}\text{C}, \overline{\text{EN}} = 0\text{V} \text{ (unless otherwise noted)})$

Parameter	Description	Test Condi	tions	Min.	Тур.	Max.	Units
Control Pins	– EN/SEL						
V _{IH} - cntrl signals	Input HIGH Voltage for SEL and EN	$V_{\rm DD} = 1.8 - 5$.5V	1.3			V
V _{IL} - cntrl signals	Input LOW Voltage for SEL and $\overline{\text{EN}}$	V _{DD} = 1.8 - 5.5V				0.6	V
I_{IH}	Input HIGH Current for SEL and EN	$V_{\rm I} = 5.5 \rm V$		-1		1	μΑ
I_{IL}	Input LOW Current for SEL and EN	$V_{\rm I} = 0V$		-1		1	μA
Over Voltage	Protection						
$V_{OVP_D\pm}$	D± OVP Trigger Voltage			4.6	4.75	5.0	V
MHL Switch							
D	ON A P C	$V_{\rm DD} = 2.7 \mathrm{V}$	$V_{I/O} = 1.65V$, $I_{ON} = -8mA$		5.7	9	
R _{ON}	ON-state Resistance	$V_{\rm DD} = 1.8V$	$V_{I/O} = 1.65V, I_{ON} = -8mA$		5.7	9.5	Ω





DC Electrical Characteristics for Switching over Operating Range Cont.

Parameter	Description	Test Conditions		Min.	Тур.	Max.	Units
$\Delta R_{ m ON}$	ON-state Resistance match between + and - paths	$V_{\mathrm{DD}} = 1.8 \mathrm{V}$	$V_{I/O} = 1.65V, I_{ON} = -8mA$		0.1		Ω
R _{ON(FLAT)}	ON-state Resistance Flatness	$V_{\mathrm{DD}} = 1.8 \mathrm{V}$	V _{I/O} = 1.65V to 3.45V, I _{ON} = -8mA		1		Ω
I_{OZ}	OFF Leakage Current	$V_{\mathrm{DD}} = 4.8 \mathrm{V}$	Switch OFF, $V_{MHL\pm} = 1.65V$ to 3.45V, $V_{D\pm} = 0V$	-2		2	μΑ
I_{OFF}	Power-off Leakage Current	$V_{\mathrm{DD}} = 0 \mathrm{V}$	Switch ON or OFF, $V_{MHL\pm}$ = 1.65V to 3.45V, $V_{D\pm}$ = NC	-10		10	μA
T	ON Leakage Current	$V_{\mathrm{DD}} = 4.8 \mathrm{V}$	Switch ON, $V_{MHL\pm}$ = 1.65V to 3.45V, $V_{D\pm}$ = NC	-2		2	μА
I_{ON}		$V_{\mathrm{DD}} = 1.8 \mathrm{V}$	Switch ON, $V_{MHL\pm}$ = 1.65V to 3.45V, $V_{D\pm}$ = NC	-125		125	
USB Switch							
R _{ON}	ON-state Resistance	$V_{\mathrm{DD}} = 1.8 \mathrm{V}$	$V_{I/O} = 0.4V$, $I_{ON} = -8mA$		4.6	7.5	Ω
$\Delta R_{ m ON}$	ON-state Resistance match between + and - paths	$V_{\mathrm{DD}} = 1.8 \mathrm{V}$	$V_{I/O} = 0.4V$, $I_{ON} = -8mA$		0.1		Ω
R _{ON(FLAT)}	ON-state Resistance Flatness	$V_{\mathrm{DD}} = 1.8 \mathrm{V}$	$V_{\rm I/O}$ = 0V or 0.4V, $I_{\rm ON}$ = -8mA		1		Ω
I_{OZ}	OFF Leakage Current	$V_{\mathrm{DD}} = 4.8 \mathrm{V}$	Switch OFF, $V_{USB\pm} = 0V$ to 3.6V, $V_{D\pm} = 0V$	-2		2	μА
I _{OFF}	Power-off Leakage Current	$V_{\mathrm{DD}} = 0 \mathrm{V}$	Switch ON or OFF, $V_{USB\pm}$ = 0V to 3.6V, $V_{D\pm}$ = NC	-10		10	μА
_		$V_{\mathrm{DD}} = 4.8 \mathrm{V}$	Switch ON, $V_{USB\pm} = 0V$ to 3.6V, $V_{D\pm} = NC$	-2		2	
I _{ON}	ON Leakage Current	$V_{\mathrm{DD}} = 1.8 \mathrm{V}$	Switch ON, $V_{USB\pm} = 0V$ to 3.6V, $V_{D\pm} = NC$	-125		125	μΑ

Dynamic Electrical Characteristics

 $(T_A = -40^{\circ}\text{C} \text{ to } 85^{\circ}\text{C}, \text{ Typical values are at V}_{DD} = 3.3\text{V}, T_A = 25^{\circ}\text{C}, \text{ (unless otherwise noted))}$

Parameter	Description	Test Cond	Test Conditions		Тур.	Max.	Units
C _{ON(MHL)}	MHL path ON Capacitance	Switch ON	$V_{\rm DD} = 3.3 {\rm V}, V_{\rm I/O} = 0 {\rm or} 3.3 {\rm V}, f = 240 {\rm MHz}$		1.5	2	pF
C _{ON(USB)}	USB path ON Capacitance	Switch ON	$V_{\rm DD} = 3.3 \text{V}, V_{\rm I/O} = 0 \text{ or } 3.3 \text{V}, f = 240 \text{MHz}$		1.5	2	pF
C _{OFF(MHL)}	MHL path OFF Capacitance	Switch OFF	$V_{\rm DD} = 3.3 \text{V}, V_{\rm I/O} = 0 \text{ or } 3.3 \text{V}, f = 240 \text{MHz}$		1.5	2	pF
C _{OFF(USB)}	USB path OFF Capacitance	Switch OFF	$V_{\rm DD}$ = 3.3V, $V_{\rm I/O}$ = 0 or 3.3V, f = 240MHz		1.5	2	pF
$C_{\rm I}$	Digital Input Capacitance		$V_{\rm DD} = 3.3 \text{V}, V_{\rm I} = 0 \text{ or } 2 \text{V}$		2.2		pF





Dynamic Electrical Characteristics Cont.

Parameter	Description	Test Conditions		Min.	Тур.	Max.	Units
O _{IOS}	OFF Isolation	Switch OFF	$R_L = 50\Omega$, $f = 240MHz$		-34		dB
X_{TALK}	Crosstalk	Switch ON	$R_L = 50\Omega$, $f = 240MHz$		-37		dB
B _{W(MHL)}	MHL path -3dB Bandwidth	Switch ON	$R_L = 50\Omega$		5.3		GHz
B _{W(USB)}	USB path -3dB Bandwidth	Switch ON	$R_L = 50\Omega$		5.5		GHz

Switching Characteristics(1)

 $(T_A = -40^{\circ}C \text{ to } 85^{\circ}C, \text{ Typical values are at V}_{DD} = 3.3V, T_A = 25^{\circ}C, \text{ (unless otherwise noted))}$

Parameter	Description	Test Conditions		Тур.	Max.	Units
$t_{\rm OVP}$	OVP Response Time ⁽¹⁾	$R_{USB/MHL} = 600\Omega$, time from the voltage on D± = $4\sim6V$ to the voltage on USB/MHL± = $4.75V$		0.5	1	μs
t _{PZH} , t _{PZL}	Line Enable Time (SEL to Output)				600	ns
$t_{\mathrm{PHZ}}, t_{\mathrm{PLZ}}$	Line Disable Time	See Test Circuit for Electrical Character-		50		ns
t _{Pd}	Propagation Delay	istics		100		ps
t _{b-b}	Bit-to-bit Skew Within the Same Differential Pair ⁽¹⁾			8	20	ps
Ton	Device Enable Time			100		μs
$T_{ m off}$	Device Disable Time			50		ns

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Note:

1. Guaranteed by design.





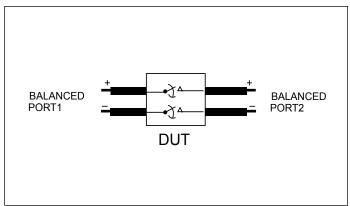


Figure 1. Differential Insertion Loss Setup

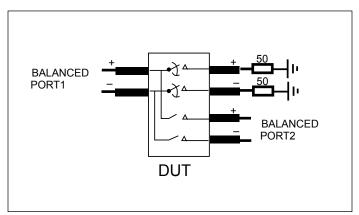


Figure 3. Crosstalk Setup

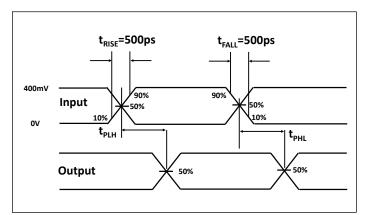


Figure 5. Skew Test

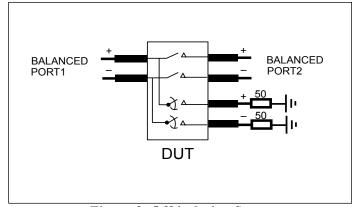


Figure 2. Off-isolation Setup

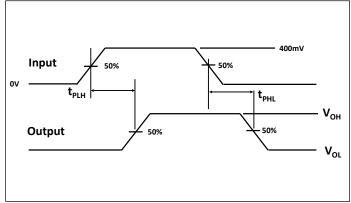
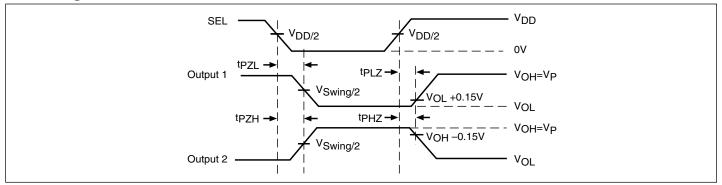


Figure 4. Propagation Delay



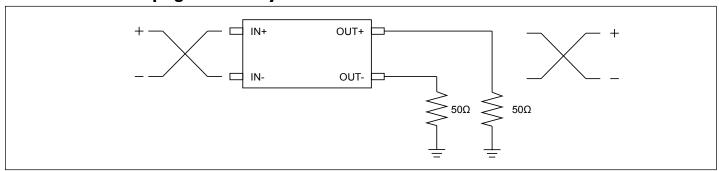


Switching Waveforms

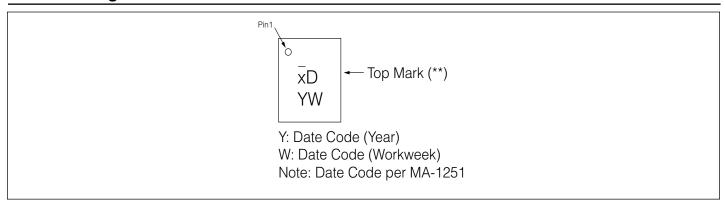


Voltage Waveforms Enable and Disable Times

Test Circuit for Propagation Delay



Part Marking

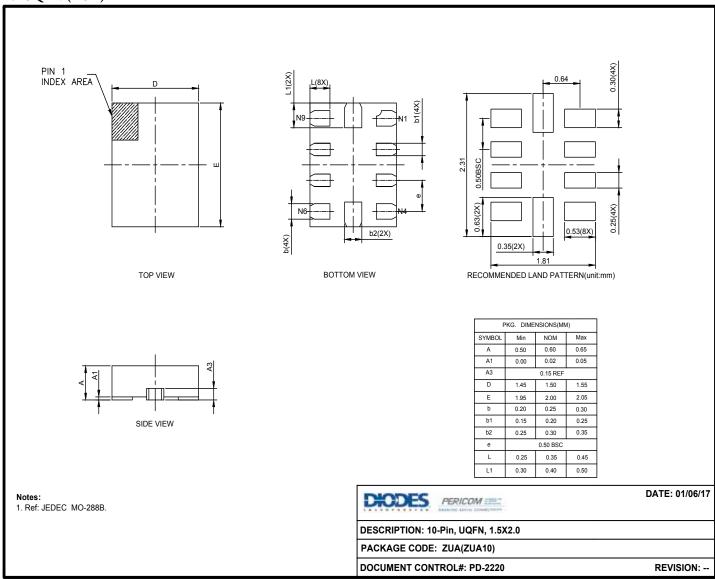






Packaging Mechanical

10-UQFN (ZUA)



17-0002

For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Package Code	Package Description
PI3USB3000ZUAEX	ZUA	10-Pin, 1.5x2.0 (UQFN)

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





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