
# PESD27VV1BA

## Bidirectional ESD protection diode

**25 June 2019** **Product data sheet**

## 1. General description

Bidirectional ElectroStatic Discharge (ESD) protection diode in very small SOD323 (SC-76) Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

## 2. Features and benefits

- Reverse stand-off voltage: $V_{RWM}$ = 27 V
- Low clamping voltage: $V_{CL}$ = 36 V at $I_{PP}$ = 3 A
- ESD protection up to 30 kV (IEC 61000-4-2)
- Ultra low leakage current: $I_{RM}$ < 1 nA
- AEC-Q101 qualified

## 3. Applications

ESD protection for low-speed interfaces in automotive, communication, consumer and computing devices.

## 4. Quick reference data

**Table 1. Quick reference data**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $V_{RWM}$ | reverse standoff voltage | $T_{amb}$ = 25 °C | | - | - | 27 | V |
| $C_d$ | diode capacitance | f = 1 MHz; $V_R$ = 0 V; $T_{amb}$ = 25 °C | | - | 14 | 17 | pF |

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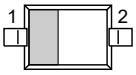
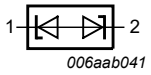
## 5. Pinning information

**Table 2. Pinning information**

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|---|---|---|---|---|
| 1 | K1 | cathode (diode 1) |  SOD323 |  |
| 2 | K2 | cathode (diode 2) | | |

## 6. Ordering information

**Table 3. Ordering information**

| Type number | Package | | |
|---|---|---|---|
| | Name | Description | Version |
| PESD27VV1BA | SOD323 | plastic, surface-mounted package; 2 leads; 1.3 mm pitch; 1.7 mm x 1.25 mm x 0.95 mm body | SOD323 |

## 7. Marking

**Table 4. Marking codes**

| Type number | Marking code |
|---|---|
| PESD27VV1BA | EG |

## 8. Limiting values

**Table 5. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|---|---|---|---|---|---|---|
| $I_{PPM}$ | rated peak pulse current | $t_p$ = 8/20 µs | [1] | - | 3 | A |
| $T_j$ | junction temperature | | | - | 150 | °C |
| $T_{amb}$ | ambient temperature | | | -55 | 150 | °C |
| $T_{stg}$ | storage temperature | | | -65 | 150 | °C |
| **ESD maximum ratings** | | | | | | |
| $V_{ESD}$ | electrostatic discharge voltage | IEC 61000-4-2; contact discharge | [2] | - | 30 | kV |
| | | IEC 61000-4-2; air discharge | [2] | - | 30 | kV |

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
[2] Device stressed with ten non-repetitive ESD pulses.

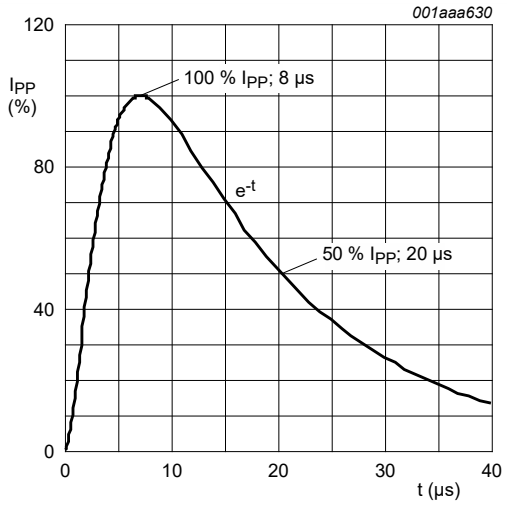



**Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5**

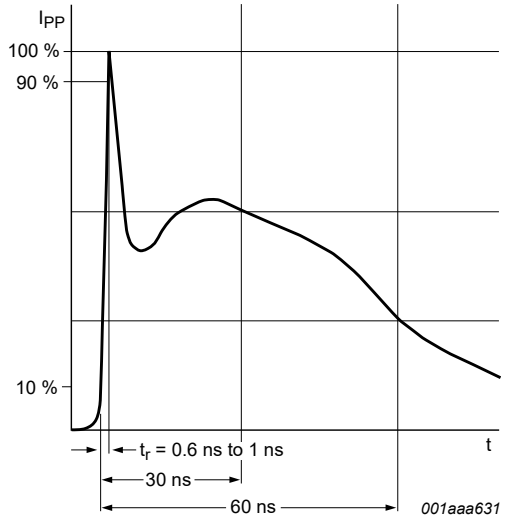



**Fig. 2. ESD pulse waveform according to IEC 61000-4-2**

# 9. Characteristics

**Table 6. Characteristics**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $V_{RWM}$ | reverse standoff voltage | $T_{amb}$ = 25 °C | | - | - | 27 | V |
| $V_{BR}$ | breakdown voltage | $I_R$ = 10 mA; $T_{amb}$ = 25 °C | | 28 | 33 | 38 | V |
| $I_{RM}$ | reverse leakage current | $V_R$ = 27 V; $T_{amb}$ = 25 °C | | - | 1 | 50 | nA |
| $C_d$ | diode capacitance | f = 1 MHz; $V_R$ = 0 V; $T_{amb}$ = 25 °C | | - | 14 | 17 | pF |
| $V_{CL}$ | clamping voltage | $I_{PPM}$ = 1 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C | [1] | - | 34 | 43 | V |
| | | $I_{PPM}$ = 3 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C | [1] | - | 36 | 45 | V |
| | | $I_{PP}$ = 16 A; $t_p$ = TLP; $T_{amb}$ = 25 °C | [2] | - | 35 | - | V |
| $R_{dyn}$ | dynamic resistance | $I_R$ = 10 A; $T_{amb}$ = 25 °C | [2] | - | 0.2 | - | Ω |

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
[2] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

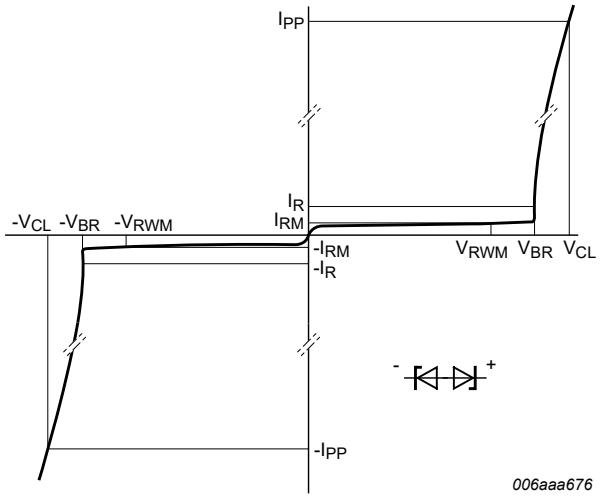



**Fig. 3. V-I characteristics for a bidirectional ESD protection diode**

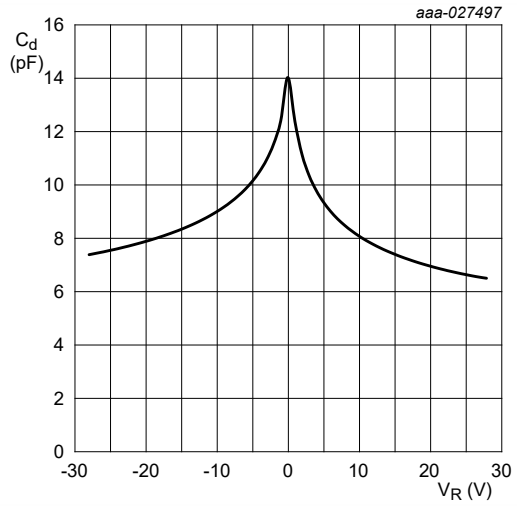
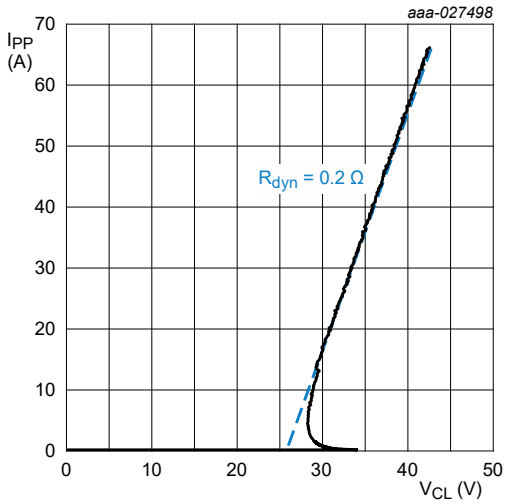
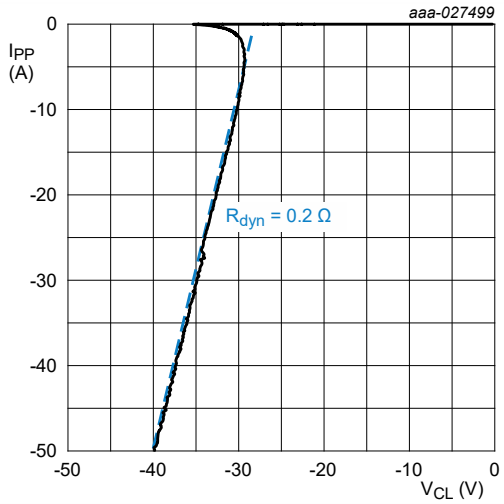



**Fig. 4. Diode capacitance as a function of reverse voltage; typical values**




$t_p$ = 100 ns; Transmission Line Pulse (TLP)

**Fig. 5. Positive clamping voltage (TLP); typical values**




$t_p$ = 100 ns; Transmission Line Pulse (TLP)

**Fig. 6. Negative clamping voltage (TLP); typical values**

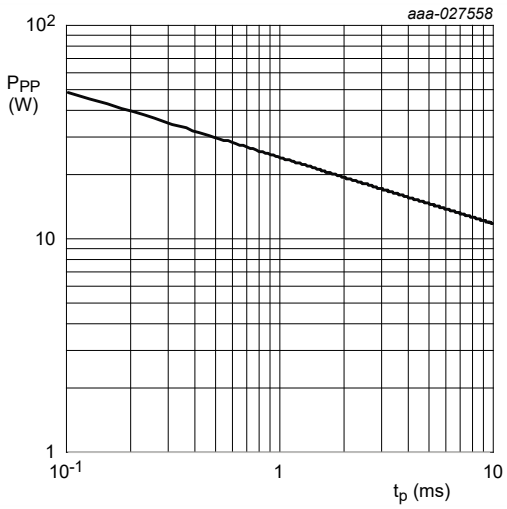



**Fig. 7. Peak pulse power as a function of exponential pulse duration; typical values**

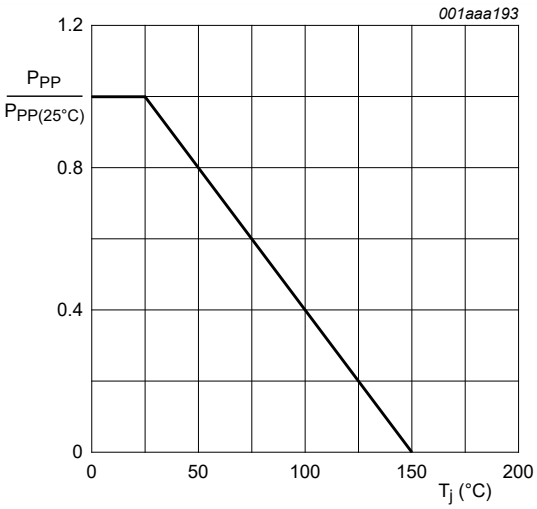



**Fig. 8. Relative variation of peak pulse power as a function of junction temperature; typical values**

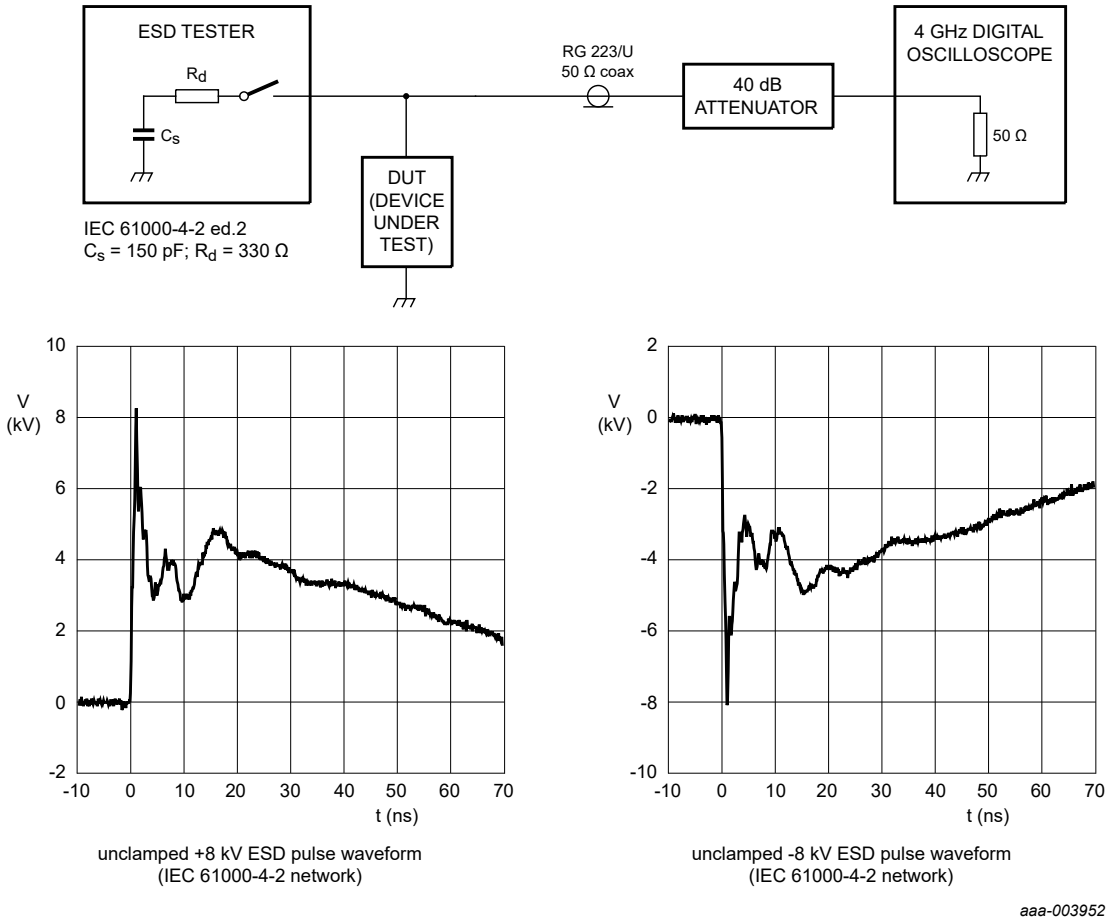



**Fig. 9. ESD clamping test setup and waveforms**

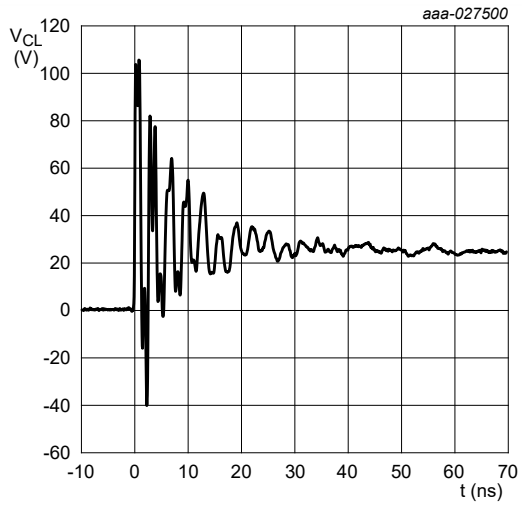



**Fig. 10. Clamped +8 kV pulse waveform (IEC 61000-4-2 network)**

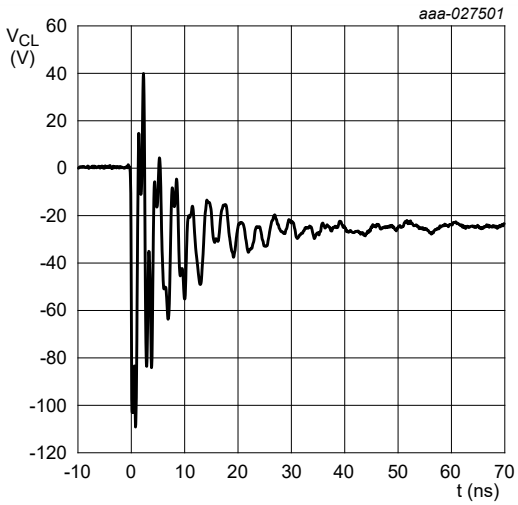



**Fig. 11. Clamped -8 kV pulse waveform (IEC 61000-4-2 network)**

# 10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

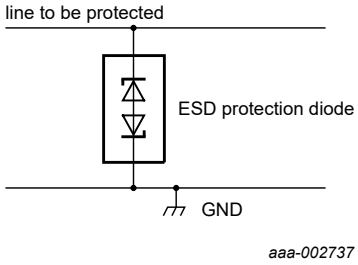



**Fig. 12. Application diagram**

**Circuit board layout and protection device placement**

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

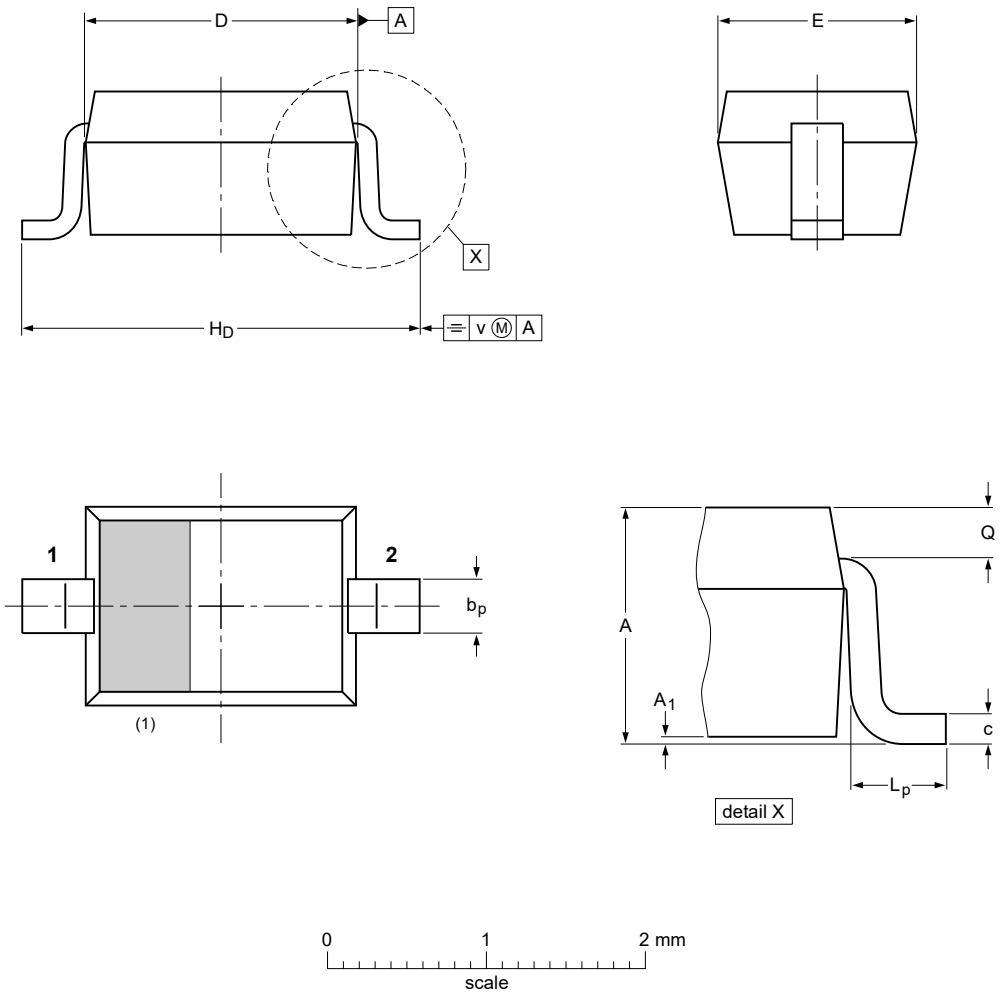
# 11. Test information

## Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

**Plastic surface-mounted package; 2 leads** **SOD323**




**DIMENSIONS (mm are the original dimensions)**

| UNIT | A | $A_1$ max | $b_p$ | c | D | E | $H_D$ | $L_p$ | Q | v |
|---|---|---|---|---|---|---|---|---|---|---|
| mm | 1.1<br>0.8 | 0.05 | 0.40<br>0.25 | 0.25<br>0.10 | 1.8<br>1.6 | 1.35<br>1.15 | 2.7<br>2.3 | 0.45<br>0.15 | 0.25<br>0.15 | 0.2 |

**Note**

1. The marking bar indicates the cathode

| OUTLINE VERSION | REFERENCES | | | | EUROPEAN PROJECTION | ISSUE DATE |
|---|---|---|---|---|---|---|
| | IEC | JEDEC | JEITA | | | |
| SOD323 | | | SC-76 | | | ~~03-12-17~~<br>06-03-16 |

**Fig. 13. Package outline SOD323**

## 13. Soldering

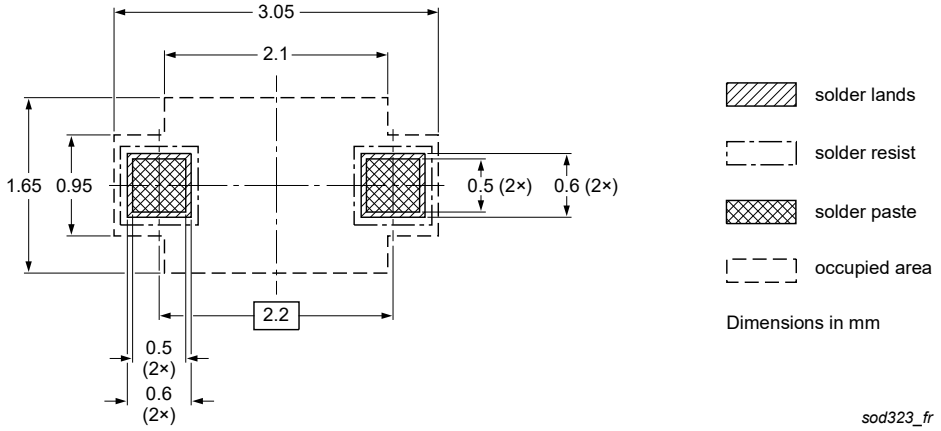



**Fig. 14. Reflow soldering footprint for SOD323**

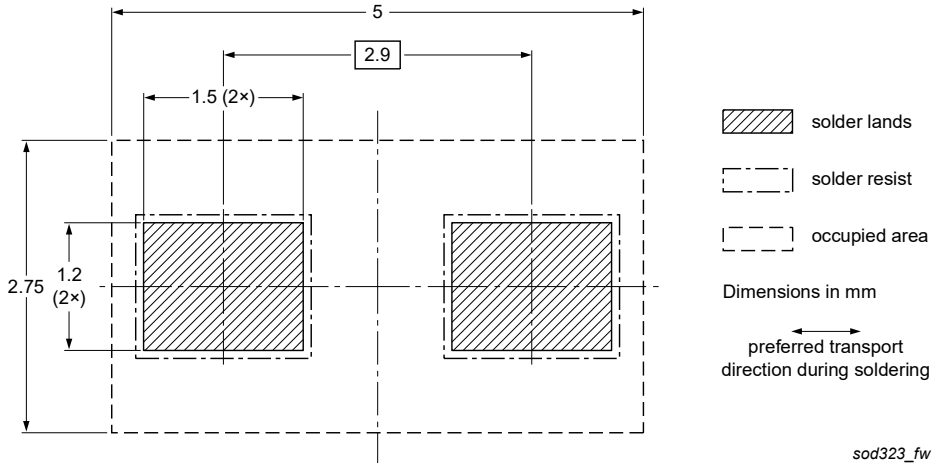



**Fig. 15. Wave soldering footprint for SOD323**

# 14. Revision history

**Table 7. Revision history**

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|---|---|---|---|---|
| PESD27VV1BA v.1 | 20190625 | Product data sheet | - | - |

# 15. Legal information

## Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|---|---|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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