



# PBSS2540M

40 V, 0.5 A NPN low  $V_{CEsat}$  (BISS) transistor

22 February 2018

Product data sheet

## 1. General description

Low  $V_{CEsat}$  NPN transistor in a SOT883 leadless ultra small plastic package.

PNP complement: PBSS3540M.

## 2. Features and benefits

- Low collector-emitter saturation voltage  $V_{CEsat}$
- High collector current capability  $I_C$  and  $I_{CM}$
- High efficiency leading to reduced heat generation
- Reduced printed-circuit board requirements.
- AEC-Q101 qualified

## 3. Applications

- Power management:
  - DC-DC converter
  - Supply line switching
  - Battery charger
  - LCD backlighting.
- Peripheral driver:
  - Driver in low supply voltage applications (e.g. lamps and LEDs).
  - Inductive load drivers (e.g. relays, buzzers and motors).

## 4. Quick reference data

Table 1. Quick reference data

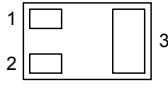
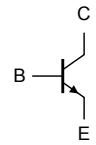
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	40	V
$I_C$	collector current		[1] [2]	-	500	mA
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	1	A
$h_{FE}$	DC current gain	$V_{CE} = 2$ V; $I_C = 10$ mA; $T_{amb} = 25$ °C	200	-	-	
$R_{CEsat}$	collector-emitter saturation resistance	$I_C = 500$ mA; $I_B = 50$ mA; $t_p \leq 300$ $\mu$ s; pulsed; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C	-	380	500	m $\Omega$

[1] Device mounted on an FR4 Printed-Circuit Board, (PCB), single-sided copper, tinplated, standard footprint, with 60  $\mu$ m copper strip line.

[2] Refer to SOT883 standard mounting conditions.

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 <p>Transparent top view</p> <p><b>DFN1006-3 (SOT883)</b></p>	 <p>sym123</p>
2	E	emitter		
3	C	collector		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS2540M	DFN1006-3	DFN1006-3: leadless ultra small plastic package; 3 solder lands	SOT883

## 7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS2540M	DC

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	40	V
$V_{CEO}$	collector-emitter voltage	open base		-	40	V
$V_{EBO}$	emitter-base voltage	open collector		-	6	V
$I_C$	collector current		[1] [2]	-	500	mA
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms		-	1	A
$I_{BM}$	peak base current			-	100	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1] [2]	-	250	mW
			[2] [3]	-	430	mW
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-65	150	°C
$T_{stg}$	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board, (PCB), single-sided copper, tinplated, standard footprint, with 60  $\mu$ m copper strip line.  
 [2] Refer to SOT883 standard mounting conditions.  
 [3] Device mounted on an FR4 PCB, single-sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>.

## 9. Thermal characteristics

**Table 6. Thermal characteristics**

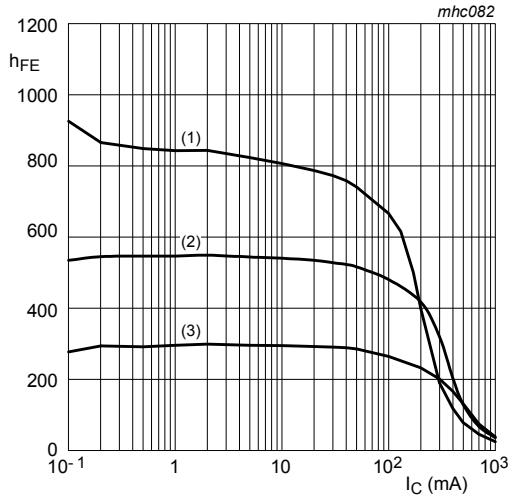
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	500	K/W
			[2] [3] [4]	-	-	290	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint, with 60  $\mu$ m copper strip line.  
 [2] Refer to SOT883 standard mounting conditions.  
 [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.  
 [4] Operated under pulsed conditions: duty cycle  $\bar{d} \leq 20\%$ , pulse width  $t_p \leq 30$  ms.

## 10. Characteristics

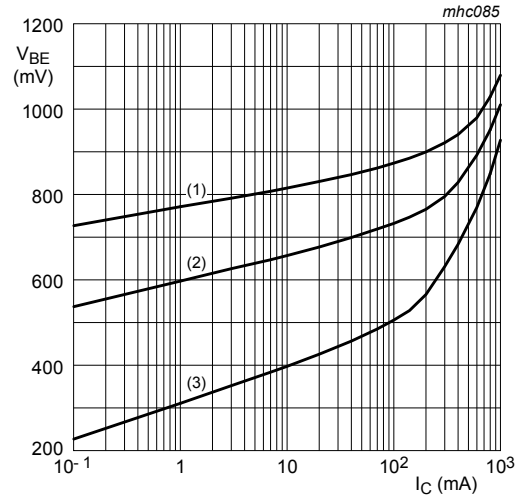
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
		V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>J</sub> = 150 °C	-	-	50	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C	200	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 100 mA; t <sub>p</sub> ≤ 300 μs; pulsed; δ ≤ 0.02 ; T <sub>amb</sub> = 25 °C	150	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; t <sub>p</sub> ≤ 300 μs; pulsed; δ ≤ 0.02 ; T <sub>amb</sub> = 25 °C	50	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>amb</sub> = 25 °C	-	-	50	mV
		I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA; T <sub>amb</sub> = 25 °C	-	-	100	mV
		I <sub>C</sub> = 200 mA; I <sub>B</sub> = 10 mA; T <sub>amb</sub> = 25 °C	-	-	200	mV
		I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; t <sub>p</sub> ≤ 300 μs; pulsed; δ ≤ 0.02 ; T <sub>amb</sub> = 25 °C	-	-	250	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance		-	380	500	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage		-	-	1.2	V
V <sub>BEon</sub>	base-emitter turn-on voltage	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 100 mA; T <sub>amb</sub> = 25 °C	-	-	1.1	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	250	450	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	6	pF



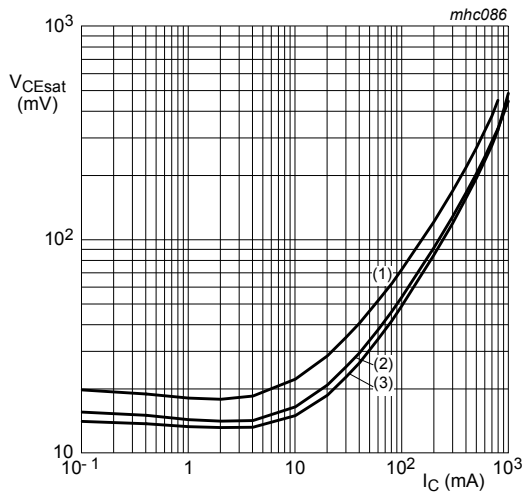
$V_{CE} = 2\text{ V}$   
 (1)  $T_{amb} = 150^\circ C$   
 (2)  $T_{amb} = 25^\circ C$   
 (3)  $T_{amb} = -55^\circ C$

**Fig. 1. DC current gain as a function of collector current; typical values**



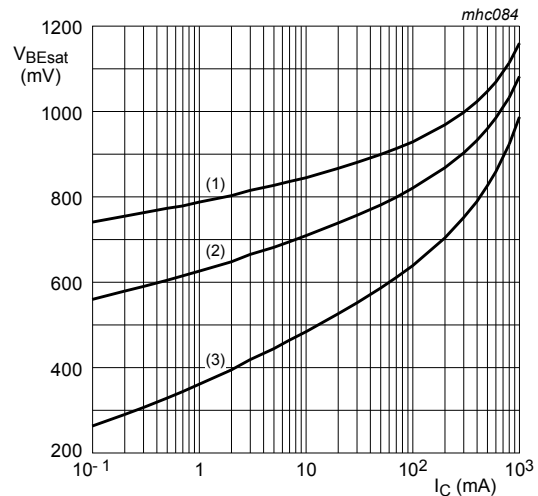
$V_{CE} = 2\text{ V}$   
 (1)  $T_{amb} = -55^\circ C$   
 (2)  $T_{amb} = 25^\circ C$   
 (3)  $T_{amb} = 150^\circ C$

**Fig. 2. Base-emitter voltage as a function of collector current; typical values**



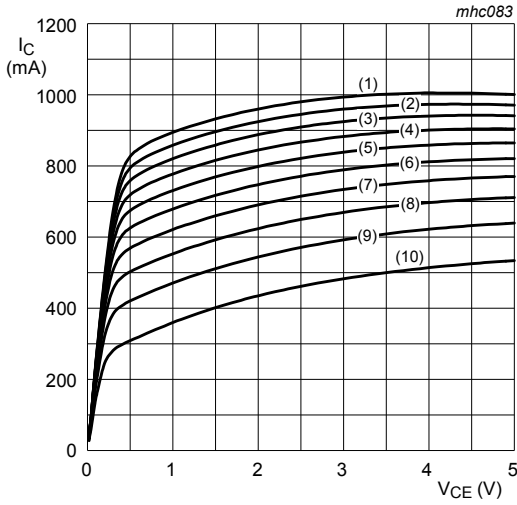
$I_C/I_B = 20$   
 (1)  $T_{amb} = 150^\circ C$   
 (2)  $T_{amb} = 25^\circ C$   
 (3)  $T_{amb} = -55^\circ C$

**Fig. 3. Collector-emitter saturation voltage as a function of collector current; typical values**



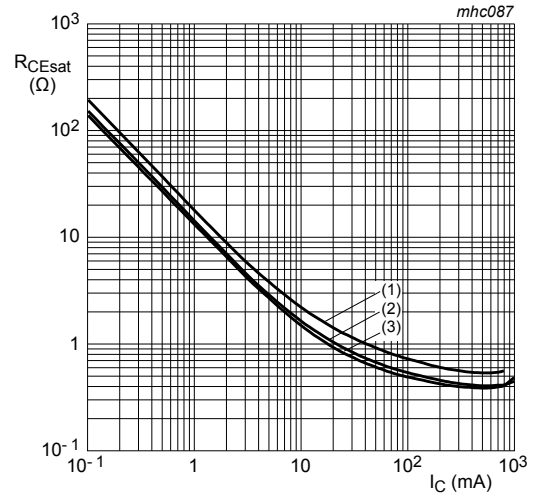
$I_C/I_B = 20$   
 (1)  $T_{amb} = 150^\circ C$   
 (2)  $T_{amb} = 25^\circ C$   
 (3)  $T_{amb} = -55^\circ C$

**Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values**



$T_{amb} = 25\text{ }^\circ\text{C}$   
 (1)  $I_B = 25\text{ mA}$   
 (2)  $I_B = 22.5\text{ mA}$   
 (3)  $I_B = 20\text{ mA}$   
 (4)  $I_B = 17.5\text{ mA}$   
 (5)  $I_B = 15\text{ mA}$   
 (6)  $I_B = 12.5\text{ mA}$   
 (7)  $I_B = 10\text{ mA}$   
 (8)  $I_B = 7.5\text{ mA}$   
 (9)  $I_B = 5\text{ mA}$   
 (10)  $I_B = 2.5\text{ mA}$

Fig. 5. Collector current as a function of collector-emitter voltage; typical values



$I_C/I_B = 20$   
 (1)  $T_{amb} = 150\text{ }^\circ\text{C}$   
 (2)  $T_{amb} = 25\text{ }^\circ\text{C}$   
 (3)  $T_{amb} = -55\text{ }^\circ\text{C}$

Fig. 6. Collector-emitter saturation resistance as a function of collector current; typical values

## 11. Package outline

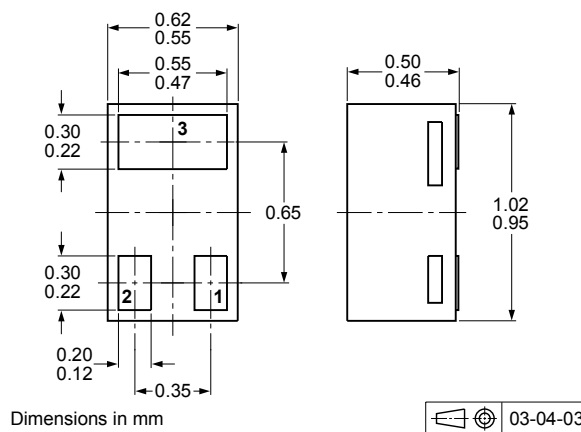


Fig. 7. Package outline DFN1006-3 (SOT883)



## 13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS2540M v.2	20180222	Product data sheet	-	PBSS2540M v.1
Modifications:	<ul style="list-style-type: none"><li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li><li>Legal texts have been adapted to the new company name where appropriate.</li></ul>			
PBSS2540M v.1	20030722	Product data sheet	-	-



## 14. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
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