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CUSTOMER . PTC

SAMPLE CODE . S05D00063-01

MASS PRODUCTION CODE . P05D00063-01

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 001

DRAWING NO. (Ver.) : LMD- P05D00063-01 (Ver.001)

PACKAGING NO. (Ver.) : PKG- P05D00063-01 (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
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Preliminary specification for design input

Specification for sample approval

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History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
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					tal: 17Daga

Total: 17Page



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1. SPECIFICATIONS

1.1 Features

Hardware

CPU	RISC Processor	N32926 (ARM926EJ-S) 64MB DDR2 SDRAM	
Momory	On Board Flash *1	1Gb NAND Flash 4GB eMMC (Option)	
Memory	External Storage *1	1x Micro SD (max. 32G)	
	Resolution	Up to 1024 RGB x 768	
Display	Touch Panel *2	Projected Capacitive Touch	
	Interface	Parallel RGB 16 bits	
I/O	USB	1x USB2.0 Device	
1/0	Serial	1 x UART	
Power Input	DC	5.0V	

Note:

- 1. Memory type (Option) will be setting by customer's request.
- 2. Touch Panel Type will be setting by customer's request.
- 3. Support PWM Signal Output. (5kHz, Duty Cycle: 256 Step)
- 4. Support JPEG Codec.
- 5. Support H.264 & MJPEG Codec
- 6. Support Video Data Processor (VPE)
- 7. Support RTC



PS:

Communication protocol system available on Jan 2019.

Powertip Graphic Editor software available on July 2019.

(support maximum resolution up to 1024x600)

Front View



NOTE: If you have any request, please feel free to contact us.



1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	77.2(W) x 63.5(L) x 10.6(H) MAX	mm

1.3 Absolute Maximum Ratings

Ta = 25°C

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply	VIN	—	-0.3	6.0	V
Operating Temperature	Top		-20	70	°C
Storage Temperature	Тѕт	_	-30	80	°C
Humidity	HD	Ta=60 °C	10	90	%RH

1.4 DC Electrical Characteristics

Ta = 25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Voltage	VIN	-	4.8	5.0	5.5	V
Power Supply Current *1	IIN	VIN = 5.0V	-	1.5	2.0	Α
Power Consumption of System	PIN	VIN = 5.0V	-	-	10	W
IO High-Level input voltage	ViH	-	2.0	-	V3V3+0.3	V
IO Low-Level input voltage	VIL	-	-	-	0.8	V
IO High-Level output voltage	Vон	-	2.4	-	-	V
IO Low-Level output voltage	Vol	-	-	-	0.4	V

Note 1: Power supply current with Powertip 7" LCM, PH800480T013-IHC



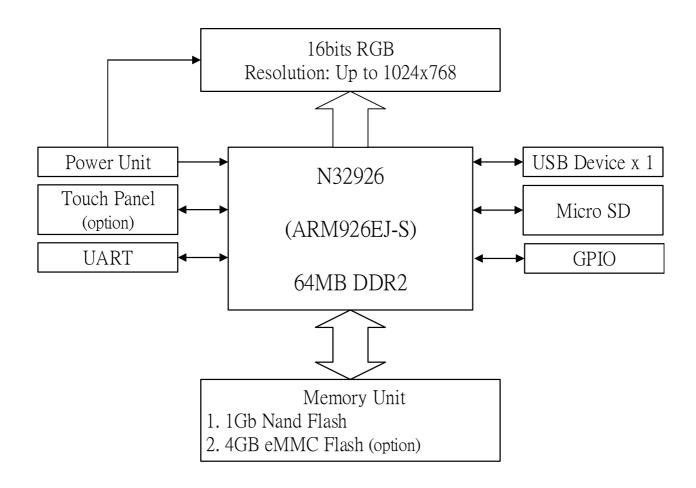
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

J8 --- I/O(Pitch0.5mm 6pin Double contact)

Pin No.	Symbol	Туре	DESCRIPTION
1	GND	Р	Power ground.
2	GPG9	Р	General Purpose I/O, Port G[9].
3	NC	-	Not Used.
4	NC	-	Not Used.
5	GND	Р	Power ground.
6	GPB13	Ю	General Purpose I/O, Port B[13].
7	GND	Р	Power ground.
8	GPB14	Ю	General Purpose I/O, Port B[14].
9	GND	Р	Power ground.
10	GPG2	Ю	General Purpose I/O, Port G[2].
11	GND	Р	Power ground.
12	GPG4	Ю	General Purpose I/O, Port G[4].
13	GPG5	Ю	General Purpose I/O, Port G[5].
14	GND	Р	Power ground.
15	GPG3	Ю	General Purpose I/O, Port G[3].
16	GND	Р	Power ground.
17	HPOUT_L	Α	Connect to N32926 pin 102.
18	HPOUT_R	Α	Connect to N32926 pin 101.
19	GPG7	Ю	General Purpose I/O, Port G[7].
20	NC	-	Not Used.
21	GND	Р	Power ground.
22	RESETn	I	System reset signal input, active low.
23	UART_RXD	I	UART port, receiver signal.



Pin No.	Symbol	Туре	Function
24	UART_TXD	0	UART port, transmitter signal.
25	GND	Р	Power ground.
26	VIN	Р	DC 5.0V Power Supply.
27	VIN	Р	DC 5.0V Power Supply.
28	GPD3	Ю	General Purpose I/O, Port D[3].
29	GPD4	Ю	General Purpose I/O, Port D[4].
30	GND	Р	Power ground.

J9 --- USB 2.0 Device Micro USB type

Pin No.	Symbol	Туре	DESCRIPTION
1	VUSB5V	Р	USB +5.0V.
2	D-	DS	Data – (Data M).
3	D+	DS	Data + (Data P).
4	NC	-	Not Used.
5	GND	Р	Ground.

J7 --- CTP (Pitch0.5mm 6pin Double contact)

Pin No.	Symbol	Туре	Function
1	GND	Р	Ground.
2	V3V3	Р	Power Supply.
3	I2C_SCL	Ю	I2C SCL for CTP.
4	I2C_SDA	Ю	I2C SDA for CTP.
5	CTP_INT	I	Interrupt Signal for CTP.
6	CTP_RST	0	Reset Signal for CTP.



J2 --- TFT Signal Output (Pitch 0.5mm 50pin Double contact)

Pin No.	Symbol	Туре	Function
1	GND	Р	Ground.
2	V3V3	Р	Power Supply (+3.3V).
3	V3V3	Р	Power Supply (+3.3V).
4	V5V	Р	Power Supply (+5.0V).
5	V5V	Р	Power Supply (+5.0V).
6	PWM	0	PWM Signal.
7	GND	Р	Ground.
8	NC	-	Not Used.
9	NC	-	Not Used.
10	NC	-	Not Used.
11	R3	0	Red Data.
12	GND	Р	Ground.
13	R4	0	Red Data.
14	R5	0	Red Data.
15	R6	0	Red Data.
16	R7	0	Red Data.
17	GND	Р	Ground.
18	NC	-	Not Used.
19	NC	-	Not Used.
20	G2	0	Green Data.
21	G3	0	Green Data.
22	GND	Р	Ground.
23	G4	0	Green Data.
24	G5	0	Green Data.
25	G6	0	Green Data.
26	G7	0	Green Data.
27	GND	Р	Ground.
28	NC	-	Not Used.
29	NC	-	Not Used.
30	NC	-	Not Used.

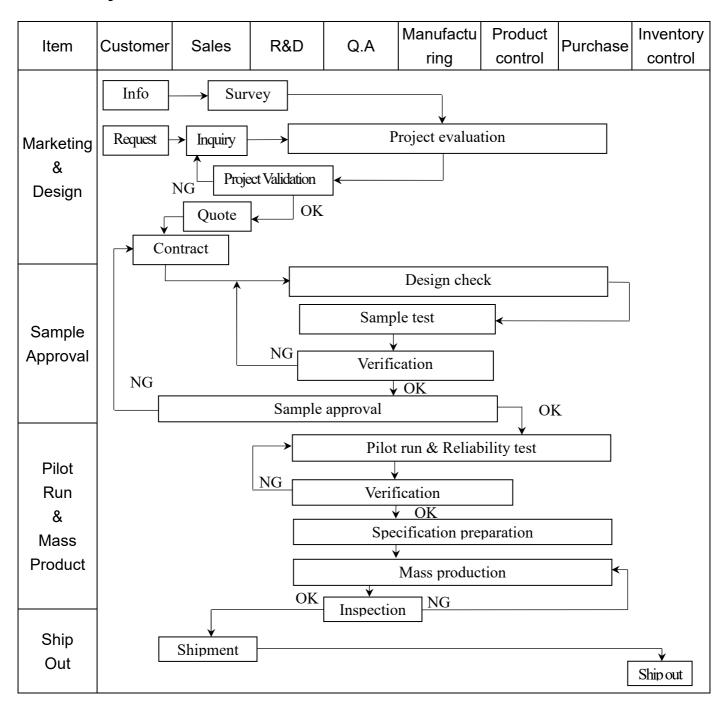


Pin No.	Symbol	Туре	Function
31	В3	0	Blue Data.
32	GND	Р	Ground.
33	B4	0	Blue Data.
34	B5	0	Blue Data.
35	B6	0	Blue Data.
36	B7	0	Blue Data.
37	GND	Р	Ground.
38	HS	0	Line synchronization signal. Horizontal Sync.
39	VS	0	Frame synchronization signal. Vertical Sync.
40	GND	Р	Ground.
41	DE	0	Data Enable.
42	GND	Р	Power Ground.
43	DCLK	0	Sample clock. Data will be latched at the falling edge of DCLK.
44	GND	Р	Power ground.
45	SPI_CS1	0	SPI1 /CS1 Signal.
46	SPI1_MOS	0	SPI1 bus, MOSI signal
47	SPI1_CLK	0	SPI1 bus, clock signal
48	DIS_CTL	0	Display Enable Control.
49	/RESET	0	Reset Signal.
50	GND	Р	Power ground.

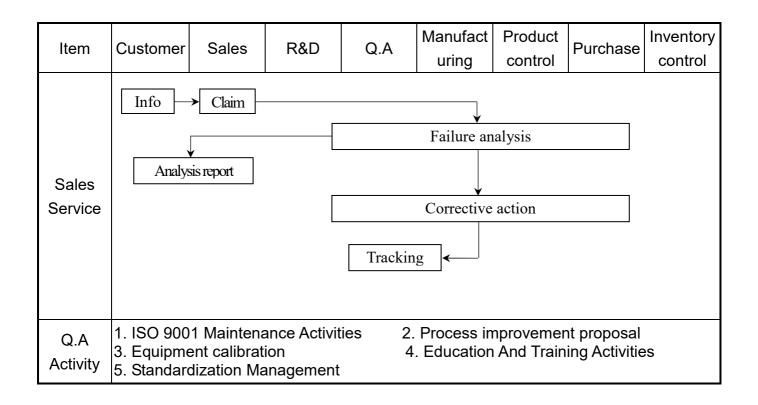


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION				
1	High Temperature Storage Test	Keep in +70 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.				
2	Low Temperature Storage Test	Keep in −20 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.				
3	High Temperature / High Humidity Storage Test	Keep in +60°C / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)				
4	Temperature Cycling Storage Test	$-20^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +70^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ $(30\text{mins}) (5\text{mins}) (30\text{mins}) (5\text{mins})$ 10 Cycle Surrounding temperature, then storage at normal condition 4h				
5	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X \ Y \ Z) duration for 2 Hrs 				
6	Drop Test (Packaged)	Packing Weight (Kg) 0 ~ 45. 4 45. 4 ~ 90. 8 90. 8 ~ 454 Over 454 Drop direction: **1 corner / 3 edge	Drop Height (cm) 122 76 61 46 es / 6 sides each 1 times			



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

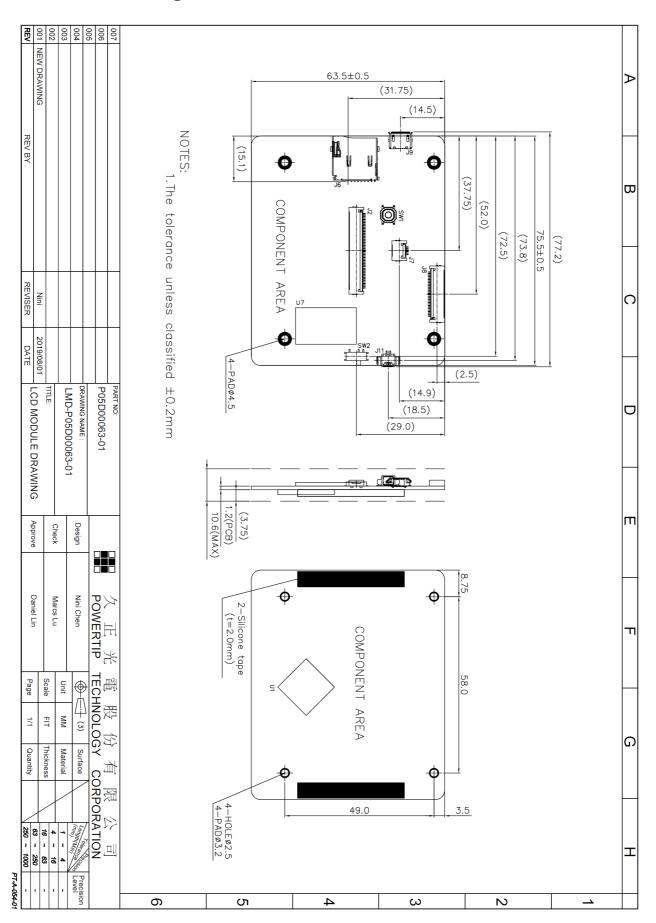
The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Appendix: 1. Drawing





Appendix: 2. Packaging Specifications

	.001		与壯-	田松書	Approve	Check	Contact
Docı	uments NO.	PKG-P05D00063-01		規格書 Specifications	Daniel	Marcs	Nini
1.包	退裝材料規模	各表 (Packaging Materia	al): (per carton)			•	
No. Item		Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weigl	
1	成品 (Main Board)		P05D00063-01	77.2X 63.5	0.02	144	2.88
2	靜電夾鏈袋(1)Antistatic Zipper Bag		BAG000000050	90 X 130	0.002	144	0.288
3			BX24500070BLBA	245 X 70 X 2.5	0.01	78	0.78
4			BX29300070BLBA	293 X 70 X 2.5	0.012	18	0.216
5			OTFOAM00112ABA	125 X 85.0 X 15	0.004	12	0.048
6	海綿墊(5)Foam Rubber Cushion		OTFOAM00006ABA	290 X 240 X 10	0.02	12	0.24
7	C3内盒(6)Pr		BX31025510AABA	310 X 255 X 105	0.162	6	0.972
_	外紙箱(7)Ca		BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
		Polylon board (Total Weight in carton)	OTPLB00000017 : 6.3 Kg±10	510 X 310 X 15	0.025	2	0.05
	(註)	Bag+Main Board (4)EPE E Remark 2)	///	聖龍板 ylon board			
	(5) Foam Rubber	2)(3)隔板 Partition (註 Remark 1))海綿墊 r Cushion	₩ ₩	(7)外紙箱 Carton		***	
	(5) Foam Rubber	Partition (註 Remark 1))海綿墊 r Cushion				⇒	
1. N	(5) Foam Rubber (1) 日 成品排放示意 Jain Board plac	Partition (註 Remark 1))海綿墊 r Cushion		i (REMARK) 數成		→	
1. N	(5) Foam Rubber (1) 日 成品排放示意 Jain Board plac	Partition (註 Remark 1))海绵墊 r Cushion (6) C5內盒 Product Box 圖(前後間隔不放置): ced as figure showing:	特記事項 2. OTFOAM00112ABA 尺寸:62.5 X 85 X 15 m	i (REMARK) 數成			

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