



0.1-26.5GHz 0.5W Traveling Wave Amplifier Data Sheet

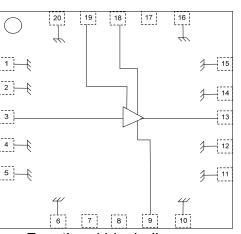
### **Features:**

- Frequency Range: 0.1 26.5 GHz
- P3dB: +27 dBm
- Gain: 12.5 dB
- Vdd =8 to 12 V
- Ids =250 to 500 mA
- Input and Output Fully Matched to 50 Ω
- Surface Mount, RoHs Compliant QFN 4x4mm package

## **Applications:**

- Fiber optics communication systems
- Microwave and wireless communication systems
- Microwave and optical instrumentations

# **Description:**



Functional block diagram

The MMA-012727-M4 is a broadband GaAs MMIC Traveling Wave Amplifier (TWA) with medium output power and high gain over 0.1 to 26.5GHz frequency range. This amplifier is optimally designed for broadband applications requiring flat gain and high power with excellent input and output matches over a 0.1 to 26.5GHz frequency range.

## Absolute Maximum Ratings: (Ta= 25 °C)\*

SYMBOL	PARAMETERS	UNITS	Min.	Max.
Vds	Drain-Source Voltage	V		12.5
Vg1	First Gate-Source Voltage	V	-2	0
lg1	First Gate Current	mA	-10	1
Vg2	Second Gate-Source Voltage	V	-3.5	6
lg2	Second Gate-Source Current	mA	-20	
Pdiss	Maximum Power dissipation	W		4.65
Pin max	RF Input Power	dBm		23
Toper	Operating Temperature	°C		-40 to +85
Tch	Channel Temperature	°C		+150
Tstg	Storage Temperature	°C		-55 to +165
Tmax	Max. Assembly Temp (60 sec max)	°C		+300

\*Operation of this device above any one of these parameters may cause permanent damage.



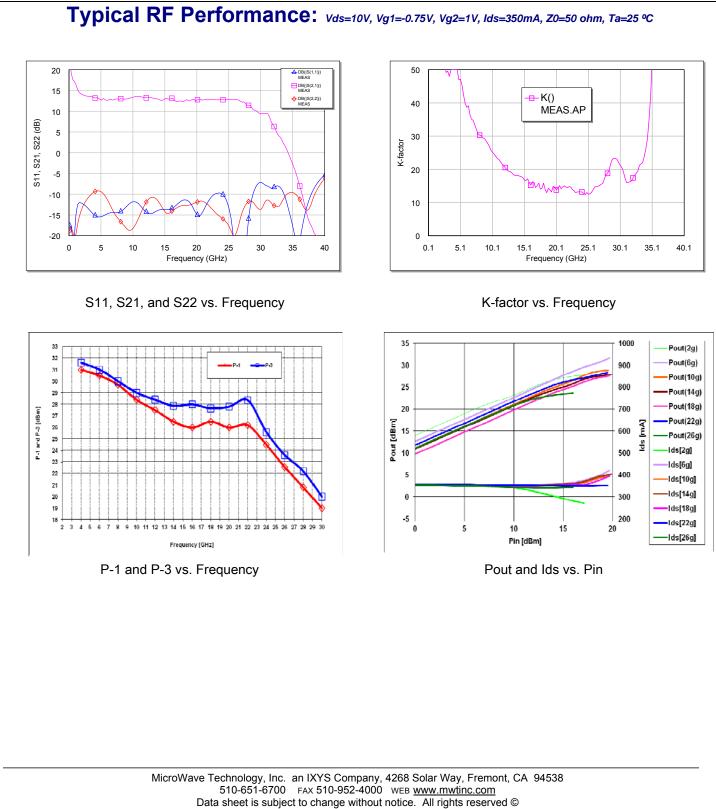


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Electrical Specifications: Vds=10V, Vg1=-0.75V, Vg2=1V, Ids=350mA, Ta=25 °C Z0=50 ohm				
Parameter	Units	Min.	Тур.	Max.
Frequency Range	MHz	0.1		26,500
Gain (Typ / Min)	dB	11.5	12.5	
Gain Flatness (Typ / Max)	+/-dB		0.5	0.7
Input RL(Typ/Max)	dB	9	11	
Output RL(Typ/Max)	dB	9	11	
Output P1dB(Typ/Min)	dBm	22	26	
Output IP3 (1)	dBm		35	
Output P3dB(Typ/Min)	dBm	23	27	
Operating Current at P1dB (Typ/Max)	mA		350	380
Thermal Resistance	°C /W		14	
(1) Output IP3 is measured with two tones at output pe	ower of 10 dBm	/tone separated by	20 MHz.	







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### Applications

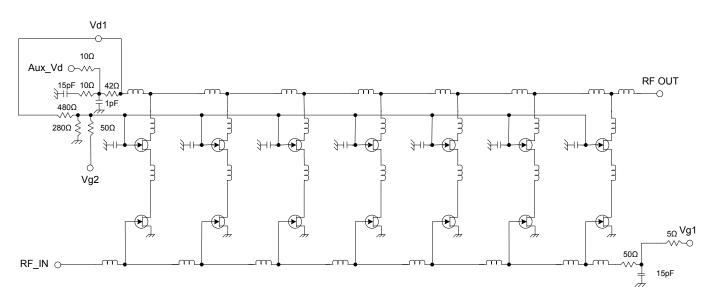
The **MMA-012727-M4** traveling wave amplifier is designed for use as a general purpose wideband power stage in microwave communication systems, and test equipments. It is ideally suited for broadband applications requiring a flat gain response and excellent port matches over a 0.1 to 26.5 GHz frequency range. Dynamic gain control and low-frequency extension capabilities are designed into these devices.

#### **Biasing and Operation**

The recommended bias conditions for best performance for the **MMA-012727-M4** are VDD = 10V, IDD = 350mA. To achieve these drain current levels, Vg1 is typically -0.75V, and Vg2 is typically +1V. No other bias supplies or connections to the device are required for 0.1 to 26.5 GHz operation. The gate voltage (Vg1) should be applied prior to the drain voltage (Vd1) during power up and removed after the drain voltage during power down. Performance improvements are possible depending on applications. The drain bias voltage range is 8 to 12V and the quiescent drain current biasing range is 250mA to 500mA. The **MMA-012727-M4** is a DC coupled amplifier. External coupling capacitors are needed on RFIN and RFOUT ports. The drain bias pad is connected to RF and must be decoupled to the lowest operating frequency. An auxiliary drain contacts is provided when performance below 2GHz in required. The second gate (Vg2) can be used to obtain 30 dB (typical) dynamic gain control. For maximum gain operation, typical Vg2 is +1V.

#### Assembly Techniques

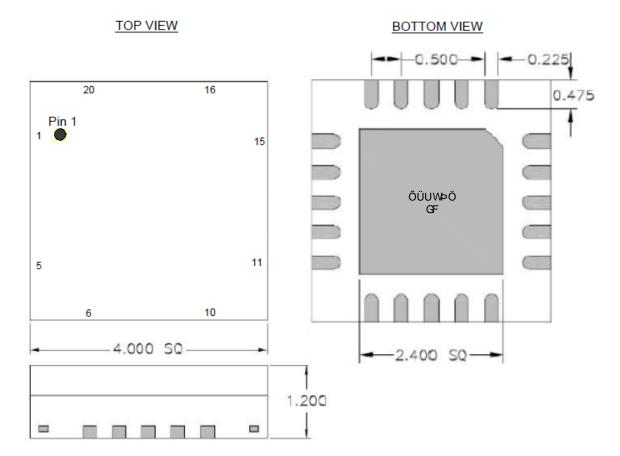
GaAs MMICs are ESD sensitive. ESD preventive measures must be employed in all aspects of storage, handling, and assembly. MMIC ESD precautions, handling considerations, die attach and bonding methods are critical factors in successful GaAs MMIC performance and reliability.





MMA-012727-M4 0.1-26.5GHz 0.5W Traveling Wave Amplifier Data Sheet

## **Outline Drawing**



The units are in [mm].

Pin	Description
3	RF Input
13	RF Output/Vdd
9	Vg1
18	Vg2
19	Vdd_Aux
1, 2, 4, 5 ,6, 10, 11, 12, 14,	Ground
15, 16, 20, 21	
7, 8, 17	N/C

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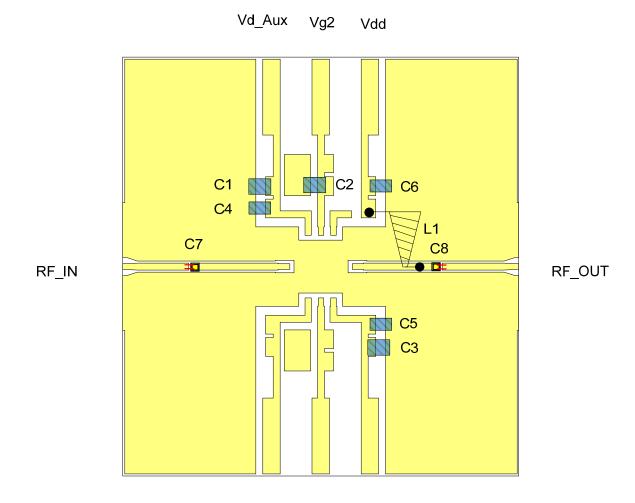
#### **Application Circuit:** Vg2 0 0 0 0 0 0 0 φ 1uF 0.1uF 0.1uF Vdd\_Aux 20 Vdd $\bigcirc$ 19 18 1 16 C 1 15 **RF** Input **RF** Output 7 14 2 GND PAD GND GND Ľ Ð Θ 3 13 RF OUT RF IN 21 4 12 GND GND 5 \_\_\_\_\_11\_ Drain bias (Vdd) must be applied through a broadband bais-T or external bias network. 1uF 0.1uF 0 0 0 0 φ 0 0 0 Vg1





### **Recommended Application Board Design:**

Board Material is 10mil (Dielectric) thickness Rogers 4350B with 0.5oz cupper clads. Board is soldered on a gold plated solid cupper block and adequate heat-sinking is required for 3.4W total maximum power dissipation.



Vg1

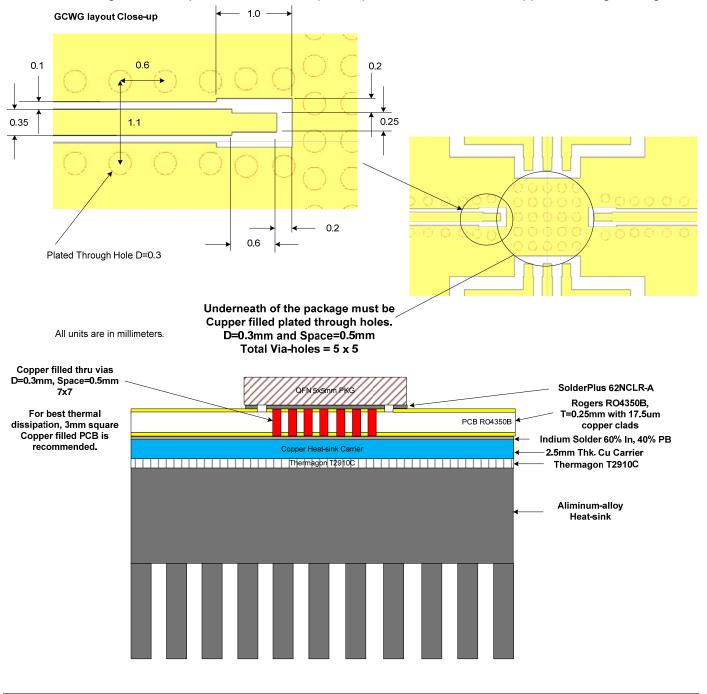
Part	Value	Description
C1, C3	1uF	0603
C2, C4, C5, C6	0.1uF	0402
C7, C8	100pF	Presidio
	-	LSB1515B101M2H5R-B
L1	0.26uH	GOWANDA
		C100FL1938G6





### **Recommended Application Board Design:**

Board Material is 10mil (Dielectric) thickness Rogers 4350B with 0.5oz cupper clads. The board material and mounting pattern, as defined in the data sheet, optimizes RF performance and is strongly recommended. An electronic drawing of the land pattern is available upon request from *MwT* Sales & Application Engineering.



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