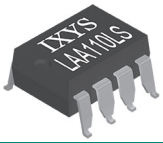
IXYS
INTEGRATED CIRCUITS DIVISION



LAA110L

350V, 120mA Dual Single-Pole Current Limiting, Normally Open Relays

Parameter	Ratings	Units
Load Voltage, AC/DC	350	V_P
Load Current	120	mA_{rms} / mA_{DC}
On-Resistance (max)	35	Ω

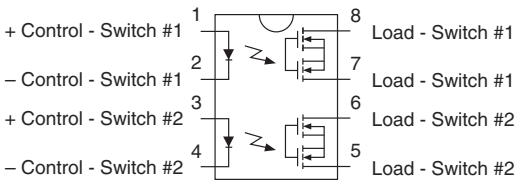
Features

- 3750V_{rms} Input/Output Isolation
- Current Limiting Device
- Low Drive Power Requirements
- Higher Reliability than Electromechanical Relays
- No EMI/RFI Generation
- Flammability Rating UL 94 V-0
- Surface Mount and Tape & Reel Versions Available
- Small 8-Pin Package

Applications

- Telecom Switching
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Industrial Controls

Pin Configuration



Description

LAA110L is a dual normally open (1-Form-A) Solid State Relay that has two independently controlled, optically coupled MOSFET switches with an additional current limiting circuit. The MOSFET switches and photovoltaic die use optically coupled MOSFET technology to provide 3750V_{rms} of input/output isolation.

The optically coupled outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

This dual switch OptoMOS relay provides a more compact design solution than discrete single-pole relays in a variety of applications, and saves board space by incorporating both switches in a single 8-Pin package.

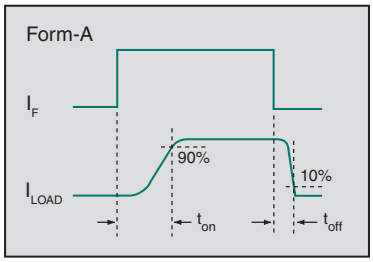
Approvals

- UL Recognized Component: File # E76270
- CSA Certified Component: Certificate # 1175739
- TUV EN 62368-1: Certificate # B 082667 0008

Ordering Information

Part #	Description
LAA110L	8-Pin DIP (50/Tube)
LAA110LS	8-Pin Surface Mount (50/Tube)
LAA110LSTR	8-Pin Surface Mount (1,000/Reel)
LAA110PL	8-Pin SOIC (Flatpack) (50/Tube)
LAA110PLTR	8-Pin SOIC (Flatpack) (1,000/Reel)

Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings @ 25ºC

Parameter	Ratings	Units
Blocking Voltage	350	V_P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation [1]	150	mW
Total Power Dissipation [2]	800	mW
Isolation Voltage, Input to Output	3750	V_{rms}
Operational Temperature, Ambient	-40 to +85	°C
Storage Temperature	-40 to +125	°C

[1] Derate linearly 1.33 mW / °C
[2] Derate output power linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

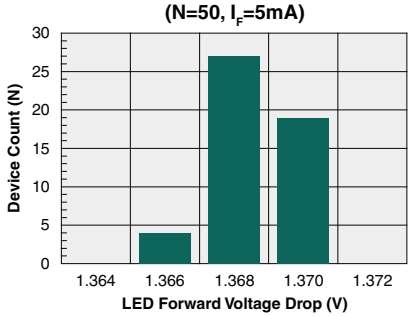
Electrical Characteristics @ 25ºC

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Blocking Voltage	I_L=1µA	V_{DRM}	350	-	-	V_P
Load Current, Continuous [1]	-	I_L	-	-	120	mA_{rms} / mA_{DC}
Load Current Limiting	V_L=8V, I_F=5mA	I_{CL}	±130	±170	±210	mA
On-Resistance [2]	I_L=120mA	R_{ON}	-	30	35	Ω
Off-State Leakage Current	V_L=350V_P	I_{LEAK}	-	-	1	µA
Switching Speeds						
Turn-On	I_F=5mA, V_L=10V	t_{on}	-	-	3	ms
Turn-Off		t_{off}	-	-	3	ms
Output Capacitance	I_F=0mA, V_L=50V, f=1MHz	C_{OUT}	-	25	-	pF
Input Characteristics						
Input Control Current to Activate	I_L=120mA	I_F	-	-	5	mA
Input Control Current to Deactivate	-	-	0.4	0.7	-	mA
Input Voltage Drop	I_F=5mA	V_F	0.9	1.36	1.5	V
Reverse Input Current	V_R=5V	I_R	-	-	10	µA
Common Characteristics						
Input to Output Capacitance	V_{IO}=0V, f=1MHz	C_{IO}	-	3	-	pF

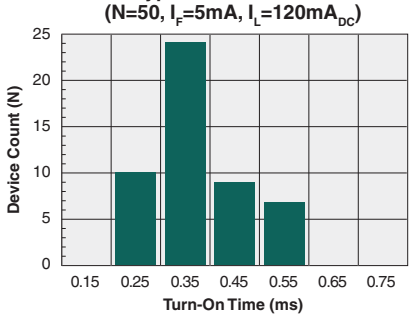
[1] If both poles operate, then the load current must be derated so that it does not exceed the package power dissipation value.
[2] Measurement taken within one second of on-time.

PERFORMANCE DATA*

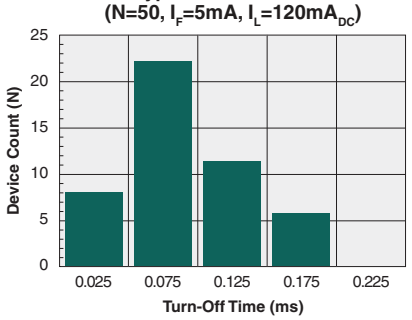
Typical LED Forward Voltage Drop (N=50, I_F=5mA)



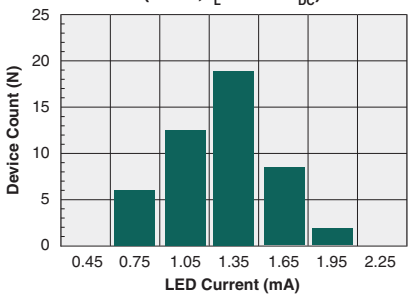
Typical Turn-On Time (N=50, I_F=5mA, I_L=120mA$_{DC}$)



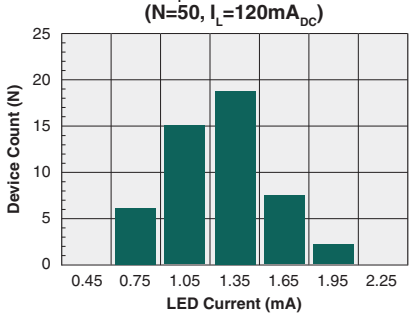
Typical Turn-Off Time (N=50, I_F=5mA, I_L=120mA$_{DC}$)



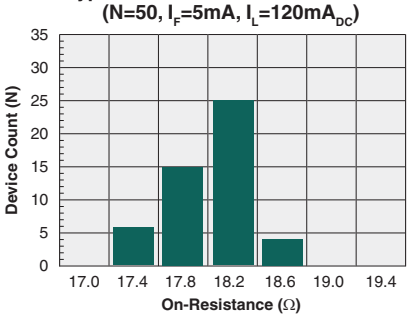
Typical I_F for Switch Operation (N=50, I_L=120mA$_{DC}$)



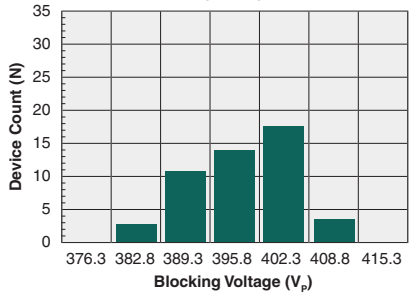
Typical I_F for Switch Dropout (N=50, I_L=120mA$_{DC}$)



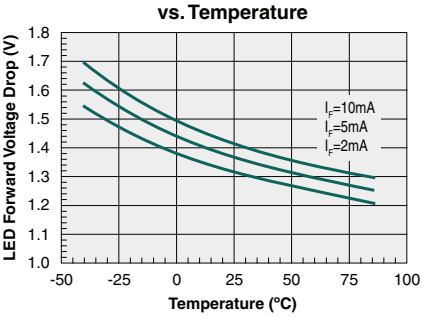
Typical On-Resistance Distribution (N=50, I_F=5mA, I_L=120mA$_{DC}$)



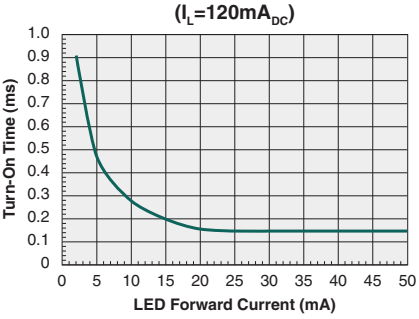
Typical Blocking Voltage Distribution (N=50)



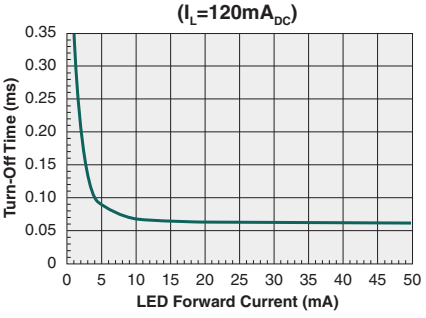
Typical LED Forward Voltage Drop vs. Temperature



Typical Turn-On Time vs. LED Forward Current (I_L=120mA$_{DC}$)



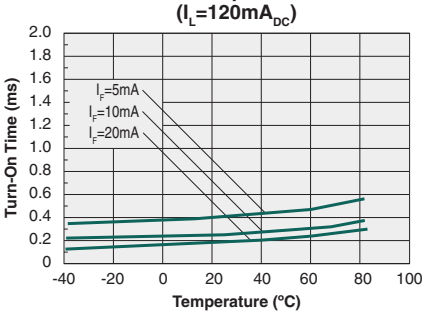
Typical Turn-Off Time vs. LED Forward Current (I_L=120mA$_{DC}$)



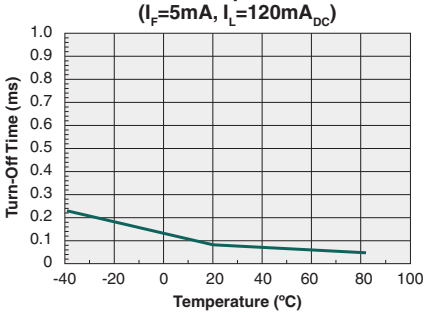
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25ºC.

PERFORMANCE DATA*

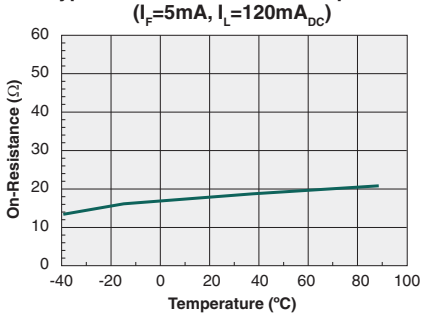
Typical Turn-On Time vs. Temperature (I_L=120mA$_{DC}$)



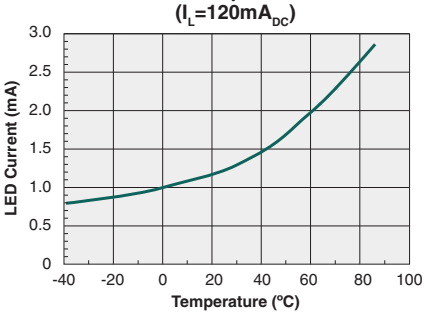
Typical Turn-Off Time vs. Temperature (I_F=5mA, I_L=120mA$_{DC}$)



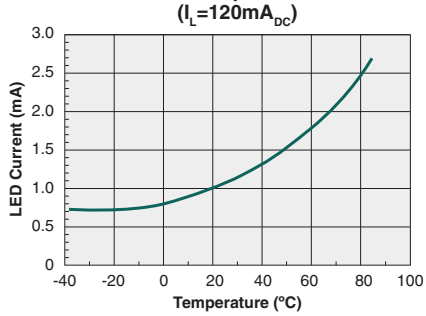
Typical On-Resistance vs. Temperature (I_F=5mA, I_L=120mA$_{DC}$)



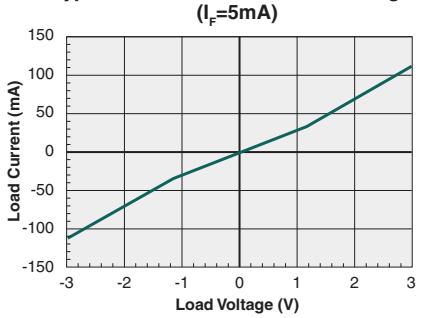
Typical I_F for Switch Operation vs. Temperature (I_L=120mA$_{DC}$)



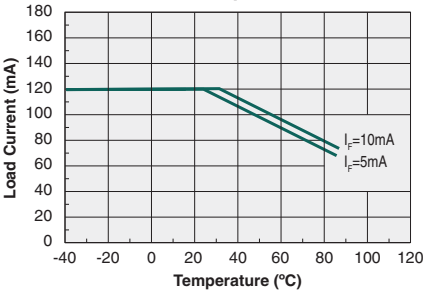
Typical I_F for Switch Dropout vs. Temperature (I_L=120mA$_{DC}$)



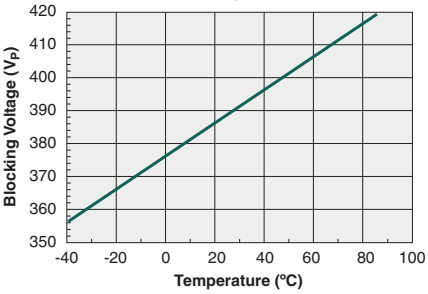
Typical Load Current vs. Load Voltage (I_F=5mA)



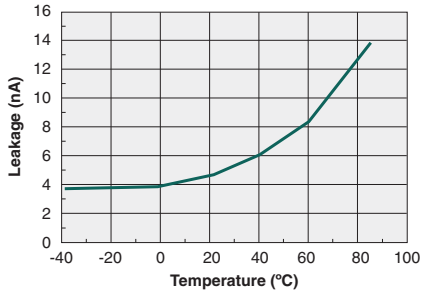
Maximum Load Current vs. Temperature



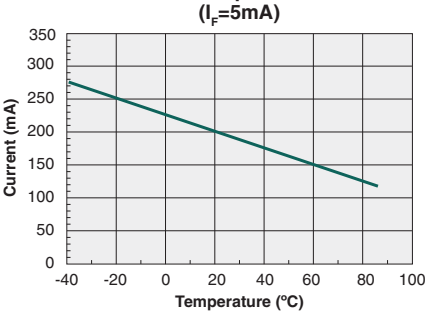
Typical Blocking Voltage vs. Temperature



Typical Leakage vs. Temperature Measured across Pins 5&6, 7&8



Typical Current Limiting vs. Temperature (I_F=5mA)



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25ºC.

Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
LAA110LS	MSL 1
LAA110PL	MSL 3

ESD Sensitivity

This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_C) and the maximum dwell time the body temperature of these surface mount devices may be (T_C - 5)°C or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

Device	Classification Temperature (T_c)	Dwell Time (t_p)	Max Reflow Cycles
LAA110LS	250°C	30 Seconds	3
LAA110PL	245C	30 seconds	3

For through-hole devices, the maximum pin temperature and maximum dwell time through all solder waves is provided in the table below. Dwell time is the interval beginning when the pins are initially immersed into the solder wave until they exit the solder wave. For multiple waves, the dwell time is from entering the first wave until exiting the last wave. During this time, pin temperatures must not exceed the maximum temperature given in the table below. Body temperature of the device must not exceed the limit shown in the table below at any time during the soldering process.

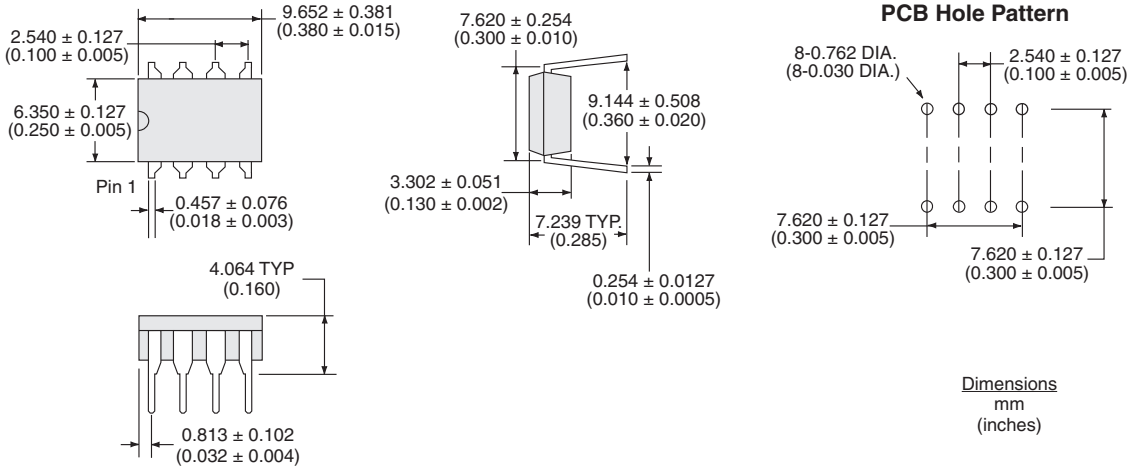
Device	Maximum Pin Temperature	Maximum Body Temperature	Maximum Total Dwell Time	Wave Cycles
LAA110L	260°C	250°C	10 seconds	1

Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.

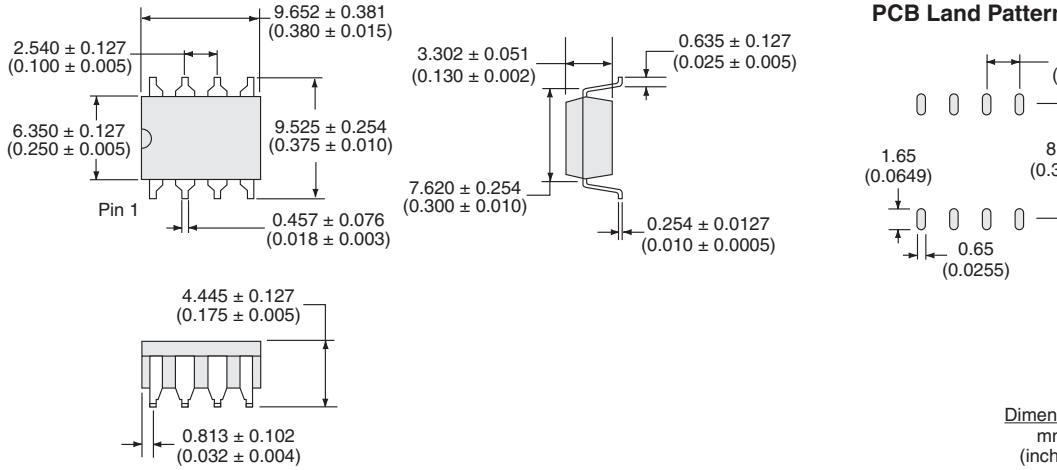
MECHANICAL DIMENSIONS

LAA110L



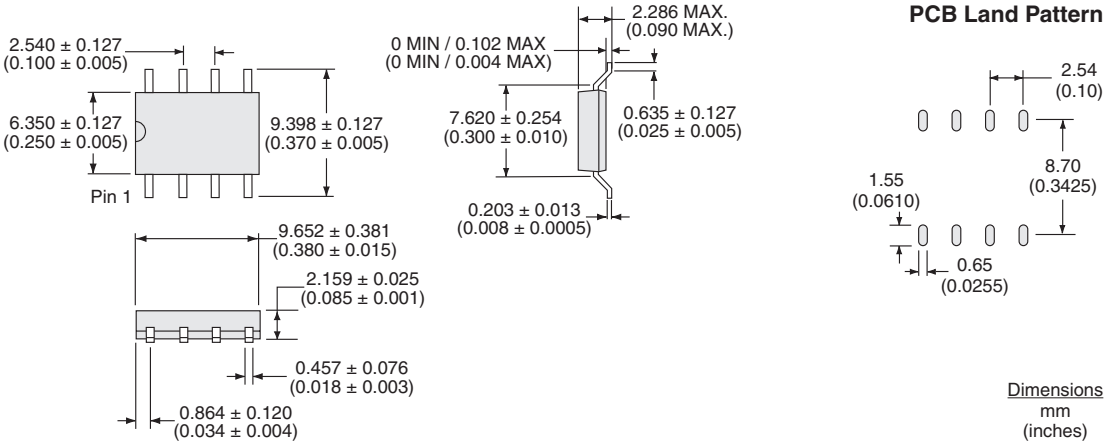
Dimensions
mm
(inches)

LAA110LS



Dimensions
mm
(inches)

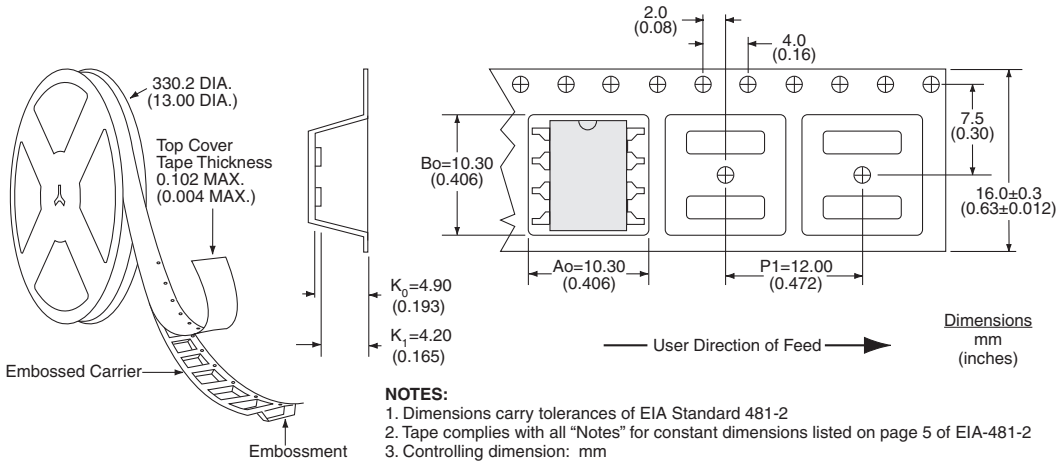
LAA110PL



Dimensions
mm
(inches)

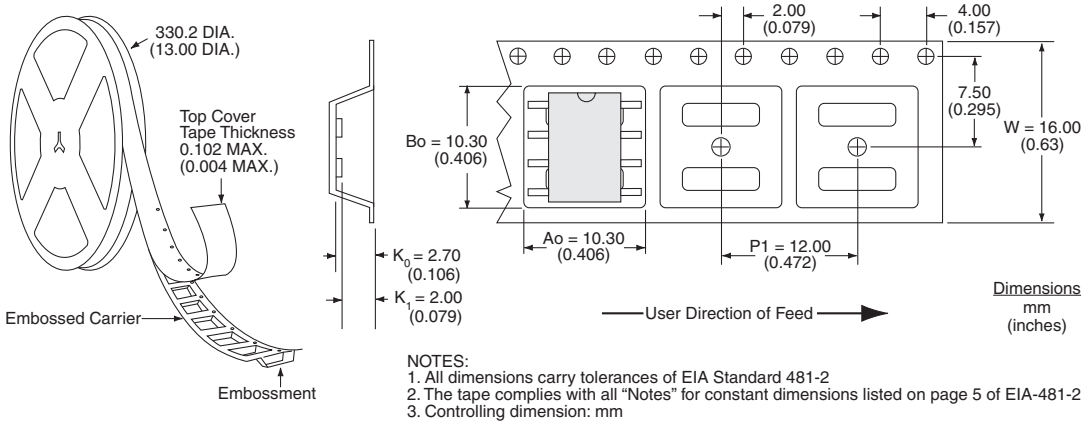
MECHANICAL DIMENSIONS

LAA110LSTR Tape & Reel



NOTES:
1. Dimensions carry tolerances of EIA Standard 481-2
2. Tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2
3. Controlling dimension: mm

LAA110PLTR Tape & Reel



NOTES:
1. All dimensions carry tolerances of EIA Standard 481-2
2. The tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2
3. Controlling dimension: mm

For additional information please visit our website at: https://www.ixysic.com

IXYS
A Littelfuse Technology

Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at https://www.littelfuse.com/disclaimer-electronics.

Specification: DS-LAA110L-R12
©Copyright 2021, Littelfuse, Inc.
OptoMOS® is a registered trademark of IXYS Integrated Circuits
All rights reserved. Printed in USA.
9/29/2021