

L0806B101JSWST

Aliases (L0806B101JSWST)

Not for New Design

KEMET, L-SWS, Commercial Grade, 100 uH, 5%



Click [here](#) for the 3D model.

General Information

Series	L-SWS
Style	SMD Chip
RoHS	Yes
Notes	Last Order Date June 30, 2023.

Specifications

Inductance	100 uH
Inductance Tolerance	5%
Temperature Range	-40/+105°C
Current	80 mAmps (MAX)
DC Resistance	8 Ohms
Self-Resonant Frequency	9 MHz MIN

Dimensions

Chip Size	0806
L	2mm +/-0.2mm
W	1.6mm +/-0.2mm
T	1.6mm +/-0.2mm
B	0.5mm +/-0.2mm

Packaging Specifications

Packaging	T&R
Packaging Quantity	2000

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