

VDC	650 V
Q _C	53 nC
I _F	20 A
T _{j,max}	175 °C

Amp+™ Features

- Unipolar rectifier with surge current
- Zero reverse recovery current
- Fast, temperature-independent switching
- Avalanche tested to 135mJ*
- All parts tested to greater than 715V

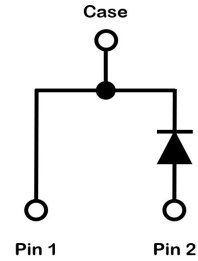
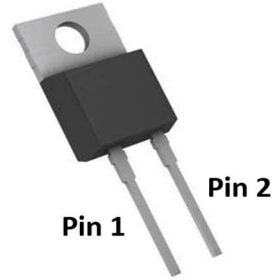
Amp+™ Benefits

- Near zero switching loss
- Higher efficiency
- Reduced heat sink requirements
- Easy to parallel

Amp+™ Applications

- Switch mode power supplies, UPS
- Power factor correction
- Output rectification
- EV charging stations

Package



Part #	Package	Marking
GP3D020A065A	TO-220-2L	3D020A065



Maximum Ratings, at T_j=25 °C, unless otherwise specified

Characteristics	Symbol	Conditions	Values	Unit
Continuous forward current	I _{F**}	T _C =25 °C, T _j =175 °C	57	A
		T _C =125 °C, T _j =175 °C	30	
		T _C =150 °C, T _j =175 °C	19	
Surge non-repetitive forward current sine halfwave	I _{FSM}	T _C =25 °C, t _p =8.3 ms	135	A
		T _C =110 °C, t _p =8.3 ms	125	
Non-repetitive peak forward current	I _{F,max}	T _C =25 °C, t _p =10 μs	1100	A
i ² t value	∫i ² dt	T _C =25 °C, t _p =8.3 ms	76	A ² s
		T _C =110 °C, t _p =8.3 ms	65	
Repetitive peak reverse voltage	V _{R,RRM}	T _j =25 °C	650	V
Diode dv/dt ruggedness	dv/dt	Turn-on slew rate, repetitive	200	V/ns
Power dissipation	P _{tot**}	T _C =25 °C	183	W
Operating junction & storage temperature	T _j , T _{storage}	Continuous	-55...175	°C
Soldering temperature	T _{solder}	Wave soldering leads	260	°C
Mounting torque		M3 Screw	1	N-m

Notes:

* EAS of 135 mJ is based on starting T_j = 25°C, L = 1.0 mH, IAS = 16.43 A, V = 50 V.

** Typical R_{th,jc} used

Electrical Characteristics, at $T_j=25\text{ }^\circ\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
DC blocking voltage	V_{DC}	$I_R=50\mu\text{A}$, $T_j=25\text{ }^\circ\text{C}$	650	-	-	V
Breakdown voltage	V_{BR}	$I_R=2\text{mA}$, $T_j=25\text{ }^\circ\text{C}$	715	-	-	V
Diode forward voltage	V_F	$I_F=20\text{A}$, $T_j=25\text{ }^\circ\text{C}$	-	1.40	1.70	V
		$I_F=20\text{A}$, $T_j=125\text{ }^\circ\text{C}$	-	1.51	-	
		$I_F=20\text{A}$, $T_j=175\text{ }^\circ\text{C}$	-	1.64	2.20	
Reverse current	I_R	$V_R=650\text{V}$, $T_j=25\text{ }^\circ\text{C}$	-	2	50	μA
		$V_R=715\text{V}$, $T_j=25\text{ }^\circ\text{C}$	-	7	-	
		$V_R=650\text{V}$, $T_j=125\text{ }^\circ\text{C}$	-	19	-	
		$V_R=650\text{V}$, $T_j=175\text{ }^\circ\text{C}$	-	72	500	
Total capacitive charge	Q_C	$V_R=400\text{V}$, $T_j=25\text{ }^\circ\text{C}$	-	53	-	nC
Total capacitance	C	$V_R=1\text{V}$, $f=1\text{ MHz}$	-	866	-	pF
		$V_R=200\text{V}$, $f=1\text{ MHz}$	-	102	-	
		$V_R=400\text{V}$, $f=1\text{ MHz}$	-	78	-	

Thermal Characteristics

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal resistance, junction-case	R_{thJC}	-	-	0.82	1.05	$^\circ\text{C/W}$

Typical Performance

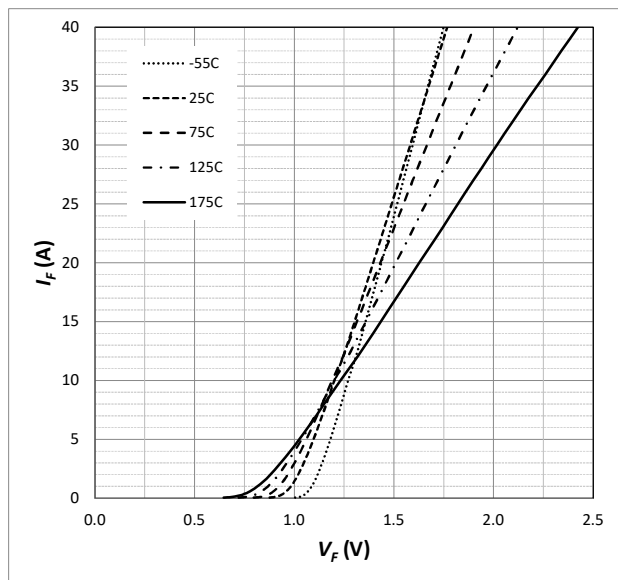


Fig. 1 Forward Characteristics (parameterized on T_j)

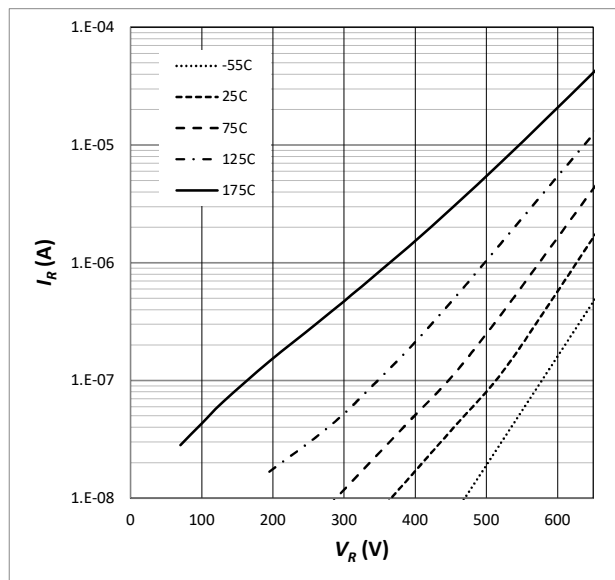


Fig. 2 Reverse Characteristics (parameterized on T_j)

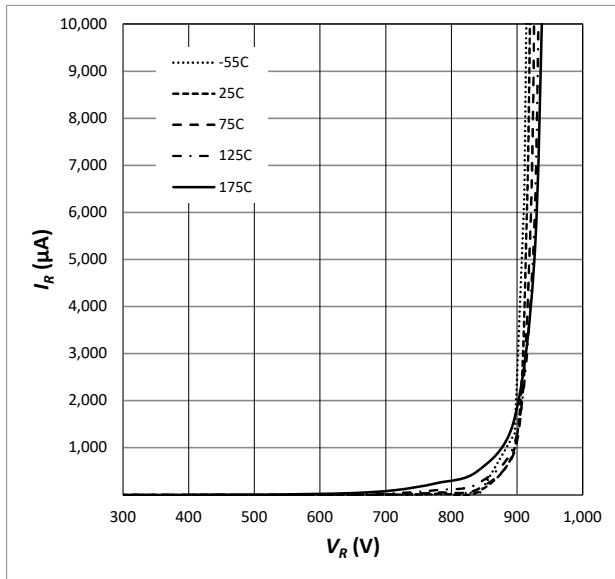


Fig. 3 Reverse Characteristics (parameterized on Tj)

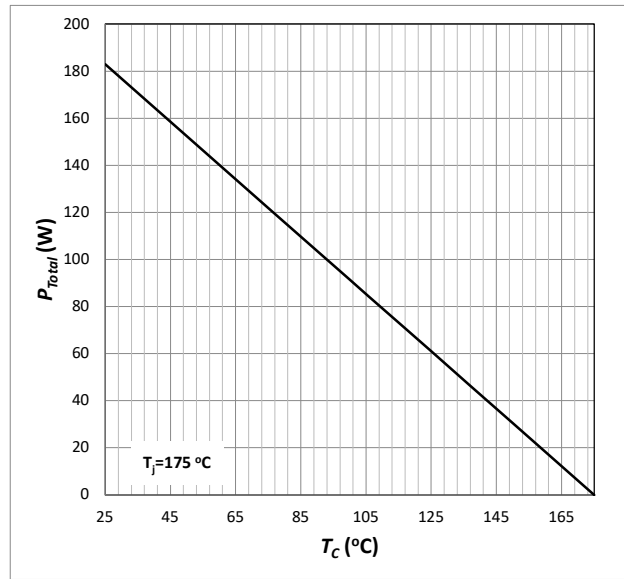


Fig. 4 Power Derating

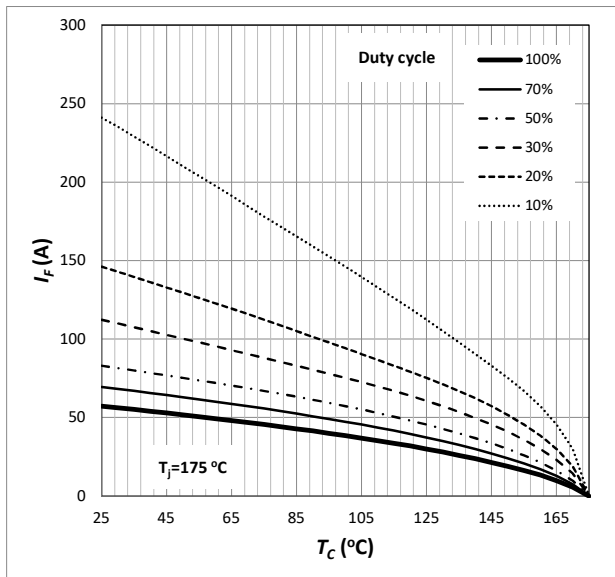


Fig. 5 Current Derating

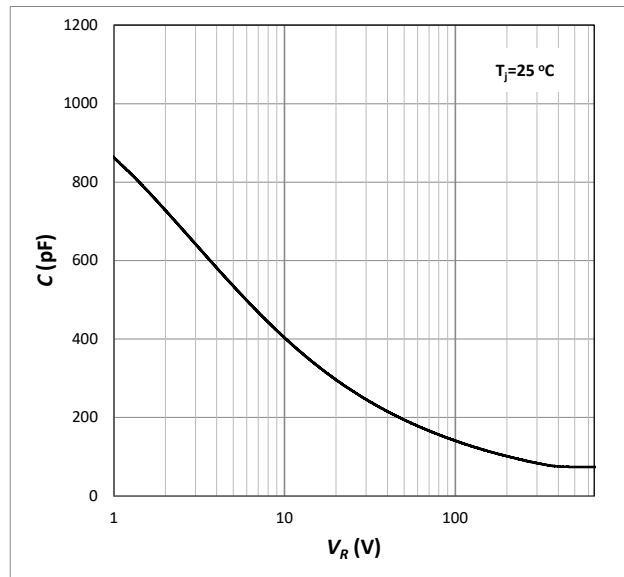


Fig. 6 Capacitance

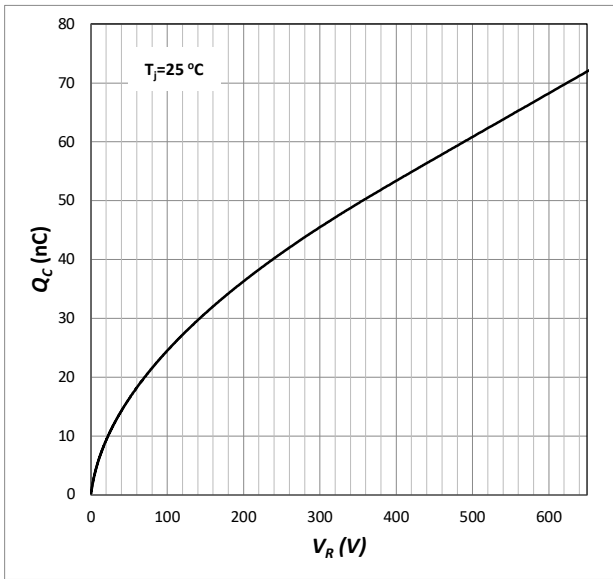


Fig. 7 Capacitive Charge

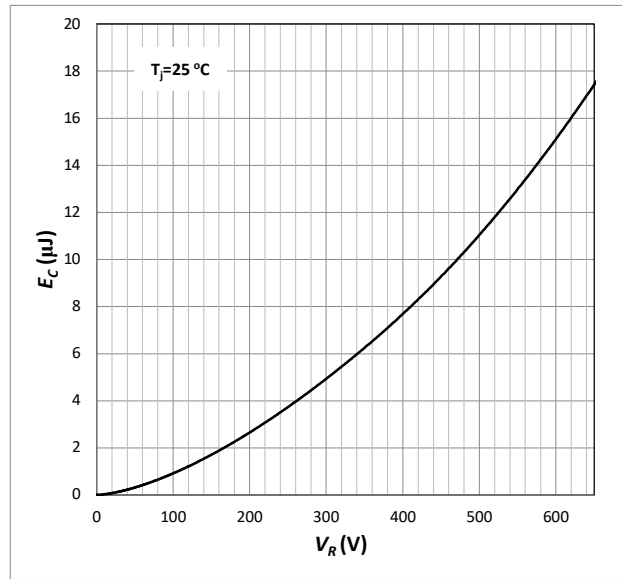


Fig. 8 Typical Capacitance Stored Energy

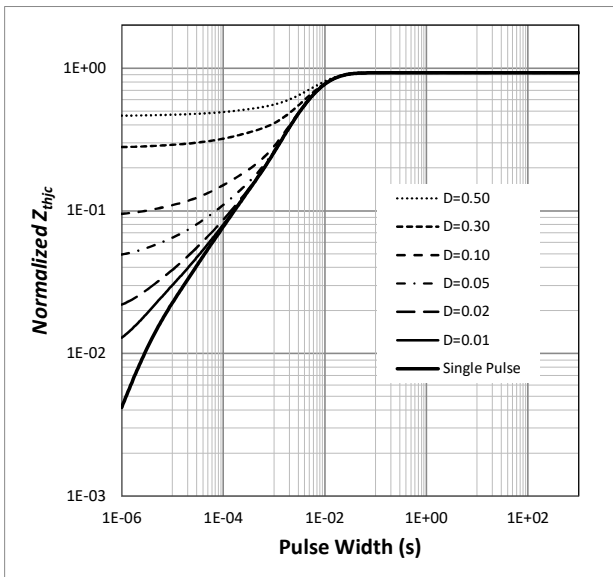


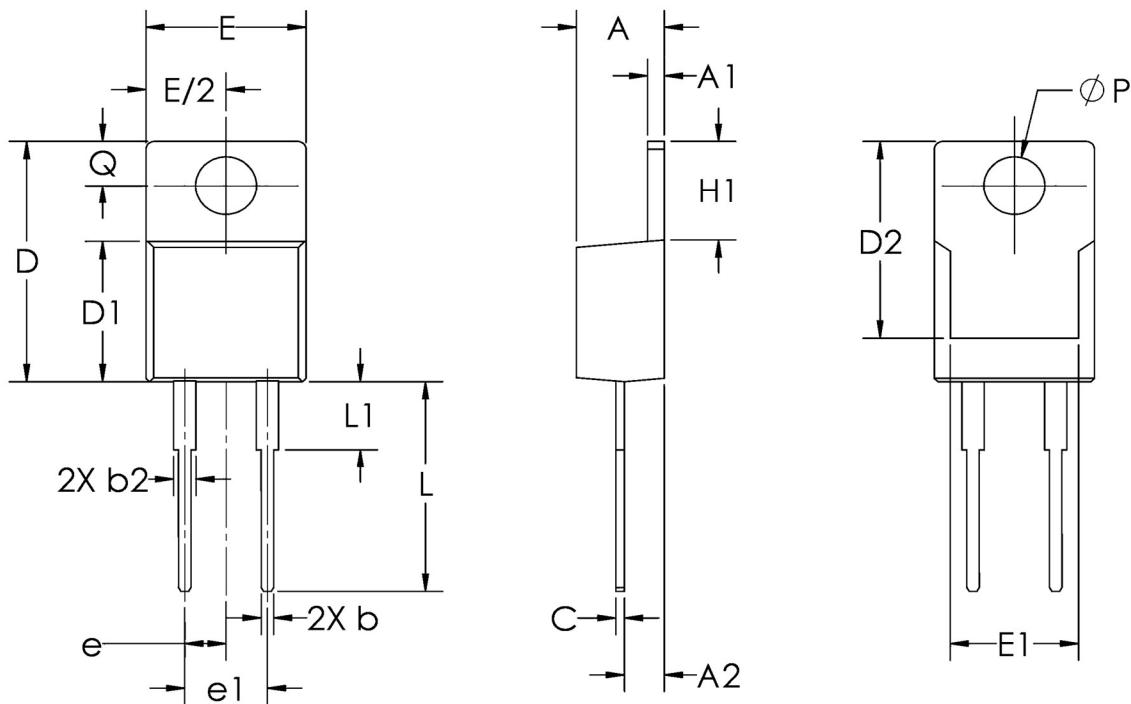
Fig. 9 Transient Thermal Impedance

650V SiC Schottky Diode

Amp+™

GP3D020A065A

Package Dimensions TO-220-2L



Sym	Millimeters		Inches	
	Min	Max	Min	Max
A	4.20	4.70	0.165	0.185
A1	1.14	1.40	0.045	0.055
A2	2.03	2.92	0.080	0.115
b	0.38	1.02	0.015	0.040
b2	1.02	1.78	0.040	0.070
c	0.36	0.76	0.014	0.030
D	14.22	16.51	0.560	0.650
D1	8.38	9.40	0.330	0.370
D2	12.19	13.13	0.480	0.517
E	9.65	10.67	0.380	0.420
E1	6.86	8.89	0.270	0.350
e	2.54 BSC		.100 BSC	
e1	5.08 BSC		.200 BSC	
H1	5.84	6.86	0.230	0.270
L	12.57	14.73	0.495	0.580
L1	3.60	6.35	0.142	0.250
ØP	3.53	4.09	0.139	0.161
Q	2.54	3.43	0.100	0.135

Revision History

Date	Revision	Notes
2/10/2020	1.0	Initial release of datasheet
2/20/2020	1.1	Using Rthjc typical for I _F and P _{tot}
3/4/2022	1.2	Updated forward voltage spec - valid for date codes after 2101 (YYWW format), updated VBR spec

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented March, 2013. RoHS Declarations for this product can be obtained from the Product Documentation sections of www.SemiQ.com.

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