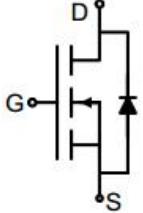
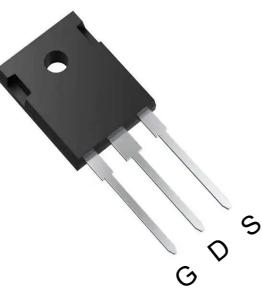


## N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The GC041N65QF uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 650V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 70A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 43mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> <li>● Ultra-fast body diode</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic diagram</p>  <p>TO-247</p>
--	--

Ordering Information			
Device	Package	Marking	Packaging
GC041N65QF	TO-247	GC041N65F	30pcs/Tube

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Continuous Drain Current	$I_D$	70	A
Pulsed Drain Current (note1)	$I_{DM}$	210	A
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Power Dissipation	$P_D$	500	W
Single pulse avalanche energy (note2)	$E_{AS}$	1620	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	°C

Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	40	°C/W
Maximum Junction-to-Case	$R_{thJC}$	0.25	°C/W

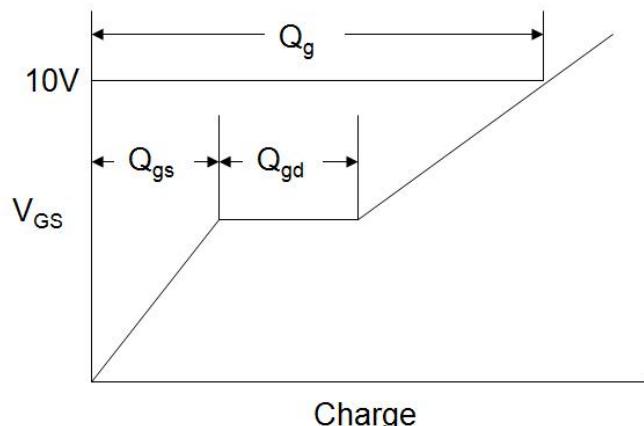
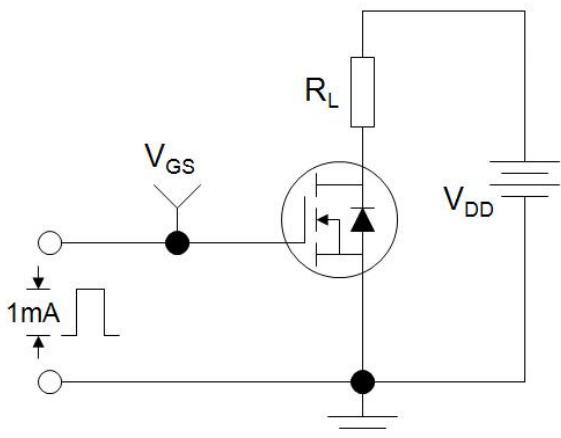
**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	650	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 650\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	10	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 30\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	3.0	4.0	5.0	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$	--	36	43	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{GS}} = 5\text{V}, I_D = 20\text{A}$	--	35	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 400\text{V}, f = 0.25\text{MHz}$	--	7668	--	pF
Output Capacitance	$C_{\text{oss}}$		--	157	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	0.6	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 400\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 10\text{V}$	--	160	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	35	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	55	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 400\text{V}, I_D = 20\text{A}, R_G = 4.7\Omega$	--	55	--	ns
Turn-on Rise Time	$t_r$		--	65	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	175	--	
Turn-off Fall Time	$t_f$		--	48	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	70	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 20\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = 100\text{A}/\mu\text{s}$	--	1.5	--	nC
Reverse Recovery Time	$T_{\text{rr}}$		--	207	--	ns

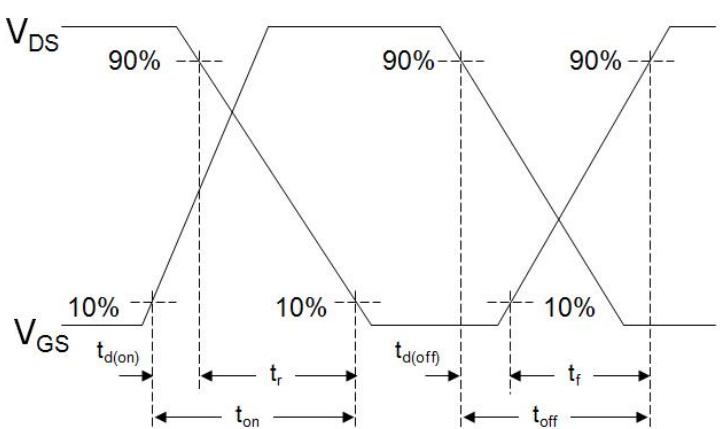
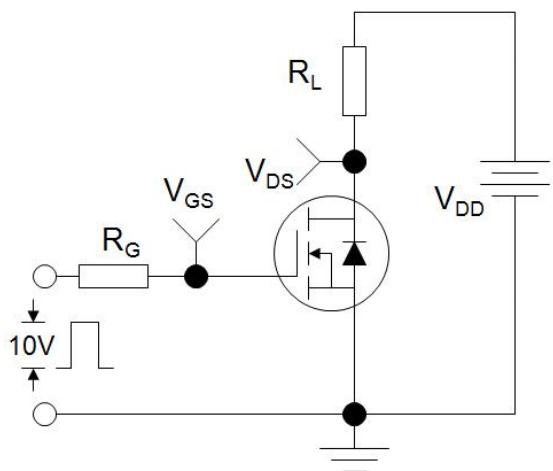
**Notes**

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=10\text{mH}$ ,  $R_G=25\Omega$
3. Identical low side and high side switch with identical  $R_G$

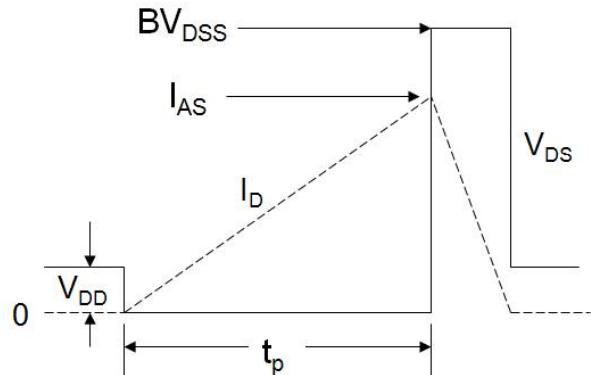
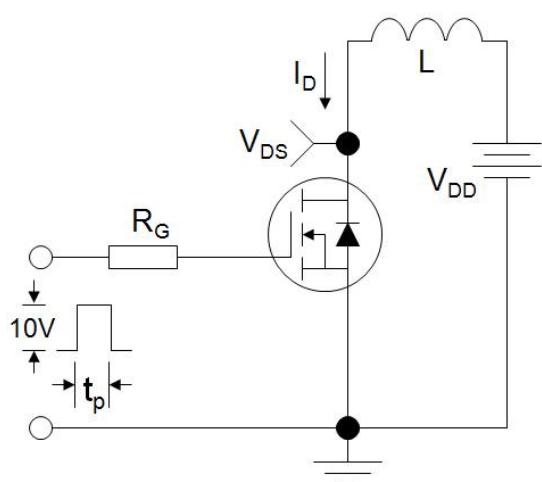
### Gate Charge Test Circuit



### Switch Time Test Circuit

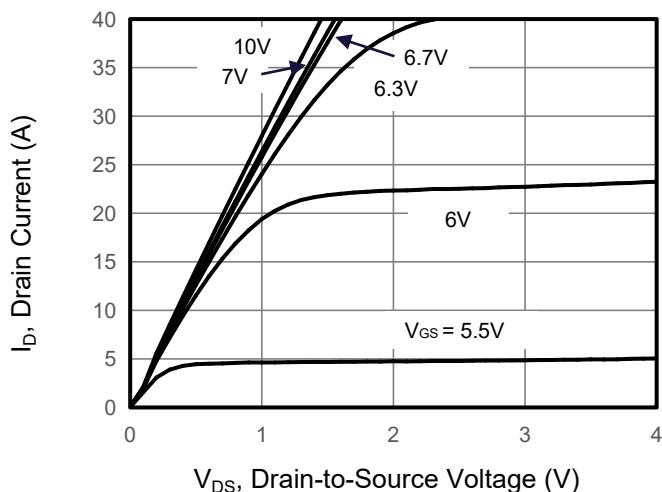


### EAS Test Circuit

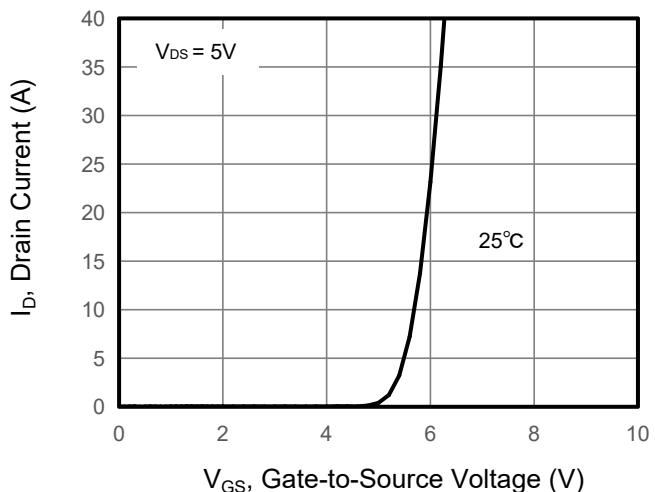


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

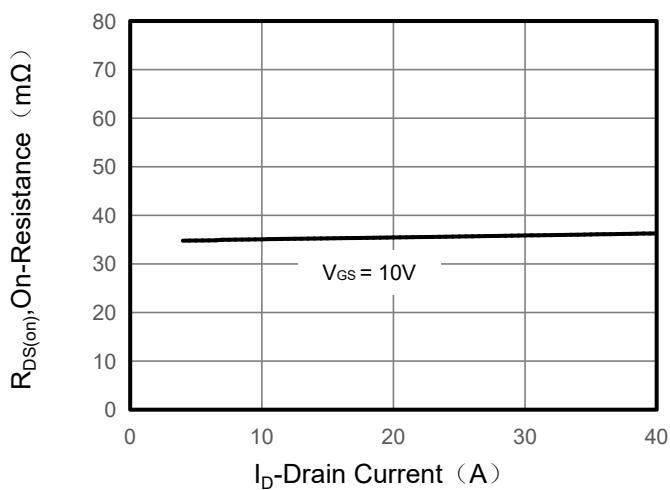
**Figure 1. Output Characteristics**



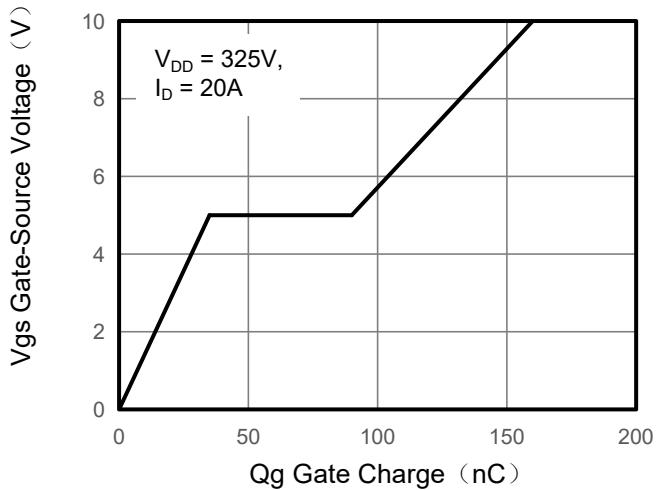
**Figure 2. Transfer Characteristics**



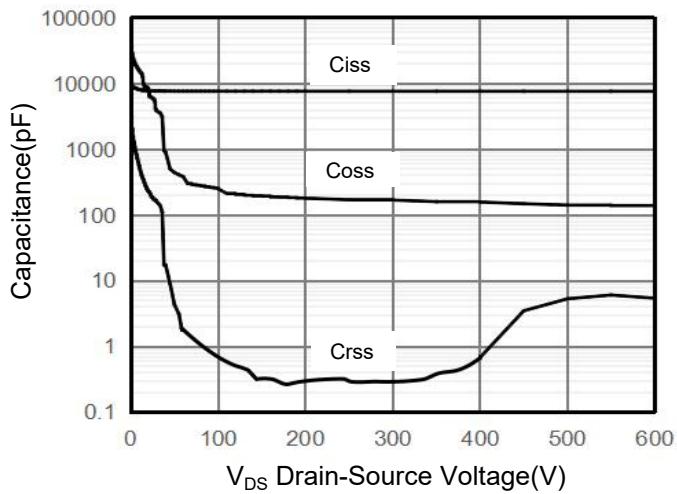
**Figure 3. Drain Source On Resistance**



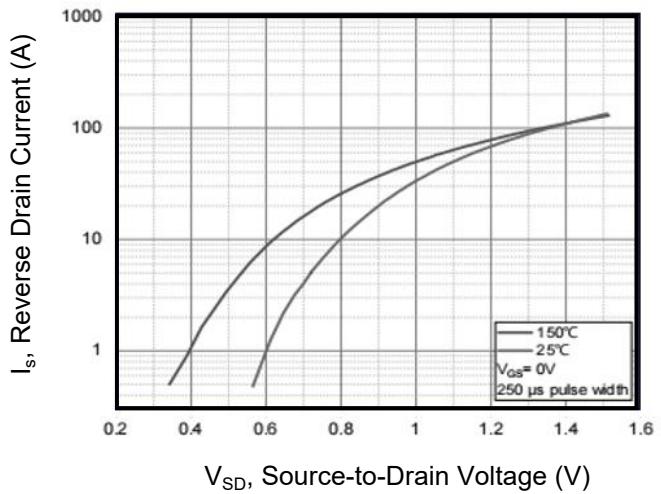
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

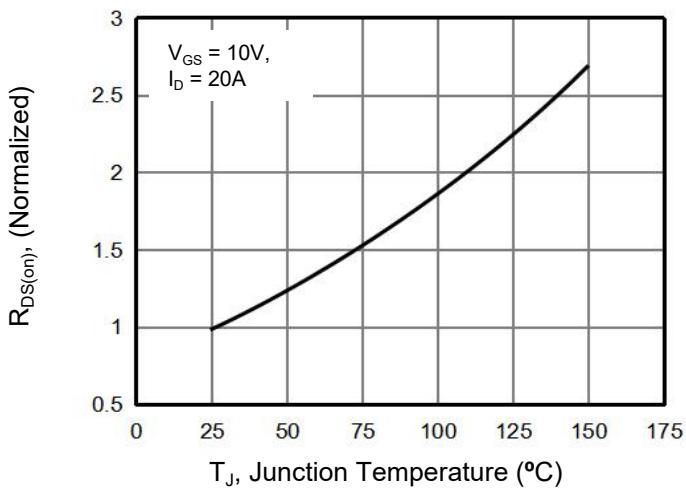


**Figure 6. Source-Drain Diode Forward**

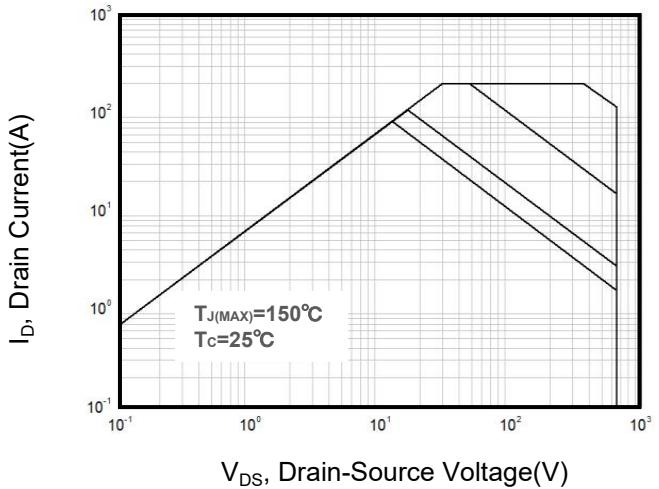


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

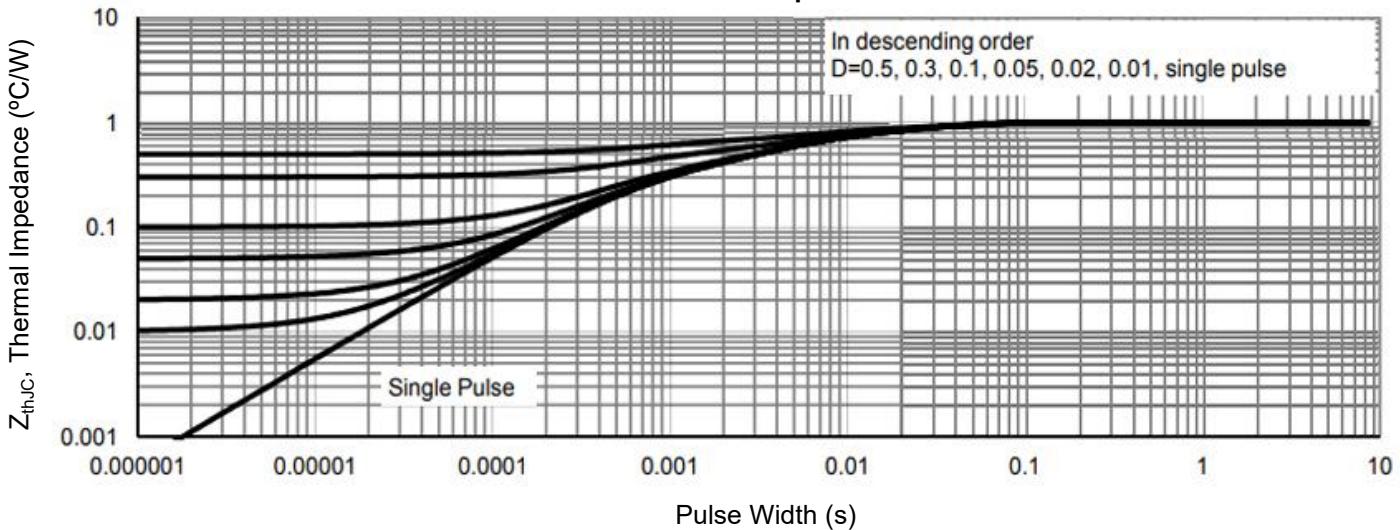
**Figure 7. Drain-Source On-Resistance**



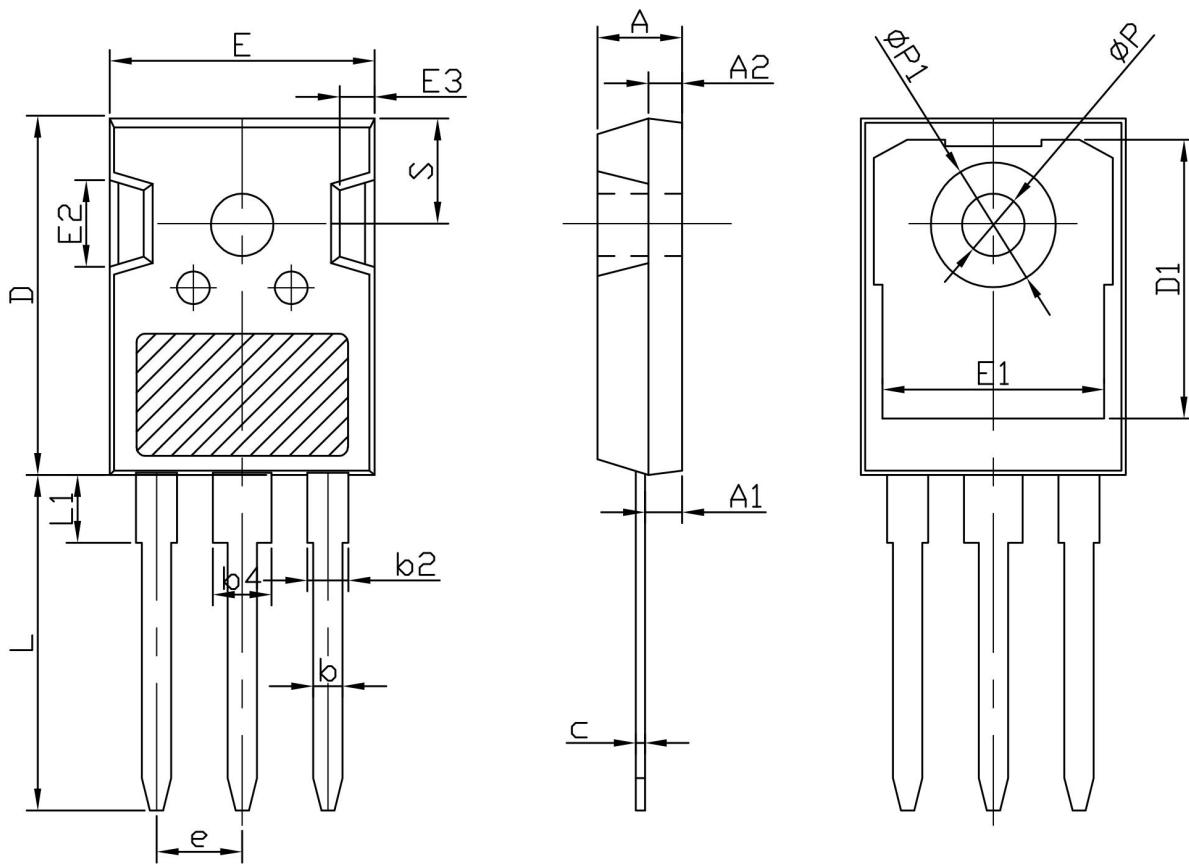
**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## TO-247 Package Information



SYMBOL	mm		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.62	19.92	20.22
L1	-	-	4.30
DP	3.40	3.60	3.80
DP1	-	-	7.30
S	6.15BSC		