






SPECIFICATIONS

CUSTOMER : _____
MODEL NO. : **GFG192065B-BNFE**
VERSION : **B**
DATE : **2022.12.21**
CERTIFICATION : **ROHS**

Customer Sign	Approved By	Prepared By	Prepared By
			

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1. SCOPE

This specification covers the engineering requirements for the GFG192065B-BNFE liquid crystal module.

2. PRODUCT SPECIFICATIONS

2.1 General

- 192 × 65 dot matrix LCD
- STN(Blue), Negative mode LCD panel, Glare
- Transmissive , Wide temperature type
- 6 o'clock
- Back light: Edge LED (WHITE)
- Multiplexing driving : 1/65duty, 1/9bias
- Controller IC: UC1604C

2.2 Mechanical Characteristics

Item	Characteristic
Dot configuration	192 × 65
Dot dimensions(mm)	0.193 × 0.195
Dot spacing (mm)	0.213 × 0.215
Module dimensions (Horizontal × Vertical × Thickness, mm)	56.7 × 24.3 × 3.5(T)
Viewing area (Horizontal × Vertical, mm)	43.28 × 18.0
Active area (Horizontal × Vertical, mm)	40.876 × 13.955
INTERFACE	4-wire SPI w/ 8-bit token (S8)



2.3 Absolute Maximum Ratings (Without LED back-light)

Characteristic	Symbol	Unit	Value
Operating Voltage (logic)	V _{DD}	V	-0.3 to +4.0
LCD Generated voltage	V _{LCD}	V	-0.3 to +13.2

Note 1: Referenced to V_{SS}=0V

2.4 Electrical Characteristics (Without LED back-light)

1/65 duty, 1/9bias, V_{op}=10.0V, T_a=25°C

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	V _{DD} – V _{SS}	T _a =25°C	2.4	3.3	3.6	V
LCD Drive Voltage (Recommended Voltage)	V _{LCD}	T _a =25°C	9.7	10.0	10.3	V
IC OUTPUT V _{lcd} Voltage	V _{LCD}	T _a =25°C	4.8	-	11.5	V
Input Voltage	"H" Level	T _a =25°C	0.9*V _{DD}	-	V _{DD}	V
	"L" Level		V _{SS}	-	0.1*V _{DD}	V
Output Voltage	"H" Level	I _{out} =-0.1mA	0.8V _{DD}	-	-	V
	"L" Level	I _{out} =0.1mA	-	-	0.2V _{DD}	V
Current Consumption	I _{DD}	V _{IN} =V _{DD}	-	-	2	mA

2.5 Optical Characteristics Absolute maximum ratings

Item	Symbol	Rating	Unit
Operating temperature range	Top	-20~70	°C
Storage temperature range	Tst	-30~80	°C

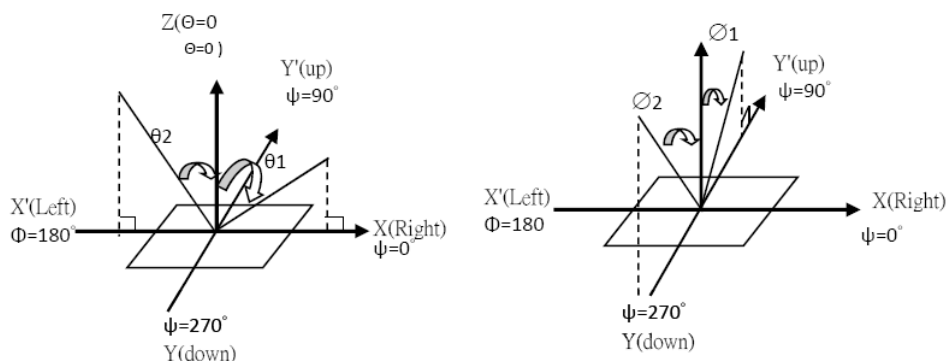


2.6 Optical Characteristics

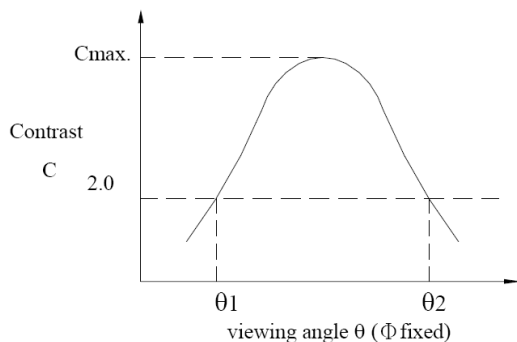
1/65 duty, 1/9bias, Vop=10.0V, Ta=25°C

Item	Symbol	Conditions	Min.	Typ.	Max	Reference
Driving voltage	Vop		9.7	10.0	10.3	
Viewing angle	$\theta 1(\Phi=0^\circ)$	$C \geq 2.0$	35°	--	--	Notes 1 & 2
	$\theta 2(\Phi=180^\circ)$		35°	--	--	
	$\theta 1(\Phi=90^\circ)$		15°	--	--	
	$\theta 2(\Phi=270^\circ)$		40°	--	--	
Contrast	C	$\theta=5^\circ, \phi=0^\circ$	2.0	--	--	Note 3
Response time(rise)	ton	$\theta=5^\circ, \phi=0^\circ$	--	80	160ms	Note 4
Response time(fall)	toff	$\theta=5^\circ, \phi=0^\circ$	--	100	200ms	Note 4

Note 1: Definition of angles θ and ϕ

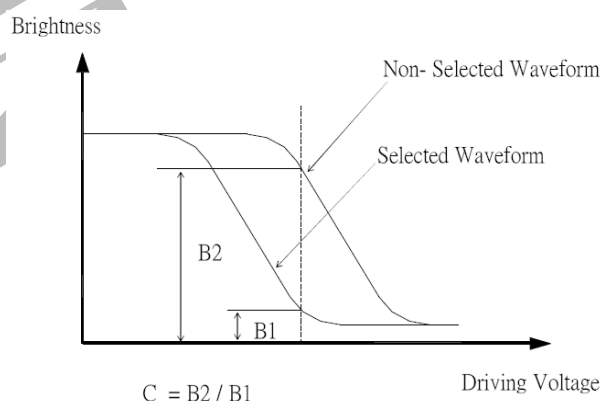


Note 2: Definition of viewing angles $\theta 1$ and $\theta 2$

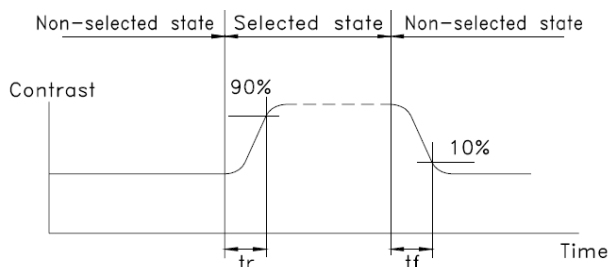


Note : Optimum viewing angle with the naked eye and viewing angle θ at Cmax. Above are not always the same

Note 3: Definition of contrast C



Note 4: Definition of response time



Note: Measured with a transmissive LCD panel which is displayed 1 cm²

V_{OPR} : Operating voltage

f_{FRM} : Frame frequency

t_{ON} : Response time (rise) t_{OFF} : Response time (fall)



2.7 LED Back-light Characteristics

2.7.1 Electrical / optical specifications

Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward voltage	V_f	If=30mA, White	2.7	3.0	3.3	V
LED *Luminous Intensity	I_v	If=30mA, White	1500	2000	--	cd/m ²
Chromaticity Coordinate	x	If=30mA, White	0.27	--	0.31	nm
	y		0.27	--	0.31	
Reverse Current	I_R	VR=5V, White	--	--	--	uA
Luminous Uniformity	ΔL_v	If=30mA, White	70			%

Note: * Measured at the bare LED back-light unit.

2.7.2 LED Maximum Operating Range

Item	Symbol	WHITE	Unit
Power Dissipation	P_{AD}	231	mW
Forward Current	I_F	70	mA
Reverse Voltage	V_R	5	V



3. RELIABILITY

NO.	ITEM	CONDITION		STANDARD	NOTE
1	High Temp. Storage	+80°C	120 hrs	Appearance Without defect	
2	Low Temp. Storage	-30°C	120 hrs	Appearance Without defect	
3	High Temp. & High Humi. Storage	40°C 90% RH	120 hrs	Appearance Without defect	
4	High Temp. Operating Display	+70°C	120 hrs	Appearance Without defect	
5	Low Temp. Operating Display	-20°C	120 hrs	Appearance Without defect	
6	Thermal Shock	-20°C, 30min. → +70°C, 30min. ↑ (1cycle)		Appearance Without defect	10 cycles

** Dissipation current, contrast and display functions

** Polarizing filter deterioration, other appearance defects

** The function test shall be conducted after 4hours storage at the normal temperature and humidity after remove from the test chamber.



4. OPERATING INSTRUCTIONS

4.1 Input signal Function

Pin No	Symbol	Function
1	CS0	Chip Select. Chip is selected when CS0 = "L".
2	/RST	When RST="L", all control registers are re-initialized by their default states.
3	CD	Select Control data or Display data for read/write operation. "L": Control data "H": Display data
4	SCK	Serial input clock (SCK).
5	SDA	serial input/output data (SDA).
6	VDD	VDD supplies for Display Data RAM and digital logic,
7	VSS	Ground
8	VB0-	LCD Bias Voltages. Connect capacitors of CBX value between VBX+ and VBX-.(Note1)
9	VB0+	
10	VB1-	
11	VB1+	
12	VLCD	Main LCD Power Supply. Capacitor CL should be connected between VLCD and VSS.(Note1)
13	K-	LED K-
14	A+	LED A+

NOTE 1.

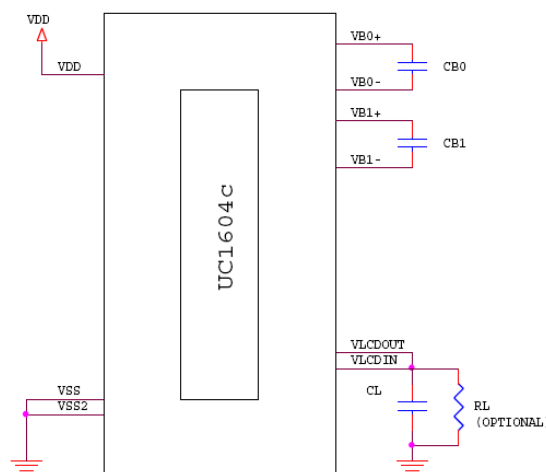


FIGURE 1: Reference circuit using internal Hi-V generator circuit

CBx : 2.2 mF/5V or 300x LCD load capacitance, whichever is higher.

CL : 330nF(16V) is appropriate for most applications.

RL : 3.3M~10M W to act as a draining circuit when VDD is shut down abruptly.



4.3 Timing Diagram

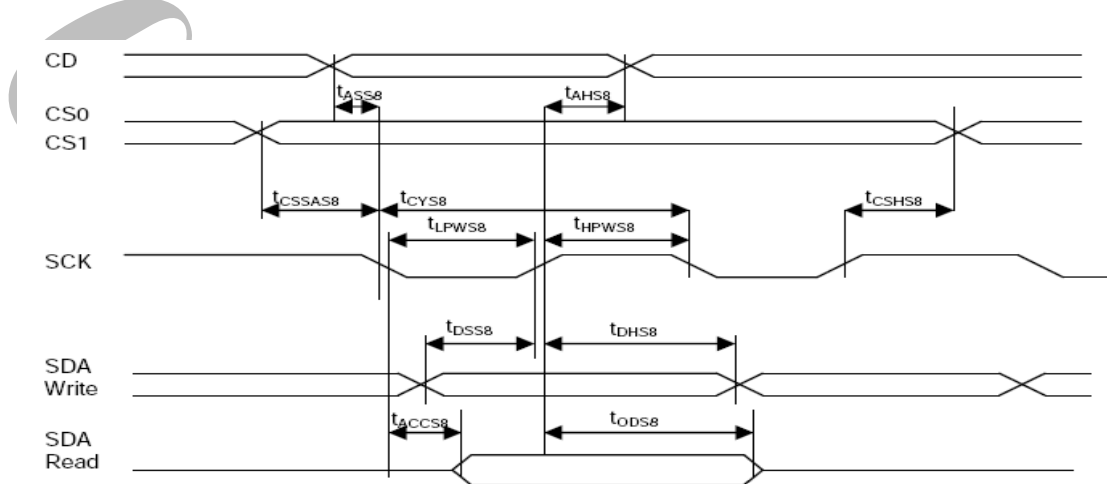


FIGURE 2 : Serial Bus Timing Characteristics (for S8)

Symbol	Signal	Description	Condition	Min.	Max.	Unit
(2.5V ≤ V _{DD} ≤ 3.6V, Ta = -30 to +85°C)						
(Read / Write)						
t _{ASS8}	CD	Address setup time		5	-	nS
t _{AHS8}	CD	Address hold time		10	-	nS
t _{CSSAS8}	CS1, CS0	Chip select setup time		5	-	nS
t _{CSHS8}		Chip select hold time		5	-	nS
t _{CYS8}	SCK	System Cycle time		190 / 70	-	nS
t _{LPWS8}		Low pulse width		80 / 20	-	nS
t _{HPWS8}		High pulse width		80 / 20	-	nS
t _{DSS8}	SDA	Data setup time		20	-	nS
t _{DHS8}	(Write)	Data hold time		10	-	nS
t _{ACC8}	SDA	Read access time	C _L = 100pF	-	80	nS
t _{OD8}	(Read)	Output disable time		-	30	nS
(1.7V ≤ V _{DD} < 2.5V, Ta = -30 to +85°C)						
(Read / Write)						
t _{ASS8}	CD	Address setup time		5	-	nS
t _{AHS8}	CD	Address hold time		10	-	nS
t _{CSSAS8}	CS1, CS0	Chip select setup time		10	-	nS
t _{CSHS8}		Chip select hold time		10	-	nS
t _{CYS8}	SCK	System Cycle time		230 / 110	-	nS
t _{LPWS8}		Low pulse width		100 / 40	-	nS
t _{HPWS8}		High pulse width		100 / 40	-	nS
t _{DSS8}	SDA	Data setup time		24	-	nS
t _{DHS8}	(Write)	Data hold time		10	-	nS
t _{ACC8}	SDA	Read access time	C _L = 100pF	-	100	nS
t _{OD8}	(Read)	Output disable time		-	60	nS

Note: tr (Rising time), tf (falling time) : ≤ 15nS

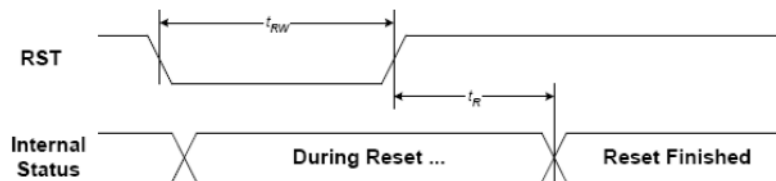


FIGURE 3 : Reset Characteristics

(1.7V ≤ V_{DD} ≤ 3.6V, Ta = -30 to +85°C)

Symbol	Signal	Description	Condition	Min.	Max.	Unit
t _{RW}	RST	Reset low pulse width		3	-	μS
t _R	RST, Internal Status	Reset to Internal Status pulse delay		6	-	mS



4.4 INSTRUCTION DESCRIPTION

Instruction Table:

The following is a list of host commands supported by UC1604c

C/D: 0: Control, 1: Data **W/R**: 0: Write Cycle, 1: Read Cycle **D7-D0**: #: Useful Data bits -: Don't Care

No	Command	C/D	W/R	D7	D6	D5	D4	D3	D2	D1	D0	Action	Default
1.	Write Data Byte	1	0	#	#	#	#	#	#	#	#	Write 1 byte	N/A
2.	Read Data Byte	1	1	#	#	#	#	#	#	#	#	Read 1 byte	N/A
3.	Get Status	0	1	ID	MX	MY	WA	DE	WS	MD	MS	Get Status PMO[5:0]	N/A
4.	Set Column Address LSB	0	0	0	0	0	0	#	#	#	#	Set CA [3:0]	0
	Set Column Address MSB	0	0	0	0	0	1	#	#	#	#	Set CA [7:4]	0
5.	Set Temp. Compensation	0	0	0	0	1	0	0	1	#	#	Set TC[1:0]	00b
6.	Set Power Control	0	0	0	0	1	0	1	#	#	#	Set PC[2:0]	110b
7.	Set Adv. Program Control (double-byte command)	0	0	0	0	1	1	0	0	R	R	Set APC[R][7:0], R = 0~3	N/A
8.	Set Scroll Line	0	0	0	1	#	#	#	#	#	#	Set SL[5:0]	0
9.	Set Page Address	0	0	1	0	1	1	#	#	#	#	Set PA[3:0]	0
10.	Set V _{BIAS} Potentiometer (double-byte command)	0	0	1	0	0	0	0	0	0	1	Set PM[7:0]	49H
11.	Set Partial Display Control	0	0	1	0	0	0	0	1	0	#	Set LC[5]	0b
12.	Set RAM Address Control	0	0	1	0	0	0	1	#	#	#	Set AC[2:0]	001b
13.	Set Frame Rate	0	0	1	0	1	0	0	0	#	#	Set LC[4:3]	01b
14.	Set All-Pixel-ON	0	0	1	0	1	0	0	1	0	#	Set DC[1]	0b
15.	Set Inverse Display	0	0	1	0	1	0	0	1	1	#	Set DC[0]	0b
16.	Set Display Enable	0	0	1	0	1	0	1	1	1	#	Set DC[2]	0b
17.	Set LCD Mapping Control	0	0	1	1	0	0	0	#	#	0	Set LC[2:1]	00b
18.	System Reset	0	0	1	1	1	0	0	0	1	0	System Reset	N/A
19.	NOP	0	0	1	1	1	0	0	0	1	1	No operation	N/A
20.	Set Test Control (double-byte command)	0	0	1	1	1	0	0	1	TT		For testing only. Do not use.	N/A
21.	Set LCD Bias Ratio	0	0	1	1	1	0	1	0	#	#	Set BR[1:0]	11b: 9
22.	Set COM End	0	0	1	1	1	1	0	0	0	1	Set CEN[5:0]	63D
23.	Set Partial Display Start	0	0	1	1	1	1	0	0	1	0	Set DST[5:0]	0
				--	--	#	#	#	#	#	#		
24.	Set Partial Display End	0	0	1	1	1	1	0	0	1	1	Set DEN[5:0]	63D
				--	--	#	#	#	#	#	#		
25.	Set MTP Operation Control	0	0	1	1	1	1	1	0	0	0	Set MTPC[4:0]	00H
				--	--	--	#	#	#	#	#		
26.	Set MTP Write Mask	0	0	1	1	1	1	1	0	0	1	Set MTPM[5:0]	0
27.	Set V _{MTP1} Potentiometer	0	0	1	1	1	1	0	1	0	0	Set VMTP1[7:0]	N/A
				#	#	#	#	#	#	#	#		
28.	Set V _{MTP2} Potentiometer	0	0	1	1	1	1	0	1	0	1	Set VMTP2[7:0]	N/A
				#	#	#	#	#	#	#	#		
29.	Set MTP Write Timer	0	0	1	1	1	1	0	1	1	0	Set MTPWT[7:0]	N/A
				#	#	#	#	#	#	#	#		
30.	Set MTP Read Timer	0	0	1	1	1	1	0	1	1	1	Set MTPRT[7:0]	N/A
				#	#	#	#	#	#	#	#		
Serial Read Command (Enabled only in S8/S9 mode)													
31.	Get Status	0	0	1	1	1	1	1	1	1	0	Get Status PMO[5:0]	N/A
		0	1	ID	MX	MY	WA	DE	WS	MD	MS		
32.	Read Data	0	0	1	1	1	1	1	1	1	1		FFH
		1	1	#	#	#	#	#	#	#	#		

Any bit pattern other than those listed above may result in NOP (No Operation).



5. NOTES

Safety

- If the LCD panel breaks, be careful not to get the liquid crystal in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Handling

- Avoid static electricity as this can damage the CMOS LSI.
- The LCD panel is plate glass; do not hit or crush it.
- Do not remove the panel or frame from the module.
- The polarizing plate of the display is very fragile; handle it very carefully

Mounting and Design

- Mount the module by using the specified mounting part and holes.
- To protect the module from external pressure, leave a small gap by placing transparent plates (e.g. acrylic or glass) on the display surface, frame, and polarizing plate
- Design the system so that no input signal is given unless the power-supply voltage is applied.
- Keep the module dry. Avoid condensation, otherwise the transparent electrodes may break.

Storage

- Store the module in a dark place where the temperature is $25\text{ }^{\circ}\text{C}\pm 10\text{ }^{\circ}\text{C}$ and the humidity below 65% RH.
- Do not store the module near organic solvents or corrosive gases.
- Do not crush, shake, or jolt the module (including accessories).

Cleaning

- Do not wipe the polarizing plate with a dry cloth, as it may scratch the surface.
- Wipe the module gently with soft cloth soaked with a petroleum benzine.
- Do not use ketonic solvents (ketone and acetone) or aromatic solvents (toluene and xylene), as they may damage the polarizing plate.

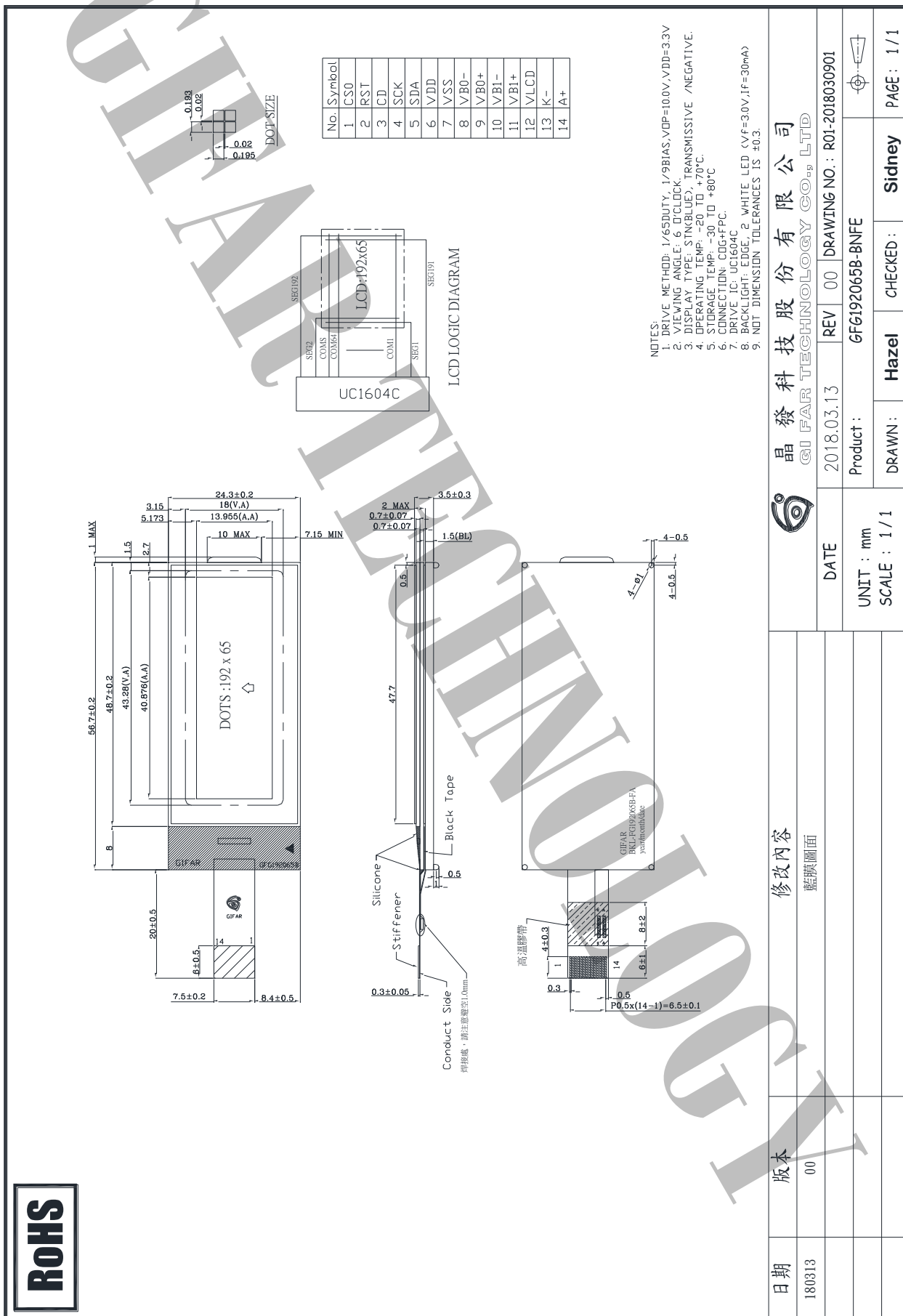
6. OPERATION PRECAUTIONS

Any changes that need to be made in this specification or any problems arising from it will be dealt with quickly by discussion between both companies.

Quality warranty period: Within one year after shipment date (excluding abnormal usage way and abnormal environments.)



7. LCM Dimension





8. PACKAGE INFORMATION

1	1 Tray	:	30 pcs (modules)
2	1 stack	:	10 tray +1 Cover tray= 300pcs
3	1 Carton	:	3 stack = 900pcs
4	Total pcs	:	1 Carton (30pcs * 10tray * 3 stack) = 900 pcs
5	Carton size = NO. 17	:	495*315*435mm
6	Net weight	:	TBD KG
7	Gross weight	:	TBD KG

** Packaging information**

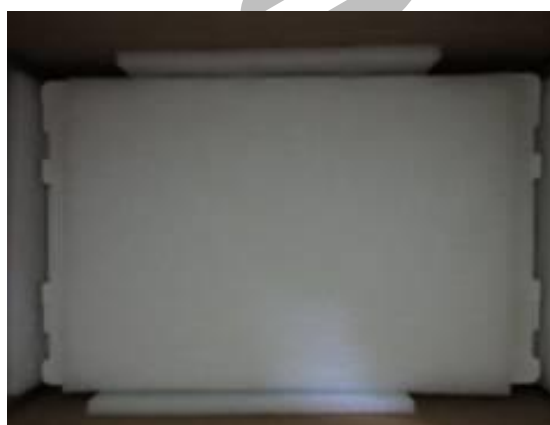
- 1 Tray = 20 pcs



- 1 stack=10 tray+1 Cover tray



- 1 Carton = 3 stack, Total pcs = 900 pcs



出貨檢驗標準書
Shipping inspection standard

核准 Approved by	審核 Checked by	作成 Made by
ANDY	JACKY	RUBY

1.目的 Purpose :

規範出貨產品之檢驗項目及判斷標準，確保產品出貨能滿足客戶要求。

Standardize the inspection items and judgment standards to ensure the products that shipped out can meet customer's requirements.

2.範圍 Area :

適用於出廠之所有產品。

Applicable to all products shipped from the factory.

3.名詞解釋 Explanation of terms :

3-1 主要缺陷：亦會造成功能缺失或嚴重外觀缺陷。

Major Defects: It also causes loss of function or serious appearance defects.

3-2 次要缺陷：稍有缺陷但不影響客戶使用。

Minor defect: Slightly defective but does not affect customer use.

4.檢驗體制 Inspection system :

4-1 抽樣計劃：依 ANSI/ASQ Z1.4 一般檢驗水準 II 之 正常檢驗一次抽驗方案。

Sampling plan: According to ANSI/ASQ Z1.4 general inspection level II the normal inspection one-time sampling plan.

4-2 允收水準 Acceptable Level : (AQL)

主要缺陷 Major defect : 0.4 %

次要缺陷 Minor defect : 0.65 %

5.檢驗條件 Inspection conditions :

5-1 使用相關之檢測儀器及測試、量測工具。

Use relevant testing instrument, testing and measuring tools .

5-2 環境要求：其條件需控制在常溫下 $23^{\circ}\text{C}\pm 3^{\circ}\text{C}$ 及溼度 70%RH 以下。

Environmental requirements: The conditions should be controlled at room temperature $23^{\circ}\text{C}\pm 3^{\circ}\text{C}$ and humidity below 70%RH.

5-3 外觀檢驗：須在 $380\pm 20\%$ LUX 的白色日光燈下，其目視距離需於產品離 30 ± 5 cm 檢驗。

Appearance inspection: Under the white fluorescent lamp of $380\pm 20\%$ LUX , the visual distance shall be checked above the product 30 ± 5 cm.

5-4 電性測試 Electrical Testing :

5-4-1 有背光之產品需關燈並在 $5\sim 300\text{Lux}\pm 3\%$ 下檢驗。

The products with backlight should be tested at $5\sim 300\pm 3\%$ Lux.

5-4-2 無背光之產品需開燈並在 $60\sim 300\text{Lux}\pm 3\%$ 白色日光燈下檢驗。

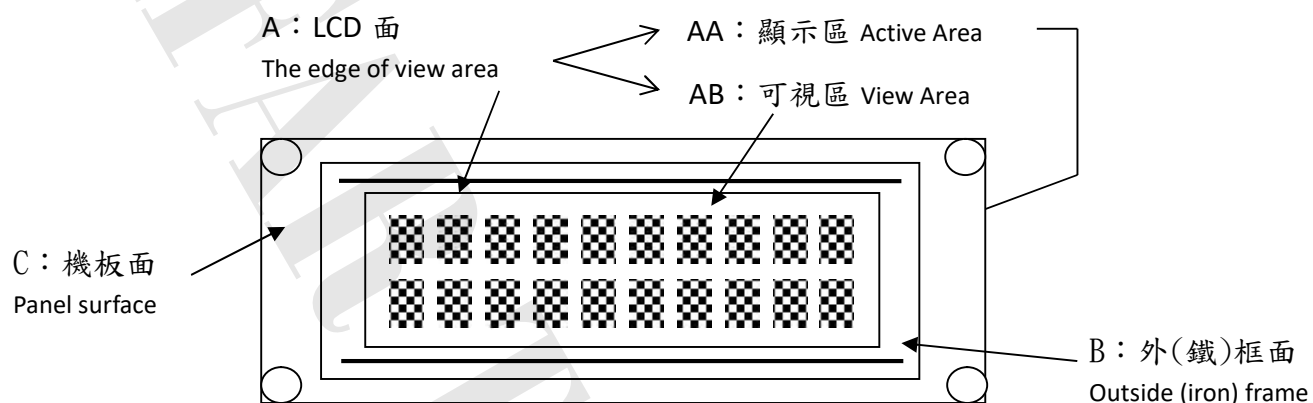
Products without backlight need to be turned on and tested under $60\sim 300 \pm 3\%$ LUX white fluorescent lamps .

5-5 檢查視角依產品視角方向。

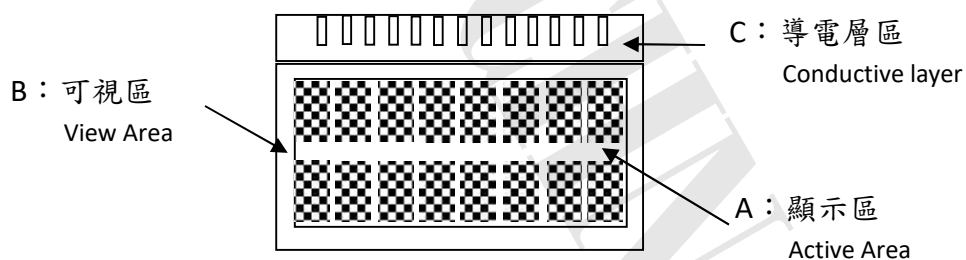
Check the viewing angle according to the product viewing angle.

5-6 其不良現象檢視區域 Bad phenomenon View area

5-6-1 適用種類 Applicable category : COB、TFT



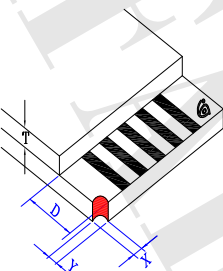
5-6-2 適用種類 Applicable category : COG、TAB、TN



種類 Category		COG																			
編號 No.	檢驗項目 Item	檢驗內容及判定標準 Inspection Content & Standard			區域 Zone	類別 Category	缺陷等級 Level														
1	點類(一) Dot(1)	黑點、刺傷...等圓狀 Black dot、Stab...and other round shape $\phi = \frac{(X + Y)}{2}$ 	兩點距離須超過 5 mm Two points have to be ≥ 5 mm		A B	外觀 Appearance	次要 Minor AQL0.65%														
		ϕ (mm)	允收數 Acceptance Qty																		
		$\phi \leq 0.1$	無視 Ignore																		
		$0.1 < \phi \leq 0.25$	3																		
		$0.25 < \phi \leq 0.3$	1																		
		$\phi > 0.3$	0																		
2	點類(二) Dot(2)	氣泡、凹凸點 Bubble、Uneven dots $\phi = \frac{(X + Y)}{2}$ 	兩點距離須超過 5 mm Two points have to be ≥ 5 mm		A B	外觀 Appearance	次要 Minor AQL0.65%														
		ϕ (mm)	允收數 Acceptance Qty																		
		$\phi \leq 0.2$	無視 Ignore																		
		$0.2 < \phi \leq 0.5$	2																		
		$\phi > 0.5$	0																		
3	線類 Line	刮傷、毛屑...等線狀 Scratch、Fiber.. and other linear shape. 	<table border="1"> <thead> <tr> <th>L (mm)</th> <th>W (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td>--</td> <td>$W \leq 0.02$</td> <td>無視 Ignore</td> </tr> <tr> <td>$L \leq 5$</td> <td>$W \leq 0.03$</td> <td>3</td> </tr> <tr> <td>$L \leq 3$</td> <td>$W \leq 0.05$</td> <td>2</td> </tr> <tr> <td>$L > 5$</td> <td>$W > 0.05$</td> <td>0</td> </tr> </tbody> </table>	L (mm)	W (mm)	允收數 Acceptance Qty	--	$W \leq 0.02$	無視 Ignore	$L \leq 5$	$W \leq 0.03$	3	$L \leq 3$	$W \leq 0.05$	2	$L > 5$	$W > 0.05$	0	A B	外觀 Appearance	次要 Minor AQL0.65%
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4	底色 Background color	同批供貨不能有明顯色差 No obvious color difference allowed in same shipment. (必要時與客端制定限度樣) (According to the gold samples if necessary)			B	外觀 Appearance	次要 Minor AQL0.65%														
5	FPC 外觀 FPC Appearance	※ FPC 上刺傷導致線路無法導通 拒收 Stabbing on the FPC causes the line to fail to conduct Reject ※ FPC 上髒污或是殘留異物以致線路無法導通 拒收 Dirty or residual foreign matter on the FPC makes the circuit unable to conduct Reject ※ FPC 直角折痕、斷裂 拒收 FPC right-angle crease and fracture Reject			C	外觀 Appearance	主要 Major AQL 0.4%														

6	點、線類 (三) Dot、Line (3)	※ 於全黑、白畫面下看見之區塊狀或線狀不良 拒收 There is a block or linear in the view area under the screen is whole black or white. Reject ※ 但依 2% ND Filter 遮蓋無視 允收 But after inspecting by 2% ND Filter without seeing block or linear, it is confirmed Acceptance	A	電訊 Electronics	次要 Minor AQL0.65%
7	點、線類 (四) Dot、Line (4)	畫面中顯示出現黑、白、亮、異色點或線狀 There is a black, white, bright or other dot or lines showing in the view area. ※ 依編號 1、3 之判定標準 According to the inspection standard: No. 1 and 3.	A	電訊 Electronics	次要 Minor AQL0.65%
8	缺字 Lack of characters	顯示時畫面缺少部份字元 拒收 Lacking part of characters in the view area. Reject	A	電訊 Electronics	主要 Major AQL 0.4%
9	無動作 No reaction	顯示畫面一直處於起始畫面而無法進行切換 拒收 The display (view area) always show in the initial screen and can't be switched to others. Reject	A	電訊 Electronics	主要 Major AQL 0.4%
10	無畫面 No display	通電後，完全無任何畫面顯示 拒收 After connecting to the power, there is no image. Reject	A	電訊 Electronics	主要 Major AQL 0.4%
11	斷線 Broken line	顯示畫面中少直、橫線 拒收 There is a lack of vertical or horizontal lines in the view area. Reject	A	電訊 Electronics	主要 Major AQL 0.4%
12	CROSS TALK	顯示畫面時有局部之條紋或拖影 There are some stripes or shadow/smear showing in the view area. 拒收或與客端簽訂限度樣 Reject or inspect according to the golden sample	A	電訊 Electronics	次要 Minor AQL0.65%
13	I CON	顯示畫面缺少部份顯示圖案 拒收 Lack of partial ICON in the view area. Reject	A	電訊 Electronics	主要 Major AQL 0.4%

14	深淺不一 Color difference	顯示畫面的對比，比其他顯示深或淺並依電氣規格(VOP)值判定 The color contrast of display is obviously lighter or darker than others and according to the VOP value in the electronics specification. 拒收或與客端簽訂限度樣 Reject or inspect according to the golden sample	A	電訊 Electronics	次要 Minor AQL0.65%												
15	畫面異常 Abnormal screen	通電後畫面出現未定義之電訊不良現象 拒收 After connecting to the power, there is an undefined electronics appearance showing in the view area. Reject	A	電訊 Electronics	主要 Major AQL 0.4%												
16	背光色不均 Uneven color of backlight	※ 點亮後 LED 有明暗不均現象依其均勻度判定 拒收 After lighting LEDs have brightness and darkness uneven the determined according to its uniformity. Reject ※ 點亮後 LED 色澤不一致 拒收 LED color is inconsistent after lighting. Reject	A	電訊 Electronics	次要 Minor AQL0.65%												
17	亮度不足 Lack of brightness	波長、色座標、輝度與圖面標示定義不符 拒收 Wave length, chromatic coordinates, brightness don't correspond to the definition of the drawing. Reject	A	電訊 Electronics	主要 Major AQL 0.4%												
18	背光腳柱 Backlit foot post	斷裂、長度不一 拒收 Fracture, different length Reject	--	外觀 Appearance	次要 Minor AQL0.65%												
19	破損 Damaged	<p>Y：破損寬 X：破損長 Y: Damaged width X: Damaged length</p>  <table border="1"> <thead> <tr> <th>Y</th> <th>X</th> <th>判定 Determination</th> </tr> </thead> <tbody> <tr> <td>Y ≤ 1.0</td> <td>-- --</td> <td>允收 Acceptance</td> </tr> <tr> <td>未進入可視區 Did not enter the viewing area</td> <td>≤ 1/8 玻璃該邊長 ≤ 1/8 The side length of the glass</td> <td>允收 Acceptance</td> </tr> <tr> <td>進入可視區 Enter the viewing area</td> <td>-- --</td> <td>拒收 Reject</td> </tr> </tbody> </table>	Y	X	判定 Determination	Y ≤ 1.0	-- --	允收 Acceptance	未進入可視區 Did not enter the viewing area	≤ 1/8 玻璃該邊長 ≤ 1/8 The side length of the glass	允收 Acceptance	進入可視區 Enter the viewing area	-- --	拒收 Reject	B	外觀 Appearance	次要 Minor AQL0.65%
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21	尺寸量測 Size Measurement	未依圖面上標示 拒收 No correspond to the indication on the drawing. Reject		ALL	外觀 Appearance	主要 Major AQL 0.4%												
22	其他 Other	如發現有上述未定義之不良則與客端簽訂限度樣 If there is another undefined defective situation. It will be listed as others. The inspection standard is according to the golden sample.		ALL	電訊 Electronics 外觀 Appearance	次要 Minor AQL0.65%												