## Crystal Oscillator, Series FCO-5K

SMD Crystal Oscillator 5.0×3.2 mm 32.768kHz

#### **FEATURE**

- Typical 5.0×3.2×1.2 mm ceramic SMD package
- Tight symmetry (45 to 55%) available
- Operation voltage: 1.8V, 2.5V, 3.3V
- Tri-state enable / disable
- Built-in ASIC enables reduction of current consumption



## • ELECTRICAL SPECIFICATIONS

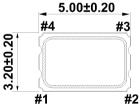
Item			Specifications						
Parameter		3.3V		2.5V		1.8V		Unit	
		Min.	Max.	Min.	Max.	Min.	Max.	Offic	
Supply Voltage Variation		2.97	3.63	2.25	2.75	1.62	1.98	V	
Supply Current	@ 15pF Load	-	70	-	66	-	63	uA	
	@ no load	-	65	-	62	-	60		
Duty Cycle	Duty Cycle		55	45	55	45	55	%	
Transition Time :Rise/Fall	Time	-	50	-	50	- 50		nSec	
Output Lovel	Out High(Logic"1")	2.97		2.25		1.62		V	
Output Level	Out Low(Logic"0")		0.33		0.25		0.18		
Startup Time		-	2	-	2	-	2	mSec	
Tri-State	Enable(High Voltage or floating)	2.31	-	1.75	-	1.26	-	V	
(Input to Pin 1)	Disable(Low Voltage or GND)	-	0.99	-	0.75	-	0.54	V	
Aging(@25 1st year)		-	±3	-	±3	-	±3	ppm	
Storage Temp. Range		-55	125	-55	125	25 -55 125		°C	

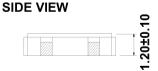
Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

#### DIMENSION (mm)

#### **⑤** SOLDER PAD LAYOUT(mm)



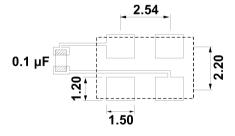




# 2.54±0.15 #3 $2.20\pm0.15$ #2 1.20±0.15

**BOTTOM VIEW** 





To ensure optimal oscillator performance, place a by-pass capacitor of  $0.1 \mu F$  as close to the part as possible between Vdd and GND pads

### ○ FREQ. STABILITY vs. TEMP. RANGE

ppm Temp. (°C)	±20	±25	±40	±50
-10 ~ +60	0	0	0	0
-20 ~ +70	Δ	0	0	0
-40 ~ +85	×	Δ	0	0
-40 ~ +125	×	×	Δ	0

o: Available A: Conditional X: Not available Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration

<sup>+</sup> Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.