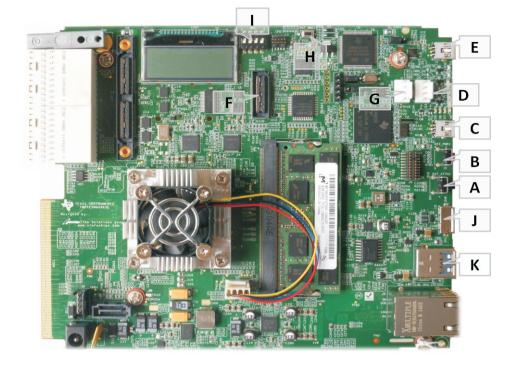


EVMK2E Evaluation Module Quick Start Guide

EVMK2E Evaluation Module



А	No Functionality	F	JTAG(MIPI-60) connector for SoC
В	1 press: safe shutdown of SOC; 2 presses within 0.5sec: warm reset; 3 presses within 0.5sec: full reset; 4 presses within 0.5sec: cancel reset	G	COM2 : BMC UART Console
С	On board XDS200 USB	Н	BMC Reset : Resets the microcontroller and will reset the entire board
D	COM1 : SoC UART Console	I	Dip switch for boot configuration : 1111 : No Boot mode/JTAG DSP Little Endian 0010 : NOR SPI ARM Master Boot
Е	USB control of both SOC COM1 & BMC COM2	J	USB3 PORT 1(Device and HOST Modes)
		Κ	USB3 PORT 0(HOST only)

Two boot procedures are described below, one for Uboot and one for CCS.

1) Uboot Steps:

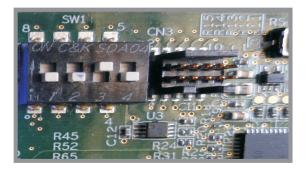
E:\Drivers		
Organize 🔻 Include	in library 🔻 Share with 🔻 Burn New	folder 📑 👻 🗍
★ Favorites ■ Desktop	x64 File folder	x86 File folder
Downloads	CP210xVCPInstaller_x64 Driver Package Installer Microsoft Corporation	CP210xVCPInstaller_x86 Driver Package Installer Microsoft Corporation
Libraries Documents Music	Apinst XML Document 11.2 KB	ReleaseNotes Text Document 9.74 KB
E Pictures	SLAB_License_Agreement_VCP_Wi ndows Text Document	slabvcp Security Catalog 11.0 KB
Computer	Setup Information 4.94 KB	

For Windows User: Insert pendrive and load USB COM drive by executing the program\drivers\ CP210xVCPInstaller_x86 For 64-bit systems execute the program\drivers\

For Linux User:

Please download driver from any of below link as per Linux kernel version on PC. Compile the driver and install it.

http://www.silabs.com/Support%20Documents/Software/Linux_3.x.x_VCP_Driver_Source.zip http://www.silabs.com/Support%20Documents/Software/Linux_2.6.x_VCP_Driver_Source.zip





Ensure EVM configuration switches are set as shown for NOR SPI Boot.



Connect the USB mini-B cable to J1

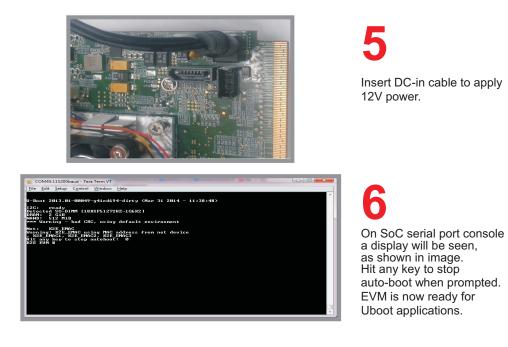
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115200 -	
113200	
8 bit 💌	Cancel
none 🔻	
1 bit 💌	Help
none 💌	
	none



Open Tera Term, select COM port, configure settings, as shown in image.

This serial port settings of Tera Term is shown as an example. It is not mandatory to use only Tera Term as a serial terminal

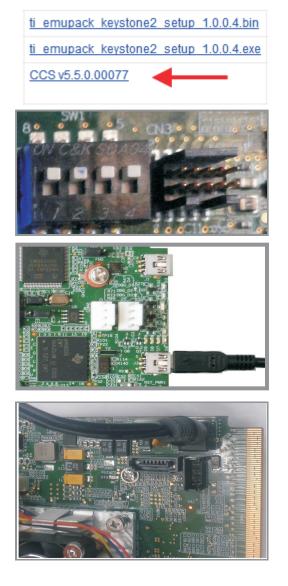


MCSDK Downlink Page:

http://software-dl.ti.com/sdoemb/sdoemb public sw/mcsdk/latest/index FDS.html

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2) CCS Steps:





Update or install CCS (V5.5.0.00077 or later) from MCSDK download page.



Ensure EVM configuration switch are set as shown for No Boot/JTAG DSP Little Endian Boot mode.

3

Connect the USB mini-B cable to XDS200.

4

Insert DC-in cable to apply 12V power.

5

Launch CCS. After CCS is opened, EVM is now ready for use.

Getting Started:

Please see the 'Getting Started' chapter in the MCSDK User Guide for hardware and software setup:

http://processors.wiki.ti.com/index.php/MCSDK_User_Guide_for_KeyStone_II.

Important Notes when handling the EVM:

Plug in all the cables before powering ON the EVM to ensure proper grounding. Do not unnecessarily flex the boards, while inserting cables. Excessive flexing can break the PCB traces. Use proper ESD procedures when handling the EVM.

Warning: Some components on the EVM may be too hot to touch with your bare hand when power is on.

Online Documentation

TI device product page http://www.ti.com/product/66ak2e05

Multicore SDK

http://www.ti.com/tool/bioslinuxmcsdk http://processors.wiki.ti.com/index.php/M CSDK User Guide for KeyStone II

CCS v5

http://processors.wiki.ti.com/index.php/Categ ory:Code Composer Studio v5

C663X EVM Information:

https://www.einfochips.com/tl-66ak2e-evm

This link contains:

Hardware & Software details User Guide Technical Reference Manual

EVMK2E EVM Support:

Please write to tievmsupport@einfochips.com for technical queries related to this EVM.

elnfochips Address:

elnfochips Limited. 303,Parishram Building,5/B Rashmi Society Mithakhali Six Road,Navarangpura. Ahmedabad- 380009. Gujarat, INDIA. Tel : +91-79-2656 3705. Fax :+91-79-2656 0722.

http://www.einfochips.com/

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