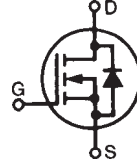


Polar™ Power MOSFET IXFC52N30P

HiPerFET™

(Electrically Isolated Back Surface)

N-Channel Enhancement Mode
Avalanche Rated



$$V_{DSS} = 300V$$

$$I_{D25} = 24A$$

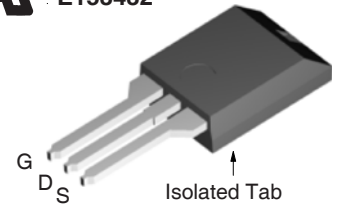
$$R_{DS(on)} \leq 80m\Omega$$

$$t_{rr} \leq 200ns$$

| Symbol | Test Conditions | Maximum Ratings | |
|---------------|--|--------------------|------------|
| V_{DSS} | $T_J = 25^\circ C$ to $150^\circ C$ | 300 | V |
| V_{DGR} | $T_J = 25^\circ C$ to $150^\circ C$, $R_{GS} = 1M\Omega$ | 300 | V |
| V_{GSS} | Continuous | ± 20 | V |
| V_{GSM} | Transient | ± 30 | V |
| I_{D25} | $T_C = 25^\circ C$ | 24 | A |
| I_{DM} | $T_C = 25^\circ C$, Pulse Width Limited by T_{JM} | 150 | A |
| I_A | $T_C = 25^\circ C$ | 52 | A |
| E_{AS} | $T_C = 25^\circ C$ | 1 | J |
| dv/dt | $I_S \leq I_{DM}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ C$ | 10 | V/ns |
| P_D | $T_C = 25^\circ C$ | 100 | W |
| T_J | | -55 ... +150 | $^\circ C$ |
| T_{JM} | | 150 | $^\circ C$ |
| T_{stg} | | -55 ... +150 | $^\circ C$ |
| T_L | Maximum Lead Temperature for Soldering | 300 | $^\circ C$ |
| T_{SOLD} | Plastic Body for 10s | 260 | $^\circ C$ |
| V_{ISOL} | 50/60 Hz, RMS | $t = 1min$ | 2500 V~ |
| | $I_{ISOL} \leq 1mA$ | $t = 1s$ | 3000 V~ |
| M_d | Mounting force | 11..66 / 2.5..14.6 | N/lb. |
| Weight | | 2 | g |

ISOPLUS220™

E153432



G = Gate D = Drain
S = Source

Features

- UL Recognized Package
- Silicon Chip on Direct-Copper-Bond Substrate
 - High Power Dissipation
 - Isolated Mounting Surface
 - 2500V~ Electrical Isolation
- Fast Intrinsic Rectifier
- Avalanche Rated
- Low $R_{DS(ON)}$ and Q_G
- Low Package Inductance

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- Laser Drivers
- AC and DC Motor Drives
- Robotics and Servo Controls

| Symbol | Test Conditions ($T_J = 25^\circ C$, Unless Otherwise Specified) | Characteristic Values | | |
|--------------|---|-----------------------|------|--------------------|
| | | Min. | Typ. | Max. |
| BV_{DSS} | $V_{GS} = 0V$, $I_D = 250\mu A$ | 300 | | V |
| $V_{GS(th)}$ | $V_{DS} = V_{GS}$, $I_D = 4mA$ | 2.5 | | 5.0 V |
| I_{GSS} | $V_{GS} = \pm 20V$, $V_{DS} = 0V$ | | | ± 100 nA |
| I_{DSS} | $V_{DS} = V_{DSS}$, $V_{GS} = 0V$ $T_J = 125^\circ C$ | | | 25 μA 1 mA |
| $R_{DS(on)}$ | $V_{GS} = 10V$, $I_D = 26A$, Note 1 | | | 80 m Ω |

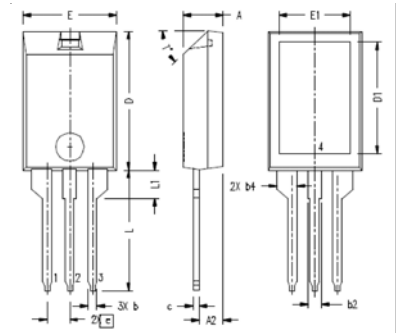
| Symbol | Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified) | Characteristic Values | | |
|--------------|---|-----------------------|------|--------------------|
| | | Min. | Typ. | Max. |
| g_{fs} | $V_{DS} = 10\text{V}$, $I_D = 26\text{A}$, Note 1 | 20 | 30 | S |
| C_{iss} | $V_{GS} = 0\text{V}$, $V_{DS} = 25\text{V}$, $f = 1\text{MHz}$ | | 3490 | pF |
| C_{oss} | | | 550 | pF |
| C_{rss} | | | 130 | pF |
| $t_{d(on)}$ | Resistive Switching Times $V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 52\text{A}$ $R_G = 4\Omega$ (External) | | 24 | ns |
| t_r | | | 22 | ns |
| $t_{d(off)}$ | | | 60 | ns |
| t_f | | | 20 | ns |
| $Q_{g(on)}$ | $V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 26\text{A}$ | | 110 | nC |
| Q_{gs} | | | 25 | nC |
| Q_{gd} | | | 53 | nC |
| R_{thJC} | | | 1.25 | $^\circ\text{C/W}$ |
| R_{thCS} | | 0.21 | | $^\circ\text{C/W}$ |

Source-Drain Diode

| Symbol | Test Conditions ($T_J = 25^\circ\text{C}$, unless otherwise specified) | Characteristic Values | | |
|----------|---|-----------------------|------|--------|
| | | Min. | Typ. | Max. |
| I_s | $V_{GS} = 0\text{V}$ | | | 52 A |
| I_{SM} | Repetitive, pulse width limited by T_{JM} | | | 150 A |
| V_{SD} | $I_F = 52\text{A}$, $V_{GS} = 0\text{V}$, Note 1 | | | 1.5 V |
| t_{rr} | $I_F = 25\text{A}$, $-di/dt = 100\text{A}/\mu\text{s}$ $V_R = 100\text{V}$, $V_{GS} = 0\text{V}$ | | 160 | 200 ns |
| Q_{RM} | | | 800 | nC |
| I_{RM} | | | 7 | A |

Note 1: Pulse test, $t \leq 300\mu\text{s}$; duty cycle, $d \leq 2\%$.

ISOPLUS220™ Outline



Note:
Bottom heatsink (Pin 4) is electrically isolated from Pin 1, 2, or 3.

| SYM | INCHES | | MILLIMETERS | |
|-----|------------|------|-------------|-------|
| | MIN | MAX | MIN | MAX |
| A | .157 | .197 | 4.00 | 5.00 |
| A2 | .098 | .118 | 2.50 | 3.00 |
| b | .035 | .051 | 0.90 | 1.30 |
| b2 | .049 | .065 | 1.25 | 1.65 |
| b4 | .093 | .100 | 2.35 | 2.55 |
| c | .028 | .039 | 0.70 | 1.00 |
| D | .591 | .630 | 15.00 | 16.00 |
| D1 | .472 | .512 | 12.00 | 13.00 |
| E | .394 | .433 | 10.00 | 11.00 |
| E1 | .295 | .335 | 7.50 | 8.50 |
| e | .100 BASIC | | 2.55 BASIC | |
| L | .512 | .571 | 13.00 | 14.50 |
| L1 | .118 | .138 | 3.00 | 3.50 |
| T* | | | 42.5* | 47.5* |

Ref: IXYS CO 0177 R0

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:

| | | | | | | | | | |
|-----------|-----------|-----------|-----------|--------------|--------------|--------------|--------------|--------------|-------------|
| 4,835,592 | 4,931,844 | 5,049,961 | 5,237,481 | 6,162,665 | 6,404,065 B1 | 6,683,344 | 6,727,585 | 7,005,734 B2 | 7,157,338B2 |
| 4,850,072 | 5,017,508 | 5,063,307 | 5,381,025 | 6,259,123 B1 | 6,534,343 | 6,710,405 B2 | 6,759,692 | 7,063,975 B2 | |
| 4,881,106 | 5,034,796 | 5,187,117 | 5,486,715 | 6,306,728 B1 | 6,583,505 | 6,710,463 | 6,771,478 B2 | 7,071,537 | |

Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

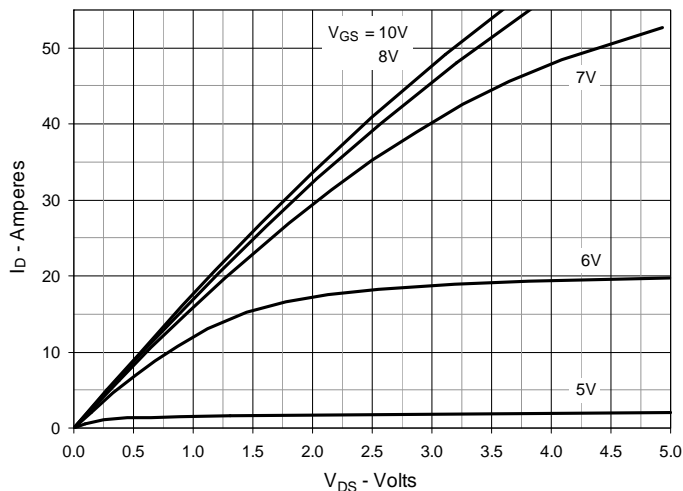


Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

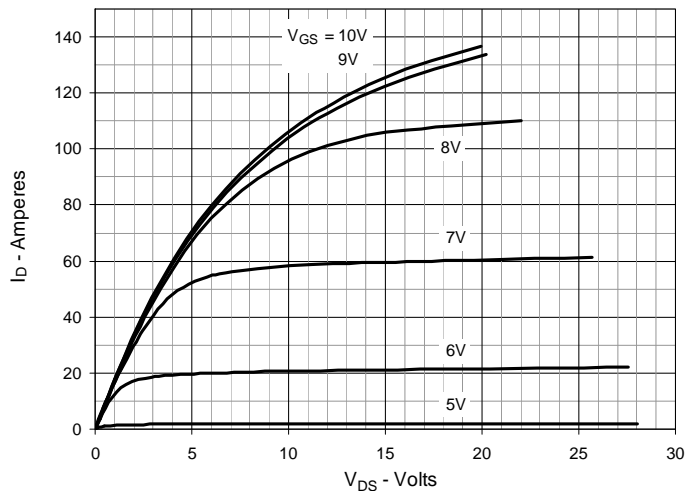


Fig. 3. Output Characteristics @ $T_J = 125^\circ\text{C}$

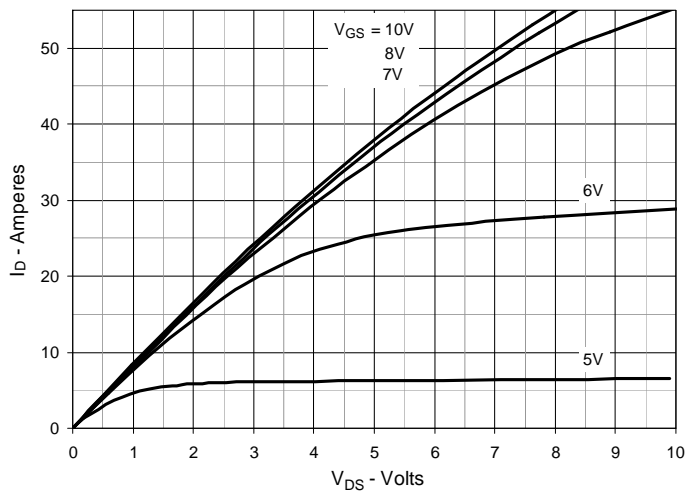


Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 26\text{A}$ Value vs. Junction Temperature

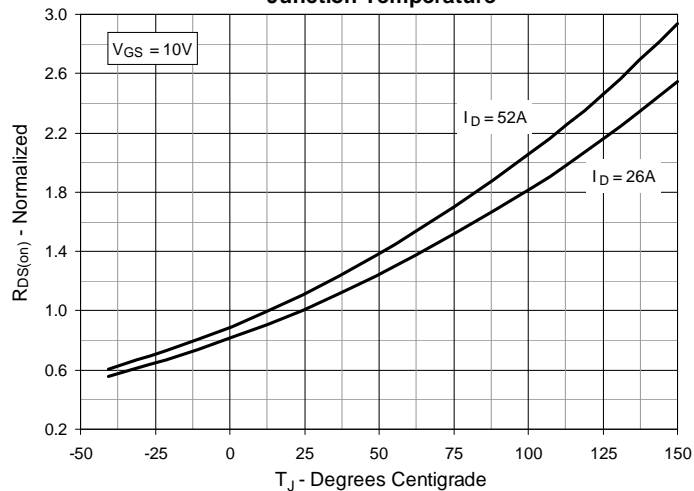


Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 26\text{A}$ Value vs. Drain Current

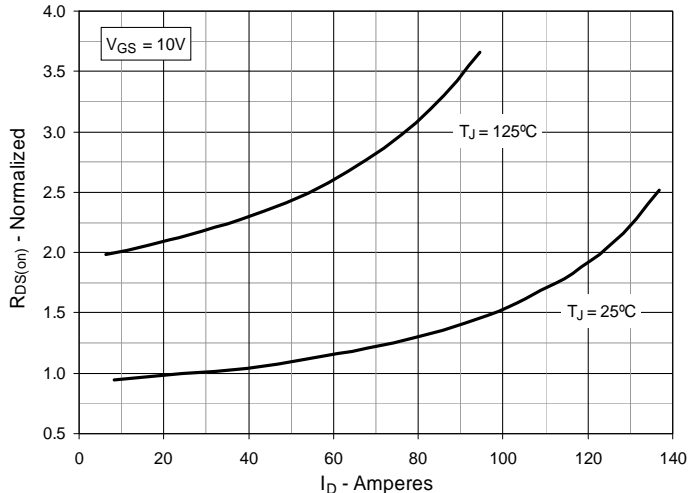


Fig. 6. Maximum Drain Current vs. Case Temperature

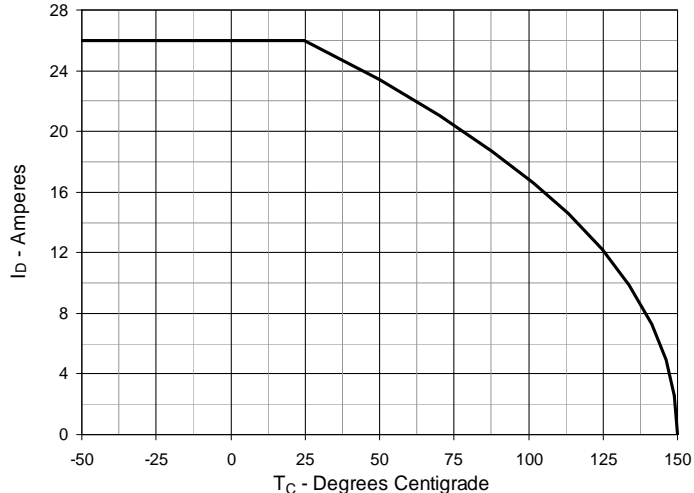


Fig. 7. Input Admittance

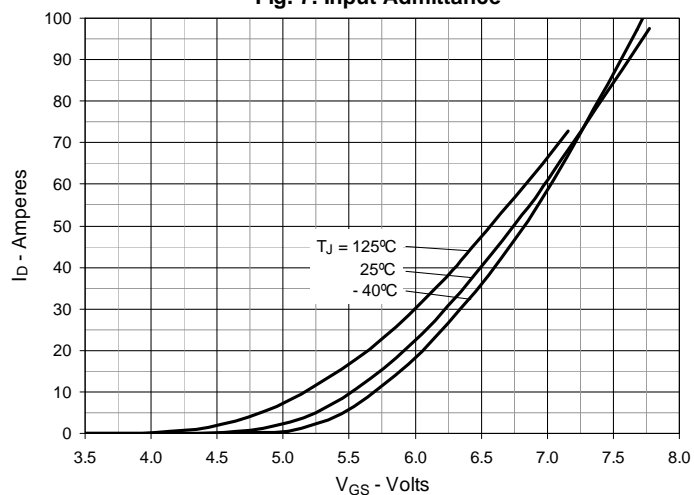


Fig. 8. Transconductance

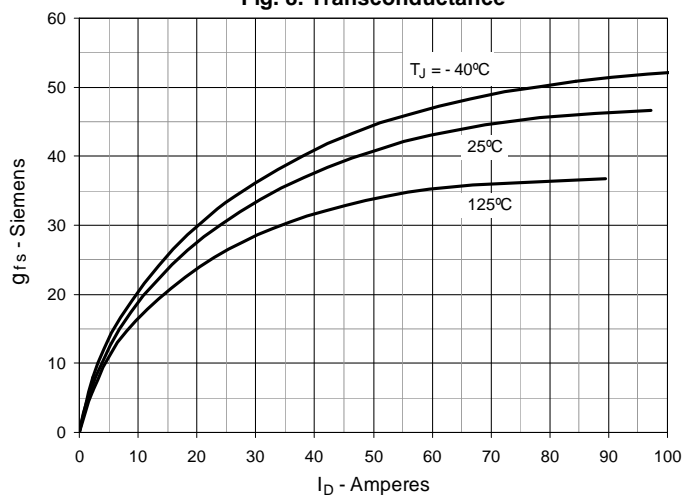


Fig. 9. Forward Voltage Drop of Intrinsic Diode

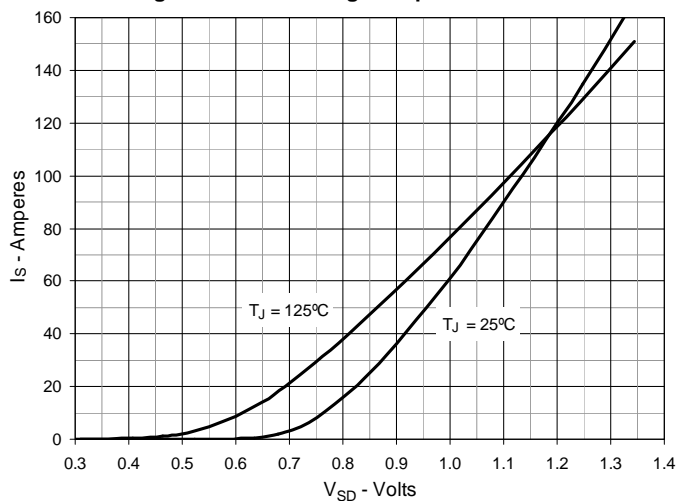


Fig. 10. Gate Charge

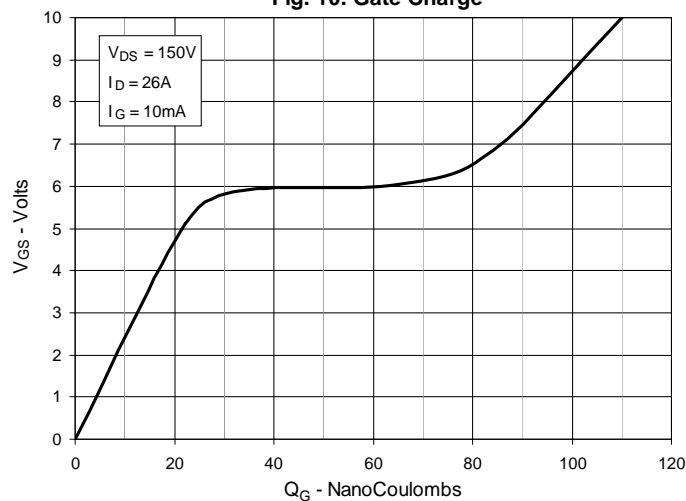


Fig. 11. Capacitance

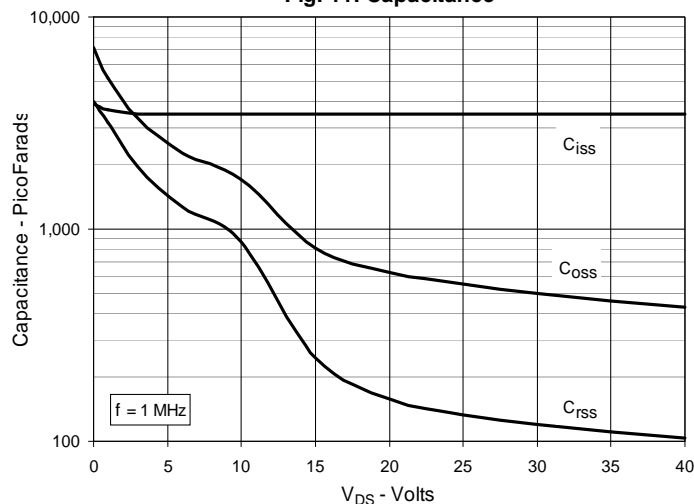


Fig. 12. Forward-Bias Safe Operating Area

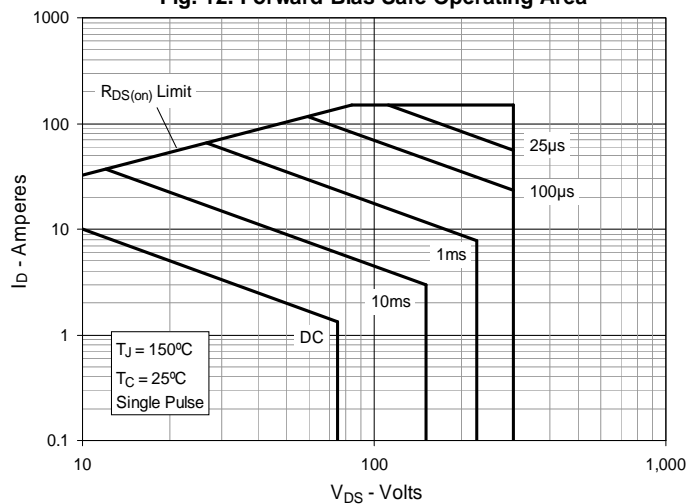


Fig. 13. Maximum Transient Thermal Impedance

