



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C
700V	1.3Ω @ V _{GS} = 10V	4.6A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

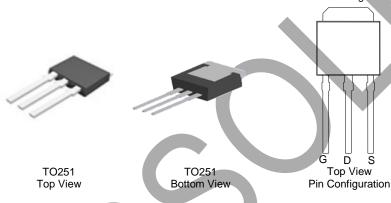
- Motor controls
- Backlighting
- DC-DC converters
- Power management functions

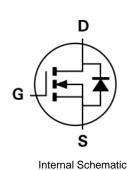
Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: TO251
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (Approximate)





Ordering Information (Note 4)

Part Number	Package		Packing		
Part Number	Package	Qty.	Carrier		
DMJ70H1D3SJ3	TO251	75 Pieces	Tube		

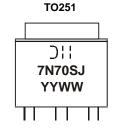
Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

Top View

- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



☐ : = Manufacturer's Marking 7N70SJ = Product Type Marking Code YYWW = Date Code Marking YY or YY= Last Digit of Year (ex: 16 = 2016) WW or $\overline{W}W = Week Code (01 to 53)$



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	700	V	
Gate-Source Voltage		Vgss	±30	V
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_C = +25$ °C $T_C = +100$ °C	I _D	4.6 2.9	А
Maximum Body Diode Forward Current (Note 6)		Is	3.0	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		Ірм	5.4	А
Avalanche Current (Note 7)	L = 60mH	I _{AS}	1.1	А
Avalanche Energy (Note 7)	L = 60mH	E _{AS}	40	mJ
Peak Diode Recovery dv/dt (Note 7)	·	dv/dt	5	V/ns

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_C = +25^{\circ}C$	Pp	41	W	
Total Fower Dissipation (Note 3)	T _C = +100°C	FU	16	VV	
Thermal Resistance, Junction to Ambient (Note 6)		Roja	79	°C/W	
Thermal Resistance, Junction to Case (Note 5)		Rojc	3.0	C/VV	
Operating and Storage Temperature Range		ТJ, Tsтg	-55 to +150	°C	

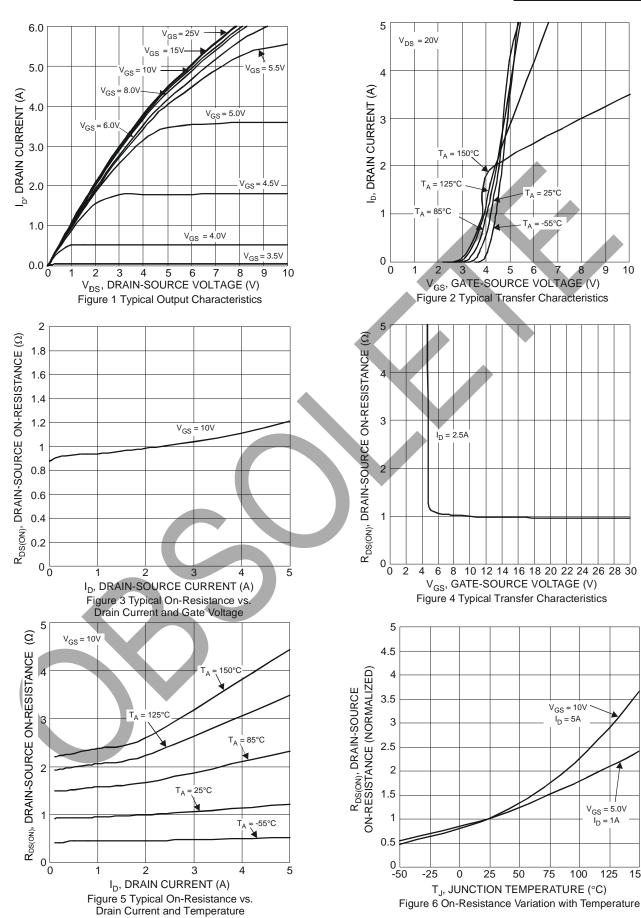
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	700		_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-		1	μA	V _{DS} = 700V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)				•			
Gate Threshold Voltage	VGS(TH)	2	2.9	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	1.0	1.3	Ω	$V_{GS} = 10V, I_D = 2.5A$	
Diode Forward Voltage	VsD		0.9	1.3	V	Vgs = 0V, Is = 5A	
DYNAMIC CHARACTERISTICS (Note 7)		•		•			
Input Capacitance	Ciss	_	351	_		V _{DS} = 50V, f = 1MHz,	
Output Capacitance	Coss	_	66	_	pF		
Reverse Transfer Capacitance	Crss	_	1.1	_		Vgs = 0V	
Gate Resistance	Rg	_	3.5	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q _G	_	13.9	_		V _{DD} = 560V, I _D = 5A, V _{GS} = 10V	
Gate-Source Charge	Qgs	_	1.9	_	nC		
Gate-Drain Charge	Q _{GD}	_	8.5	_			
Turn-On Delay Time	tD(ON)	_	8.5	_			
Turn-On Rise Time	t _R	_	11.6	_		$V_{DD} = 350V$, $V_{GS} = 10V$, $R_G = 4.7\Omega$, $I_D = 2.5A$	
Turn-Off Delay Time	tD(OFF)	_	24.5	_	ns		
Turn-Off Fall Time	tF	_	10	_			
Body Diode Reverse Recovery Time	trr	_	212	_	ns		
Body Diode Reverse Recovery Time (T _J = +150°C)	trr	_	251	_	ns	1	
Body Diode Reverse Recovery Charge	Qrr	_	1.8	_	μC	Is = 5A, dI/dt = 100A/µs	
Body Diode Reverse Recovery Charge (T _J = +150°C)	Qrr	_	2.3	_	μC		

Notes: 5. Device mounted on infinite heatsink.

- 6. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
- 7. Guaranteed by design. Not subject to production testing.
- 8. Short duration pulse test used to minimize self-heating effect.





 $I_D = 1A$

125



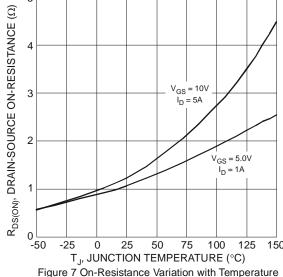
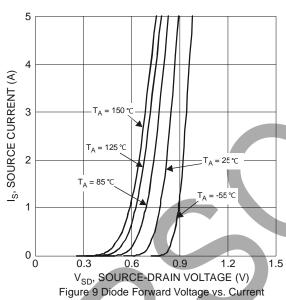
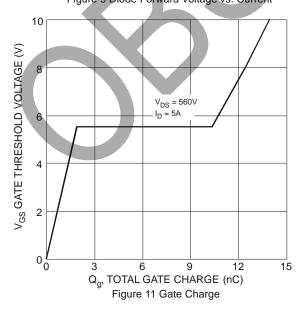


Figure 7 On-Resistance Variation with Temperature





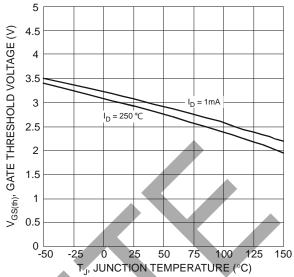
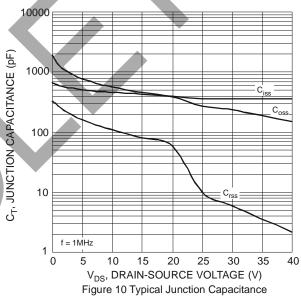
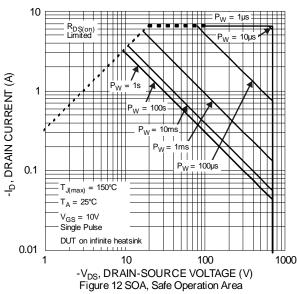
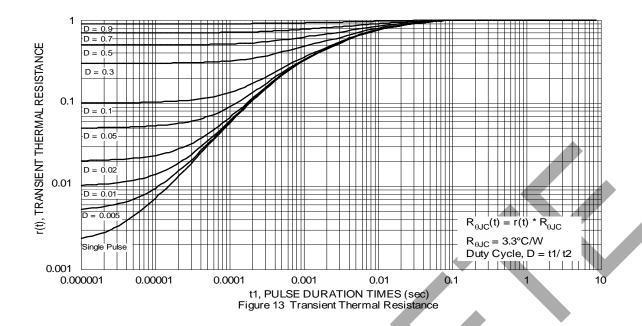


Figure 8 Gate Threshold Variation vs. Ambient Temperature







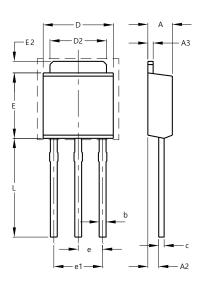


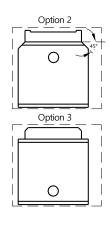


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

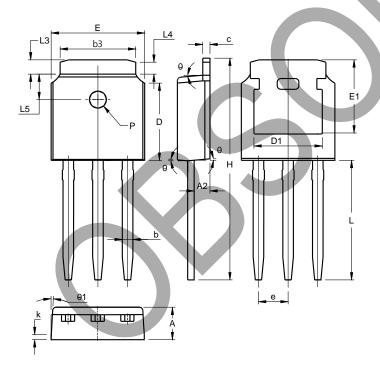
TO251





TO251					
Dim	Min	Max			
Α	2.200	2.400			
A2	0.890	1.150			
A3	0.450	0.550			
В	0.550	0.740			
С	0.450	0.570			
D	6.400	6.750			
D2	5.200	5.400			
E	5.950	6.250			
E2	2 0.900 1.250				
E	2.240	2.340			
e1	4.430	4.730			
L	8.900	9.500			
All Dimensions in mm					

TO251 (Type TH)



TO251 (Type TH)					
Dim	Min	Max	Тур		
Α	2.20	2.40	2.30		
A2	0.97	1.17	1.07		
b	0.68	0.90	0.78		
b3	5.20	5.50	5.33		
C	0.43	0.63	0.53		
D	5.98	6.22	6.10		
D1	5	5.30 REF			
е	2	.286 BS	С		
Е	6.40	6.80	6.60		
E1	4.63	5.03	4.83		
Н	16.22	16.82	16.52		
k	(0.40REF			
L	9.15	9.65	9.40		
L3	0.88	1.28	1.02		
L4	0.75 REF				
L5	1.65	1.95	1.80		
PØ	1.20				
θ	5°	9°	7°		
θ1	5°	9°	7°		
All Dimensions in mm					



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