

# **CLOCK OSCILLATOR**

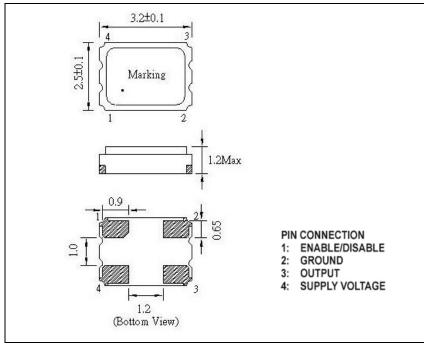
Page 1 of 3

### COM13025-38.400-EXT-T-TR

#### ELECTRICAL SPECIFICATION

PARAMETER	SYMBOL	CONDITIONS	VALUE	UNIT
Nominal Frequency	fo	Ta=25°C	38.400	MHz
Supply Voltage	Vcc	Vcc ±10%	3.3	VDC
Supply Current, max	Is	Ta=25°C	20	mA
Operating Temperature	Ta		-40 ~ +85	°C
Storage Temperature	T <sub>(stg)</sub>	Absolute max	-55 ~ +125	°C
Frequency Stability	Δf/fo	Inclusive of 25°C Tolerance and Changes due to Operating Temperature	±25	ppm
Outroot Walterna	VoL	Logic "0" Level	0.1 x Vcc	VDC
Output Voltage	Voн	Logic "1" Level	0.9 x Vcc	VDC
Output Load		CMOS Output	15	pF
Facility / Bis able Facetion	E/D	Pin 1: N.C. (Open) or High	Pin 3 – Oscillation (Enabled)	
Enable / Disable Function	E/D	Pin 1: Low	Pin 3 – High Impedance (Disabled)	
Symmetry (Duty Cycle)	DC	@50% Vdd	45 ~ 55	%
Rise Time and Fall Time	tr / tf	@10% to 90% Vdd	10	ns
Start-up Time, max	ts	V <sub>OUT</sub> ≥ 90% V <sub>P-P</sub>	10	ms
Standby Current	I <sub>(std)</sub>		10	μΑ

### MECHANICAL SPECIFICATION



NOTE: A capacitor of 0.01 µF between Vcc and Ground is recommended

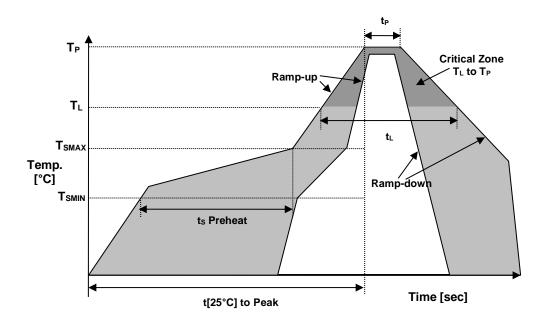


# **CLOCK OSCILLATOR**

Page 2 of 3

## COM13025-38.400-EXT-T-TR

#### REFLOW PROFILE



Reflow profile			
Temperature Min Preheat	T <sub>SMIN</sub>	150°C	
Temperature Max Preheat	T <sub>SMAX</sub>	200°C	
Time (T <sub>SMIN</sub> to T <sub>SMAX</sub> )	ts	60-180 sec.	
Temperature	T∟	217°C	
Peak Temperature	$T_P$	260°C	
Ramp-up rate	Rup	3°C/sec max.	
Ramp-down rate	R <sub>DOWN</sub>	6°C/sec max.	
Time within 5°C of Peak Temperature	t₽	10 sec.	
Time t[25°C] to Peak Temperature	t[25°C] to Peak	480 sec.	
Time	t∟	60-150 sec.	

### ENVIRONMENTAL

PARAMETER	VALUE
MOISTURE SENSITIVITY LEVEL	1
RoHS	Compliant
REACH-SVHC	Compliant
HALOGEN-FREE	Compliant
TERMINATION FINISH	Au





## **CLOCK OSCILLATOR**

Page 3 of 3

### COM13025-38.400-EXT-T-TR

#### MARKING

Rx38.40T •3AEyw

x – Internal Production ID code

y – Year code

w - Week code

YEAR CODE		
Year	Code	
2015	5	
2016	6	
2017	7	
2018	8	
2019	9	
2020	0	
2021	1	
2022	2	
2023	3	
2024	4	
2025	5	

ALPHA WEEK CODE TABLE					
Week	Code	Week	Code	Week	Code
1	а	19	S	37	K
2	b	20	t	38	L
3	С	21	u	39	М
4	d	22	٧	40	N
5	е	23	W	41	0
6	f	24	Х	42	Р
7	g	25	У	43	Q
8	h	26	Z	44	R
9	i	27	Α	45	S
10	j	28	В	46	Т
11	k	29	С	47	U
12	I	30	D	48	V
13	m	31	E	49	W
14	n	32	F	50	X
15	О	33	G	51	Υ
16	р	34	Н	52	Z
17	q	35	Ī		
18	r	36	J		

#### APPROVAL

RALTRON		
DRAWN BY:	XLiu, February 21, 2020	
APPROVED BY:	JIvens, February 21, 2020	
REVISION:	A Initial Release	

Raltron Electronics / RAMI Technology USA, LLC, including its affiliates, employees, agents and other persons acting on its behalf (collectively Raltron/RAMI Tech), disclaim any and all liability for any errors or inaccuracies contained in this data sheet. While Raltron/RAMI Tech has made every reasonable effort ensure the accuracy of all product information, specifications and data contained herein, Raltron/RAMI Tech does not guarantee that the information is accurate, reliable or current. The product information is provided only for reference purposes only and is subject to change, correction or revision, at any time without notice. Raltron/RAMI ech does not assume any liability arising out of an application or use of any product described herein and disclaims any warranties expressed or implied. The user of products in such applications shall assume all risks of such use and will agree to hold Raltron/RAMI Tech, harmless against all damages.

Copyright © 2016, Raltron Electronics / RAMI Technology USA, LLC. All rights reserved. No part of this document may be reproduced in any form without the prior written permission of Raltron Electronics / RAMI Technology USA, LLC.