



# 2A TRENCH SCHOTTKY BARRIER RECTIFIER

### Product Summary (@TA = +25°C)

VRRM (V)	lo (A)	V <sub>F(MAX)</sub> (V)	I <sub>R(MAX)</sub> (mA)
40	2	0.50	0.25

### **Applications**

For use in low-voltage, high-frequency inverters, freewheeling, DC-DC converters, and polarity applications.

- SMPS
- AC-DC
- DC-DC converters
- · Freewheeling diodes
- Reverse polarity protections
- Blocking diodes

### **Features and Benefits**

- Low Leakage Current
- · Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

### **Mechanical Data**

- Package: SMA
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead Free Plating (Matte Tin Finish). Solderable per MIL-STD-202, Method 208 @3
- Polarity Indicator: Cathode Band
- Weight: 0.064 grams (Approximate)

#### SMA





Top View Bottom View

# Ordering Information (Note 4)

Part Number	Compliance	Package	Packing		
Part Number	Compliance		Qty.	Carrier	
B240AX-13	Commercial	SMA	5,000	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### Marking Information (Note 5)



B240AX = Product Type Marking Code

OH = Manufacturer's Code Marking

YWW = Date Code Marking

Y = Last Digit of Year (ex: 2 for 2022)

WW = Week Code (01 to 52)

XX = Foundry and Assembly Site

Note: 5. Device has a cathode band (as shown) and may also have a cathode notch.

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# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	Vrrm		
Working Peak Reverse Voltage	VRWM	40	V
DC Blocking Voltage	$V_{RM}$		
Average Rectified Output Current	lo	2	Α
Non-Repetitive Peak Forward Surge Current 1ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	35	А

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Thermal Resistance Junction to Ambient (Note 6) Thermal Resistance Junction to Case (Note 6)	R <sub>θ</sub> JA R <sub>θ</sub> JC	80 30	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Note:

# Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Тур	Max	Unit	Test Condition
Forward Voltage Drop	VF	0.43 0.37	0.50 —	٧	I <sub>F</sub> = 2.0A, T <sub>J</sub> = +25°C I <sub>F</sub> = 2.0A, T <sub>J</sub> = +125C
Leakage Current (Note 7)	I <sub>R</sub>	25 —	250 20	μA mA	V <sub>R</sub> = 40V, T <sub>J</sub> = +25°C V <sub>R</sub> = 40V, T <sub>J</sub> = +100°C
Total Capacitance	Ст	220	_	pF	V <sub>R</sub> = 4V, f = 1MHz

Note:

<sup>6.</sup> Device mounted on FR-4 substrate, 0.4"\*0.5", 2oz, single-sided, PC boards with 0.2"\*0.25" copper pad. The heat generated must be less than the thermal conductivity from junction to case:  $dP_D/dT_J < 1/R_{\theta JC}$  or junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ .

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect.



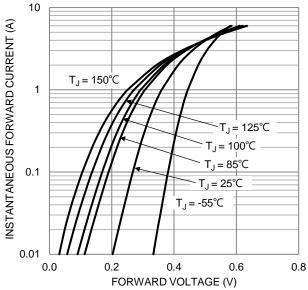


Figure 1. Typical Forward Characteristics

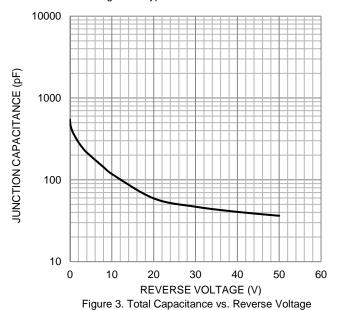


Figure 2. Typical Reverse Characteristics

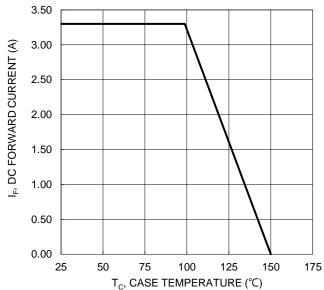


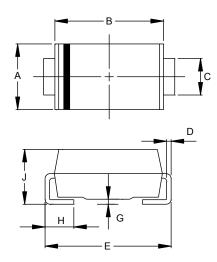
Figure 4. DC Forward Current Derating



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

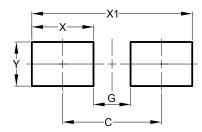
### SMA



SMA				
Dim	Min	Max		
Α	2.29	2.92		
В	4.00	4.60		
С	1.27	1.63		
D	0.15	0.31		
Е	4.80	5.59		
G	0.05	0.20		
Н	0.76	1.52		
J	1.96	2.40		
All Dimensions in mm				

# **Suggested Pad Layout**

### SMA



Dimensions	Value (in mm)
С	4.00
G	1.50
Х	2.50
X1	6.50
Υ	1.70



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