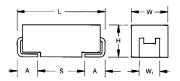
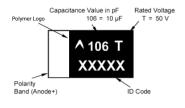
# **High Temperature Automotive Polymer Chip Capacitors**







#### **MARKING**



#### **FEATURES**

- · Conductive Polymer Electrode
- · Benign Failure Mode Under Recommended Use Conditions
- · Robust Design for Automotive Applications
- Meets Requirements of AEC-Q200
- · -55 to +150°C Operation Temperature
- · Humidity 85°C/85%RH, Vr, 1000 Hours
- Basic Reliability 1%/1000hrs@85°C Vr with 60% Confidence Level
- DCL 0.1 CV
- · 3x reflow 260°C Compatible
- · 100% Surge Current Tested





## **APPLICATIONS**

DC/DC converters, Telecommunication (coupling/decoupling), Industrial & special, Automotive (body electronics, cabin controls, infotainment, comfort, after market etc)

Not recommended for use of conductive polymer parts in high power applications. For more information please see KYOCERA AVX automotive application guide at kyocera-avx.com, or contact manufacturer.

KYOCERA AVX's qualification of TCO capacitors meets requirements of AEC-Q200. TCO series is manufactured in an IATF 16949 certified facility.

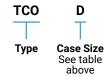
#### **CASE DIMENSIONS:**

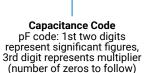
millimeters (inches)

	Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H+0.20 (0.008) -0.10 (0.004)	W1±0.20 (0.008)	A+0.30 (0.012) -0.20 (0.008)	S Min.
ĺ	D	2917	7343-31	7.30 (0.287)	4.30 (0.169)	2.90 (0.114)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)

W1 dimension applies to the termination width for A dimensional area only

### **HOW TO ORDER**





106



050 **Rated DC Voltage** 025 = 25 Vdc 050 = 50 Vdc



0150 ESR in m0

Ε Additional Character E = Black resin

#### **TECHNICAL SPECIFICATIONS**

Technical Data:	All technical data relate to an ambient temperature of +25°C
Capacitance Range:	10 μF to 33μF
Capacitance Tolerance:	±20%
Leakage Current DCL:	0.1CV
Temperature Range:	-55°C to +150°C
	Meets requirements of AEC-Q200

NOTE: Conductive Polymer Capacitors are designed to operate within the limits of the environmental conditions specified for each series. If operated continuously at their maximum temperature and / or humidity limit, or beyond these limits, capacitors may exhibit a parametric shift in capacitance and increases in ESR. These changes may occur earlier if the specified environmental conditions are exceeded. Similarly, their normal operational time period will be significantly extended if their general duty cycle includes operation below maximum temperature within humidity controlled environments. Careful attention should be paid to maximum temperature with associated high humidity environments as well as voltage derating, ripple current and current surges. Please reference the KYOCERA AVX Conductive Polymer Capacitor Guidelines for more information or contact factory for application assistance.



# **High Temperature Automotive Polymer Chip Capacitors**

## **CAPACITANCE AND RATED VOLTAGE RANGE** (LETTER DENOTES CASE SIZE)

Capac	itance	Rated Voltage DC (V <sub>R</sub> ) @ 105°C						
μF	Code	25V (E)	35V (V)	50V (T)				
10	106			D(150)				
15	156							
22	226							
33	336	D(100)						

Released ratings, (ESR ratings in mOhms in parentheses)

Note: Voltage ratings are minimum values. KYOCERA AVX reserves the right to supply higher volage ratings in the same case size, to the same reliability standards.

#### **RATINGS & PART NUMBER REFERENCE**

Part Number	Case	Capacitance	Rated	Maximum Operating Temp.		DF Max	ESR Max @ 100kHz	100kHz RMS Current (mA)		Humidity 85°C/85% RH,	MSL			
Part Number	Size	(μ <b>F</b> )	Voltage (V)	(°C)	(µA)	(%)	(mΩ)	45°C	85°C	105°C	125°C	150°C		IVIOL
	25 Volt													
					1000	3								
	50 Volt													
TCOD106M050#0150E	D	10	50	150	50	10	150	1225	857	551	306	184	1000	3

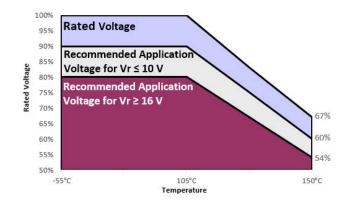
Moisture Sensitivity Level (MSL) is defined according to J-STD-020. All technical data relates to an ambient temperature of +25C.

Capacitance and DF are measured at 120Hz, 0.5RMS with DC bias of 2.2 volts. DCL is measured at rated voltage after 5 minutes. ESR allowed to move up to 1.25 times catalog limit post mounting. For typical weight and composition see page 259.

#### RECOMMENDED DERATING FACTOR

Voltage and temperature derating as percentage of Vr

Rated	Operating Temperature							
voltage	≤85°C	105°C	150°C					
≤10V	90%	90%	60%					
≥16V	80%	80%	54%					



TDS-PTNO-0002 | Rev 1



# **High Temperature Automotive Polymer Chip Capacitors**

## **QUALIFICATION TABLE**

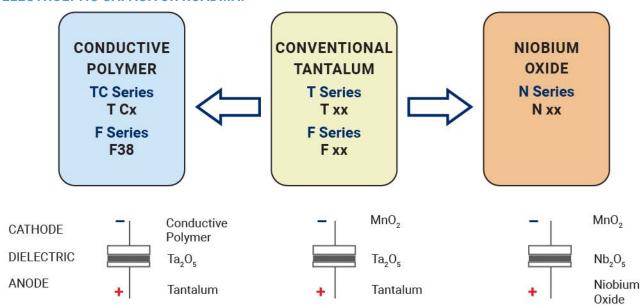
TEST	TCO series (Temperature range -55°C to 150°C)											
1591		Condition		Characteristics								
				Visual examination no visible damage								
		ge (Ur) at 105°C fo		DCL	2 x initial	2 x initial limit						
Endurance		(Ur) at 150°C for 1 impedance of ≤0.1		ΔC/C	within +1	within +10/-20% of initial value						
		e for 1-2 hours bef		DF	2 x initial	2 x initial limit						
	l com tomporatar	0.00.12.100.000.0	oro mododinig.	ESR	2 x initial	2 x initial limit						
				Visual examination	no visible	no visible damage						
	Store at 150°C, n	o voltage applied, f	or 1000 hours.	DCL	2x initial	2x initial limit						
Storage Life		temperature for 1-		ΔC/C	within +1	within +10/-20% of initial value						
	measuring.			DF	2 x initial	2 x initial limit						
				ESR	2 x initial	2 x initial limit						
				Visual examination	no visibl	no visible damage						
		ge (Ur) at 85°C, 85°		DCL	2 x initia	2 x initial limit						
<b>Biased Humidity</b>	,	O hours. Stabilize a humidity for 1-2 ho		ΔC/C	within +	within +35/-5% of initial value						
	measuring.	marmanty for 1-2 fic	ours before	DF	1.5 x init	1.5 x initial limit						
				ESR	2 x initia	l limit						
	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+105°C	+150°C	+20°C		
	1	+20	15	<u> </u>								
Temperature	3	-55 +20	15 15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	IL*		
Stability	4	+105	15	ΔC/C	n/a	±20%	±5%	±20%	±30%	±5%		
	5	+150	15	DF	IL*	*	IL*	15,11+	1.5 x IL*	IL*		
	6	+20	15	1			IL^	1.5 x IL*	I.5XIL^	IL^		
				Visual examination		no visible damage						
				DCL	initial lim	initial limit						
Surge Voltage	1 1 2	ated voltage (Ur) a		ΔC/C		within +10/-20% of initial value for Vr ≤ 10V within +20/-30% of initial value for Vr ≥ 16V						
	1000 dyoled, chai	ige / dioonalge les	iotarioe com.	DF		initial limit for Vr ≤ 10V 1.25x initial limit for Vr ≥ 16V						
				ESR	1.25 x ini	1.25 x initial limit						
				Visual examination	no visibl	no visible damage						
Machaniaal				DCL	initial lin	nit						
Mechanical Shock	MIL-STD-202, Me	thod 213, Conditio	n F	ΔC/C	within ±	within ±10% of initial value						
SHOCK				DF	initial lin	initial limit						
				ESR	1.25 x in	1.25 x initial limit						
				Visual examination	no visibl	no visible damage						
				DCL	initial lin	nit						
Vibration	MIL-STD-202, Me	thod 204, Conditio	n D	ΔC/C	within ±	within ±10% of initial value						
				DF	initial lin	initial limit						
				ESR	1.25 x in	1.25 x initial limit						

For use outside of recommended conditions and special request, please contact KYOCERA AVX. Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.





## SOLID ELECTROLYTIC CAPACITOR ROADMAP



## **FIVE CAPACITOR CONSTRUCTION STYLES**



## **SERIES LINE UP: Conductive Polymer**

