

Product Detail

Displaying Result for Part Number: 5SGTMC7K3F40C2

Resources

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|---|----------------|
| Device Name | Stratix® IV |
| Part Number | 5SGTMC7K3F40C2 |
| Product Line | 5SGTMC7 |
| Logic Elements | 0 |
| Registers | 938880 |
| MLAB memory (Mb) | 0 |
| Hardened single-precision Floating-point multipliers/adders | 256 |
| 18x18 multipliers | 256 |
| 36x36 multipliers | 0 |
| Peak GMACS | 0 |
| Peak GFLOPS | 0 |

Clocks and Maximum I/O Pins

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|-------------------------|-----|
| Global Clocks | 16 |
| Regional Clocks | 92 |
| Maximum LVDS channels | 150 |
| Maximum user I/O pins | 776 |
| Transceiver Count | 0 |
| PCI Express (PCIe) | 0 |
| Processor Cores | 0 |
| PLLs | 28 |
| GPIOs | 600 |
| Hard memory controllers | 0 |