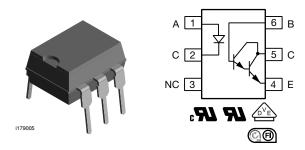


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Vishay Semiconductors

# Optocoupler, Photodarlington Output, High Gain, With Base Connection



### **FEATURES**

- Very high current transfer ratio, 500 % min.
- High isolation resistance,  $10^{11} \Omega$  typical
- Standard plastic DIP package
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





### **LINKS TO ADDITIONAL RESOURCES**







### **AGENCY APPROVALS**

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- FIMKO

### **DESCRIPTION**

The 4N32 and 4N33 are optically coupled isolators with a gallium arsenide infrared LED and a solicon photodarlington sensor

Switching can be achieved while maintaining a high degree of isolation between driving and load circuits.

These optocouplers can be used to replace reed and mercury relays with advantages of long life, high speed switching and elimination of magnetic fields.

#### ORDERING INFORMATION DIP 3 4 Χ 0 TAPE AND PART NUMBER PACKAGE OPTION REEL **AGENCY CERTIFIED / PACKAGE CTR (%)** UL, cUL, FIMKO ≥ 500 ≥ 500 DIP-6 4N33 4N32

### Note

Additional options may be possible, please contact sales office



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PARAMETER PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
	TEST CONDITION	STIVIDUL	VALUE	UNIT	
input					
Reverse voltage		$V_R$	3	V	
Forward current		l <sub>F</sub>	60	mA	
Power dissipation		$P_{diss}$	100	mW	
Derate linearly	From 55 °C		1.33	mW/°C	
output					
Collector emitter breakdown voltage		BV <sub>CEO</sub>	30	V	
Emitter base breakdown voltage		BV <sub>EBO</sub>	8	V	
Collector base breakdown voltage		BV <sub>CBO</sub>	50	V	
Emitter collector breakdown voltage		BV <sub>ECO</sub>	5	V	
Collector (load) current		Ic	100	mA	
Power dissipation		P <sub>diss</sub>	150	mW	
Derate linearly			2	mW/°C	
coupler					
Total dissipation		P <sub>tot</sub>	250	mW	
Derate linearly			3.3	mW/°C	
Isolation test voltage (between emitter	1 s	V <sub>ISO</sub>	5300	V <sub>RMS</sub>	
Leakage path			7	mm min.	
Air path			7	mm min.	
le eletion modistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω	
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω	
Storage temperature		T <sub>stg</sub>	-55 to +150	°C	
Operating temperature		T <sub>amb</sub>	-55 to +100	°C	
Lead soldering time (1)	At 260 °C		10	S	

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
input							
Forward voltage	$I_F = 50 \text{ mA}$	V <sub>F</sub>	-	1.25	1.5	V	
Reverse current	$V_R = 3 V$	I <sub>R</sub>	ı	0.1	100	μΑ	
Capacitance	$V_R = 0 V$	Co	-	25		pF	
output							
Collector emitter breakdown voltage (1)	$I_C = 100 \ \mu A, \ I_F = 0$	BV <sub>CEO</sub>	30	-	-	V	
Collector base breakdown voltage (1)	$I_C = 100 \ \mu A, \ I_F = 0$	BV <sub>CBO</sub>	50	-	-	V	
Emitter base breakdown voltage (1)	$I_C = 100 \ \mu A, \ I_F = 0$	BV <sub>EBO</sub>	8	-	-	V	
Emitter collector breakdown voltage (1)	$I_C = 100 \ \mu A, \ I_F = 0$	BV <sub>ECO</sub>	5	10	-	V	
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0$	I <sub>CEO</sub>	-	1	100	nA	
	$I_C = 0.5 \text{ mA}, V_{CE} = 5 \text{ V}$	h <sub>FE</sub>	13	-	-		
coupler							
Collector emitter saturation voltage		V <sub>CEsat</sub>	ı	1	-	V	
Coupling capacitance			-	1.5	-	pF	

### **Notes**

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

(1) Indicates JEDEC® registered values



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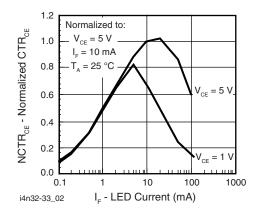
CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$V_{CE} = 10 \text{ V}, I_F = 10 \text{ mA}$	CTR	500		ı	%

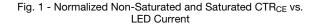
SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$V_{CC} = 10 \text{ V}, I_{C} = 50 \text{ mA}$	t <sub>on</sub>	-	-	5	μs
Turn-off time	$I_F$ = 200 mA, $R_L$ = 180 $\Omega$	t <sub>off</sub>	-	-	100	μs

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification	According to IEC 68 part 1		=	55 / 100 / 21	-		
Comparative tracking index		CTI	175	-	399		
V <sub>IOTM</sub>			8000	-	-	V	
V <sub>IORM</sub>			890	-	-	V	
P <sub>SO</sub>			-	-	700	mW	
I <sub>SI</sub>			-	-	400	mA	
T <sub>SI</sub>			-	-	175	°C	
Creepage distance	Standard DIP-6		7	-	-	mm	
Clearance distance	Standard DIP-6		7	-	-	mm	
Insulation thickness, reinforced rated	Per IEC 60950 2.10.5.1		0.4	-	-	mm	

#### Note

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)





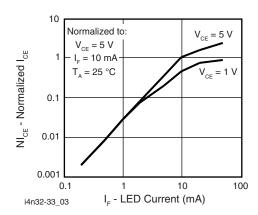


Fig. 2 - Normalized Non-Saturated and Saturated Collector Emitter Current vs. LED Current

As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.



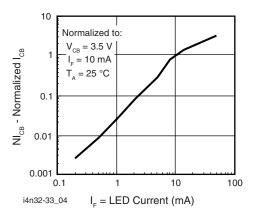


Fig. 3 - Normalized Collector Base Photocurrent vs. LED Current

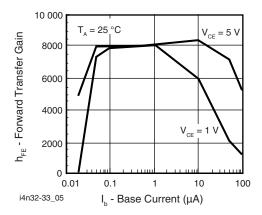


Fig. 4 - Non-Saturated and Saturated hFE vs. **Base Current** 

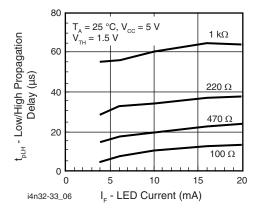


Fig. 5 - Low to High Propagation Delay vs. Collector Load Resistance and LED Current

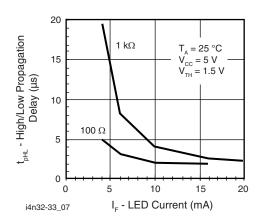


Fig. 6 - High to Low Propagation Delay vs. Collector Load Resistance and LED Current

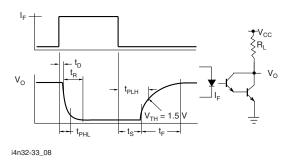


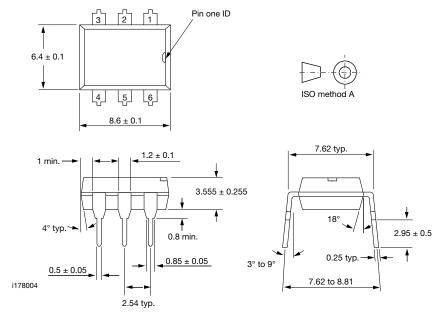
Fig. 7 - Switching Waveform and Switching Schematic

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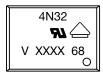
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### **PACKAGE DIMENSIONS** in millimeters

### **DIP-6 Package Dimensions**



### **PACKAGE MARKING**



#### Notes

- XXXX = LMC (lot marking code)
- Example marking for 4N32
- Only options 1, and 7 reflected in the package marking
- The VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking



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Vishay

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