DIGITAL**VIEW**®   PRODUCT REVISION RECORD

Part Number:	**4172701XX-3**	Model:	**HLR-1920**

DATE	VERSION	SUMMARY OF CHANGES	NOTE
		Conformal Coating Required	

8 October 2015	20-3	Production release harsh environment version of ALR-1920
		With reference to standard ALR-1920, 417270023-3
		- Add conformal coating
		- Change ECAP to Tantalum Cap
		- Change ICs whatever the industrial grade is available
		- Change crystal to high temperature grade
		- Change Fuse to non-resettable type
		
		Bare PCB: 017270002-3; P/N 417270120-3
		BIOS: U10, V1.25.00 (Flash PCT25VF040)(same as 417270023-3)
		         U13, V0.40 (ARGB DDC, 24C02) (same as 417270023-3)
		         U12, V0.30 (HDMI DDC, 24C02) (same as 417270023-3)
		         U20, V0.30 (Display Port DDC, 24C02) (same as 417270023-3)
			ECN# 870689530

2 June 2016	20-3 to 21-3	Release new firmware for HLR-1920
		1. Fixed the accuracy of backlight level in PWM and D/A mode. (It was found that the backlight level is not linear to the percentage of the level and different ranges for different PWM frequency or in D/A mode, also the system backlight level control function was replaced by own method)
		2. Add a RS-232 Backlight Status Query command - 0xe1 "S" or 0xe1 "s".
		
		Bare PCB: 017270002-3; P/N 417270121-3
		BIOS: U10, V1.28.00 (Flash PCT25VF040)(changed)
		         U13, V0.40 (ARGB DDC, 24C02)(no change)
		         U12, V0.30 (HDMI DDC, 24C02)(no change)
		         U20, V0.30 (Display Port DDC, 24C02)(no change)
			ECN# 870689580

16 February 2017	21-3 (Version remain unchanged)	**Update BOM Only**
		- Change the VGA connector to black color (running change)
		- Change the jumper instruction tape to label.
		
		Bare PCB: 017270002-3; P/N 417270121-3
		BIOS: U10, V1.28.00 (Flash PCT25VF040)(no change)
		         U13, V0.40 (ARGB DDC, 24C02)(no change)
		         U12, V0.30 (HDMI DDC, 24C02)(no change)
		         U20, V0.30 (Display Port DDC, 24C02)(no change)
			ECN# 870689631

21 April 2017	21-3 to 22-3	Release new firmware V1.30.00 on U10
		(1) Support "Resolution default by EDID" for different resolution panel. The controller will set the preferred timing based off the dip switch setting selection, but also be able to go higher to 1920x1200. For example, if the panel is a 1024x768 and the dip switch setting are set for 1024x768. The preferred EDID resolution should be 1024x768. It should also have the capability to set the max resolution to 1920x1200. Remark: This function supports on HDMI and Display port only
		(2) Bug fixed on overflow the RS-232 command
		
		Bare PCB: 017270002-3; P/N 417270122-3
		BIOS: U10, V1.30.00 (Flash PCT25VF040)(changed)
		         U13, V0.40 (ARGB DDC, 24C02)(no change)
		         U12, V0.40 (HDMI DDC, 24C02) (changed, 1920x1200, CS= 45,48)
		         U20, V0.40 (Display Port DDC, 24C02) (changed, 1920x1200, CS= 45,48)
			ECN# 870689642

Prepared by: DV-HK	Date: 21 April 2017