

Electrical interface CPX-CTEL-2-M12-5POL-LK

Part number: 2900543

FESTO



Data sheet

| Feature | Value |
|--|--|
| Protocol | IO-Link® |
| Dimensions W x L x H | (incl. interlinking block) 50 mm x 107 mm x 55 mm |
| Product weight | 110 g |
| Ambient temperature | -5 °C ... 50 °C |
| Storage temperature | -20 °C ... 70 °C |
| Degree of protection | IP65 IP67 |
| Corrosion resistance class (CRC) | 1 - Low corrosion stress (when installed) |
| Max. cable length | 20 m |
| LABS (PWIS) conformity | VDMA24364-B2-L |
| Note on materials | RoHS-compliant |
| Housing material | PA-reinforced PC |
| LED indicators | PS: electronic supply, sensor supply PL: load supply X1: system status, IO-Link® port 1 X1: system status, IO-Link® port 2 Group diagnostics |
| Diagnostics | Communication error Module short circuit Module-orientated diagnostics Undervoltage |
| Control elements | DIL switch |
| Max. address capacity inputs | 32 byte |
| Max. address capacity outputs | 32 byte |
| Parameterization | Diagnostic behavior Fail-safe modes per channel Forcing per channel Idle mode per channel Module parameters |
| Electrical connection | 2x socket 5-pin A-coded M12 |
| DC operating voltage range | 18 V ... 30 V |
| Nominal operating voltage DC | 24 V |
| Intrinsic current consumption at nominal operating voltage | Typ. 65 mA |
| Power failure buffering | 10 ms |
| Electrical isolation between channels | no |

| Feature | Value |
|---|---|
| Electrical isolation between channel and internal bus | Yes, when using intermediate feed |
| IO-Link®, number of ports | 2 |
| IO-Link®, port class | A B |
| IO-Link®, protocol version | Master V 1.0 |
| IO-Link®, communication mode | COM1 (4.8 kBd), COM2 (38.4 kBd), COM3 (230.4 kBd) |
| IO-Link®, process data width OUT | 4/4 to 16/6 bytes parameterizable |
| IO-Link®, process data width IN | 4/4 to 16/6 bytes parameterizable |
| IO-Link®, minimum cycle time | Min. 1 ms per 1 byte of process data |
| IO-Link®, output current | 1.6 A PL/port 1.6 A PS/port |